

# NCE07TD60B

**PbFreeProduct** 

### 600V, 7A, Trench FS II Fast IGBT

#### **General Description:**

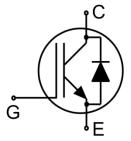
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 600V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

#### **Features**

- Trench FSII Technology Offering
- Very low V<sub>CE(sat)</sub>
- High speed switching
- Positive temperature coefficient in V<sub>CE(sat)</sub>
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

### **Application**

- Air Condition
- Inverters
- Motor drives



Schematic diagram

#### **Package Marking and Ordering Information**

Device	Device Package	Device Marking
NCE07TD60B	TO-220	NCE07TD60B



#### **Absolute Maximum Ratings (Tc=25°C unless otherwise noted)**

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	600	V
$V_{GES}$	Gate- Emitter Voltage	±30	V
	Collector Current	14	A
lc	Collector Current @T <sub>C</sub> = 100 °C	7	A
I <sub>Cplus</sub>	Pulsed Collector Current, tp limited by Tjmax	21	A
-	turn off safe operating area, V <sub>CE</sub> =600V, T <sub>J</sub> =150°C	21	A
I <sub>F</sub>	Diode Continuous Forward Current @T <sub>C</sub> = 100 °C	7	A
I <sub>FM</sub>	Diode Maximum Forward Current	21	A
_	Power Dissipation @ T <sub>C</sub> = 25°C	73	W
P <sub>D</sub>	Power Dissipation @Tc = 100 °C	36.5	W
T <sub>J</sub> ,T <sub>stg</sub>	Operating Junction and Storage Temperature Range	-55 to +175	°C
TL	Maximum Temperature for Soldering	260	°C
t <sub>sc</sub>	Short circuit withstand time $V_{GE}$ =15V, $V_{CC}$ $\leq$ 400V, Allowed number of short circuits<1000Time between short circuits: $\geq$ 1.0s, $T_j$ $\leq$ 150°C	5	us



# NCE07TD60B

#### **Thermal Characteristic**

Symbol	Parameter	Value	Units
R <sub>θ</sub> JC	Thermal Resistance, Junction to case for IGBT	2.05	°C/W
Rejc	Thermal Resistance, Junction to case for Diode	2.50	°C/W
RθJA	Thermal Resistance, Junction to Ambient	62	°C/W

# Electrical Characteristics (Tc=25°C unless otherwise noted)

0	Barranatan	Parameter Test Conditions		Value			
Symbol	Parameter			Min.	Тур.	Max.	Units
Static Chara	cteristics						
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	V <sub>GE</sub> =0V	,I <sub>CE</sub> =1mA	600			V
Ices	Collector-Emitter Leakage Current	V <sub>GE</sub> =0V,	Vce=600V			4	uA
I <sub>GES(F)</sub>	Gate to Emitter Forward Leakage	V <sub>GE</sub> =+30	V,Vce=0V			100	nA
I <sub>GES(R)</sub>	Gate to Source Reverse Leakage	V <sub>GE</sub> =-30	V,Vce =0V			100	nA
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	Ic=5A	Tj=25°C		1.7	1.9	V
V CE(sat)	Collector-Emitter Saturation voltage	$V_{GE}$ =15 $V$	Tj=100°C		1.9		V
$V_{\text{GE(th)}}$	Gate Threshold Voltage	Ic=1mA,VcE=VGE		4.0	5.0	6.0	V
Dynamic Ch	aracteristics						
Cies	Input Capacitance	Vce=25V, Vge=0V, f=1MHz			675		pF
Coes	Output Capacitance				22		
Cres	Reverse Transfer Capacitance				13		
Qg	Total Gate Charge				28		
Qge	Gate to Emitter Charge	Vcc=480V, lc=7A, V <sub>GE</sub> =15V			8		nC
Q <sub>gc</sub>	Gate to Collector Charge				13		
I <sub>C(SC)</sub>	Short circuit collector current Max.1000 short circuits Time between short circuits: ≥1.0s	V <sub>GE</sub> =15V,V <sub>CC</sub> ≤400V, t <sub>SC</sub> ≤5us,Tj≤150°C			34		А
Switching Cl	haracteristics						
$t_{d(ON)}$	Turn-on Delay Time				20		
t <sub>r</sub>	Rise Time	$V_{CC}$ =400 $V$ , $I_{C}$ =7 $A$ , $V_{GE}$ =0/15 $V$ , $R_{g}$ =5 $\Omega$ Inductive Load			15		ns
t <sub>d(OFF)</sub>	Turn-Off Delay Time				73		
t <sub>f</sub>	Fall Time				18		
Eon	Turn-On Switching Loss				0.21		
E <sub>off</sub>	Turn-Off Switching Loss				0.10		mJ
Ets	Total Switching Loss				0.31		

# Electrical Characteristics of the Diode(Tc= 25°C unless otherwise specified):

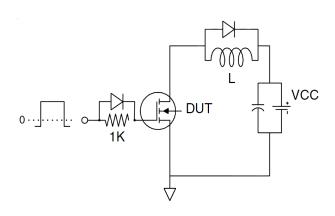
Cumbal	Parameter	Test Conditions	Rating			Units
Symbol		rest Conditions	Min.	Тур.	Max.	Units
$V_{FM}$	Diode Forward Voltage	I <sub>F</sub> =7A		1.5	1.7	V
Trr	Reverse Recovery Time			230		ns
I <sub>RRM</sub>	Diode Peak Reverse Recovery Current	I <sub>F</sub> =7A, di/dt=200A/us		3.5		А
Qrr	Reverse Recovery Charge			0.44		uC
Pulse width $t_{tp} \le 380 \mu s, \delta \le 2\%$						



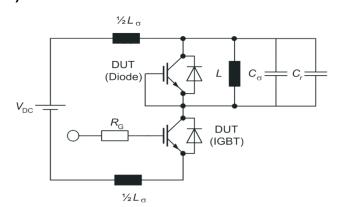


#### **Test Circuit**

#### 1) Gate Charge Test Circuit

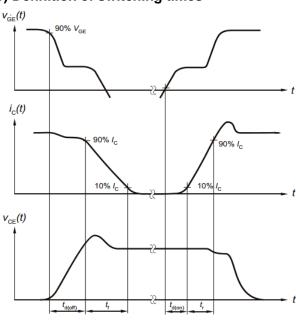


#### 2) Switch Time Test Circuit

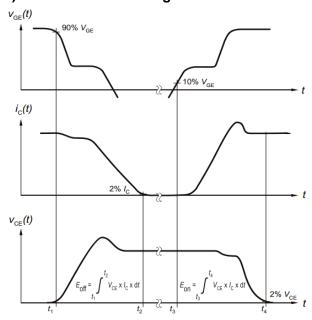


#### **Switching characteristics**

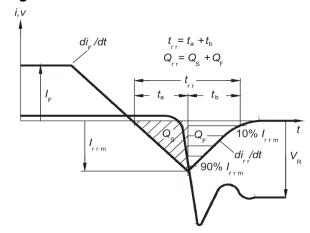
#### 1) Definition of switching times



#### 2) Definition of switching losses



## 3) Definition of diode switching characteristics





## **Typical Electrical and Thermal Characteristics**

**Figure 1 Output Characteristics** 

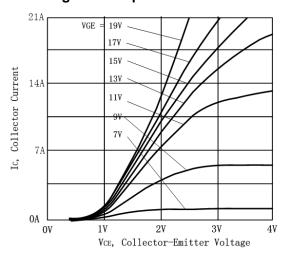
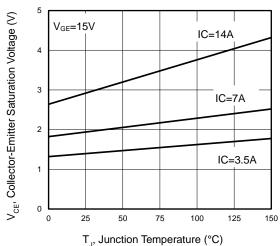
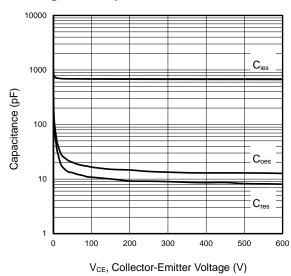


Figure 3 V<sub>CEsat</sub> vs. Case Temperature



**Figure 5 Capacitance Characteristics** 



**Figure 2 Transfer Characteristics** 

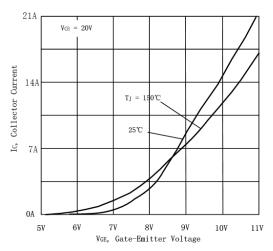


Figure 4 Saturation Voltage vs.  $V_{\text{GE}}$ 

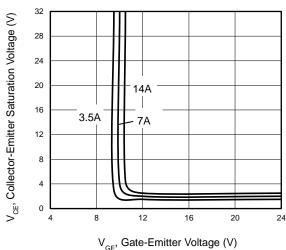
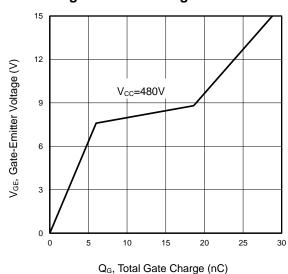


Figure 6 Gate charge waveform





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#### **Typical Electrical and Thermal Characteristics**

#### **Figure 7 Forward Characteristics**

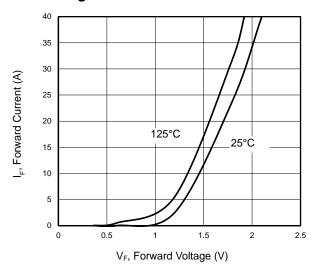


Figure 9 Typical Switching Times as a **Function of Gate Resistor** 

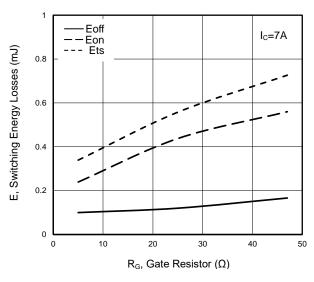


Figure 11 Gate-emitter Threshold Voltage as a **Function of Junction Temperature** 

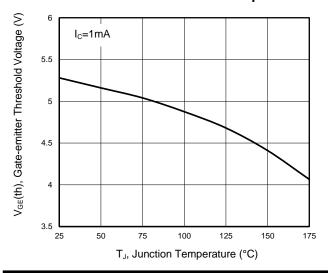


Figure 8 V<sub>F</sub> vs. Temperature

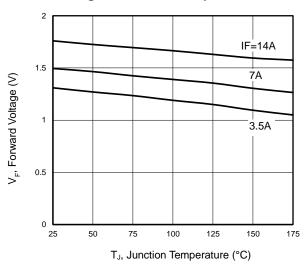
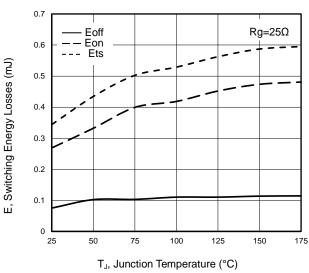
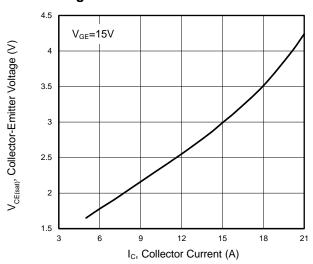


Figure 10 Typical Switching Times as a **Function of Junction Temperature** 



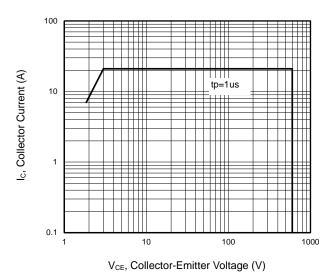
**Figure 12 Typical Collector-emitter Saturation** Voltage as a function of Collector Current





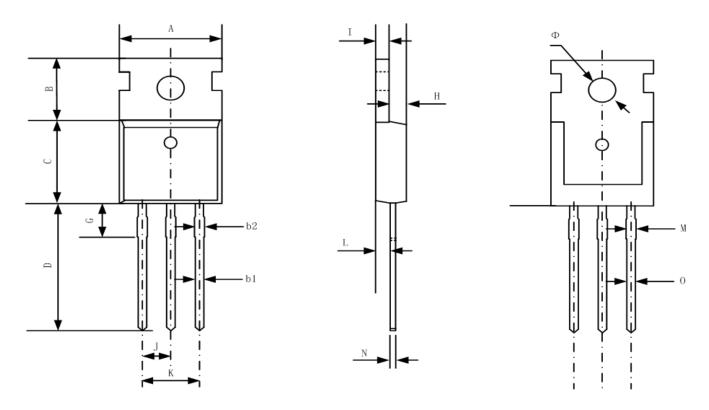
## **Typical Electrical and Thermal Characteristics**

## Figure 13 Forward Bias Safe Operating Area





# **TO-220-3L-C** Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	9.70	10.20	0.38	0.40	
В	6.30	6.70	0.25	0.26	
С	9.00	9.47	0.35	0.37	
D	12.78	13.38	0.50	0.53	
G	2.65 REF		0.104	REF	
Н	3.00	3.40	0.12	0.13	
I	1.25	1.40	0.05	0.06	
J	2.40	2.70	0.09	0.11	
K	5.00	5.15	0.20	0.20	
L	2.20	2.60	0.09	0.10	
М	1.25	1.45	0.05	0.06	
N	0.45	0.60	0.02	0.02	
0	0.70	0.90	0.03	0.04	
Ф	3.6 REF		0.142 REF		





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