

VDS	RDS(on)	ID@25℃
1200V	25mΩ	90A

Applications:

- Solar Inverters
- Switch Mode Power Supplies
- High Voltage DC/DC Converters
- EV Charging
- Motor Drives

Features:

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Easy to Parallel and Simple to Drive
- Avalanche Ruggedness

Benefits:

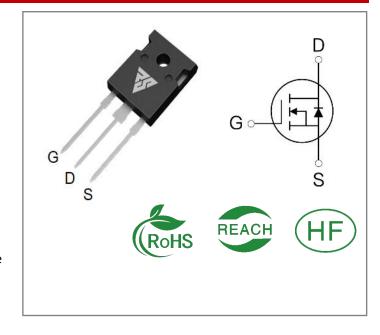
- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

Ordering Information

Part Number	Package	Marking	Packing	Qty.
RSM120025W	TO-247-3	RSM120025W	Tube	30 PCS

Maximum Ratings (TJ= 25℃ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
VDSmax	Drain - Source Voltage	1200	V	VGS=0V,ID =100μA	
VGSmax	Gate - Source Voltage	-10/+2 5	V	Absolute maximum values	
VGSop	Gate - Source Voltage	-5/+20	V	Recommended operational values	
ID	Continuous Drain Current	90 60	А	VGS=20V, TC =25°C VGS=20V, TC =100°C	
ID(pulse)	Pulsed Drain Current	200	А	Pulse width tp limited by TJmax	
PD	Power Dissipation	370	W	TC =25°C, TJ =150°C	
TL	Solder Temperature	260	$^{\circ}$ C		
TJ, Tstg	Operating Junction and StorageTemperature	-40 to + 150	$^{\circ}$		





Electrical Characteristics (TJ= 25 °C unless otherwise specified)

Symbol	Parameter	Min.	Тур.	Max	Unit	Test Conditions	Note
V(BR)D SS	Drain-Source Breakdown Voltage	120 0			٧	VGS=0V,ID =100μA	
\(CC(11\)	Gate Threshold	2.0	2.4	4.0	V	VGS= VDS, IDS=15mA, TC =25°C	
VGS(th)	Voltage		1.8		٧	VGS= VDS, IDS=15mA, TC =150°C	
IDSS	Zero Gate Voltage Drain Current		1	100	μΑ	VDS= 1200V, VGS=0V	
IGSS	Gate-Source Leakage Current			250	nA	VGS=20V, VDS= 0V	
	Drain-Source on-state		25	34	mΩ	VGS=20V, ID =50A, TC =25℃	
RDS(on)	Resistance		43			VGS=20V, ID =50A, TC =150°C	
Ciss	Input Capacitance		360 0			VGS=0V, VDS=1000 V,	
Coss	Output Capacitance		24		pF	f=1MHz, VAC=25 mV	
Crss	Reverse Transfer Capacitance		16			. 1	
EON	Turn-On Switching Energy		180 0		1	VDS =800V, VGS =-5/20V,	
EOFF	Turn-Off Energy		150 0		μͿ	ID = 30A, RG(ext) = 2.5Ω , L= 200μ H	
td(on)	Turn-On Delay Time		16				
tr	Rise Time		16.2			VDS =800V, VGS =-5/20 V	
td(off)	Turn-Off Delay Time		33		ns	ID = 30A, RG(ext) =2.5 Ω , RL =2.5 Ω	
tf	Fall Time		7.8				
RG(int)	Internal Gate Resistance		2.0		Ω	f=1 MHz, VAC=25mV	
Qgs	Gate to Source Charge		54		nC		
Qgd	Gate to Drain Charge		29		nC	VDS=800V, VGS=-5/20V ID =30A	
Qg	Total Gate Charge		195				



Reverse Diode Characteristics (TJ= 25°C unless otherwise specified)

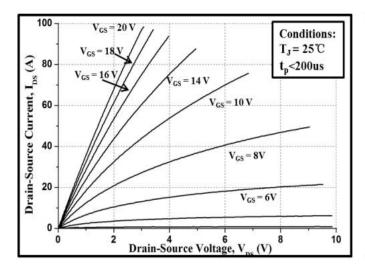
Symbol	Parameter	Тур.	Max	Unit	Test Conditions	Note
VCD	Die de Ferrend Welkere	3.6		٧	VGS=-5V, ISD =25 A, TJ = 25℃	
VSD	Diode Forward Voltage	3.3		٧	VGS=-5V, ISD=25 A, TJ= 150°C	
IS	Continuous Diode Forward Current		90	А	VGS=-5V,TC= 25°C	
trr	Reverse Recovery time	55		ns		
Qrr	Reverse Recovery Charge	320		nC	ISD= 25A, VR = 800V	
Irrm	Peak Reverse Recovery Current	10.7		А	VIX 300V	

Thermal Characteristics (TJ= 25[°]C unless otherwise specified)

Symbol	Parameter	Тур.	Unit	Test Conditions	Note
RθJC	Thermal Resistance from Junction to Case	0.25	°C/W		
RθJA	Thermal Resistance From Junction to Ambient	40	C/ VV		



Typical Feature Curve



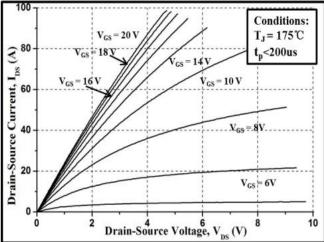
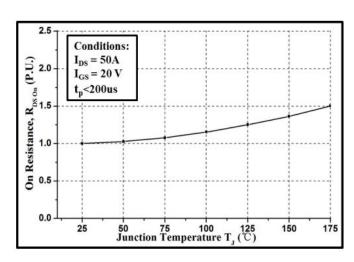


Figure 1. Typical Output Characteristics T_J=25°C

Figure 2. Typical Output Characteristics T_J=175 ℃



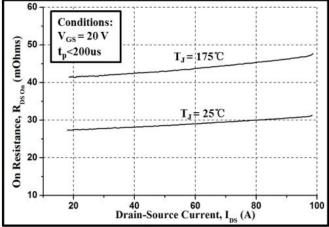


Figure 3. Normalized On-Resistance vs. Temperature

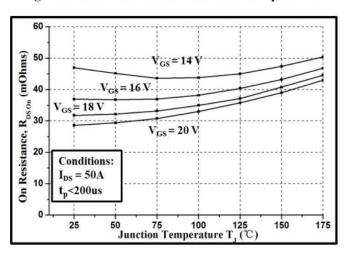


Figure 4. On-Resistance vs. Drain Current

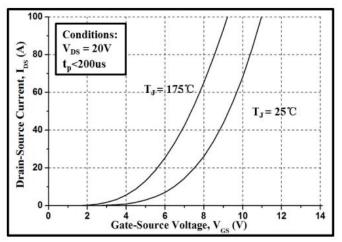


Figure 5. On-Resistance vs. Temperature

Figure 6. Typical Transfer Characteristics

www.reasunos.com 4 / 7 Copyright Reasunos



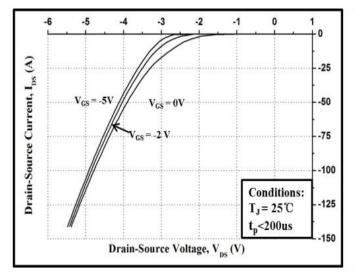
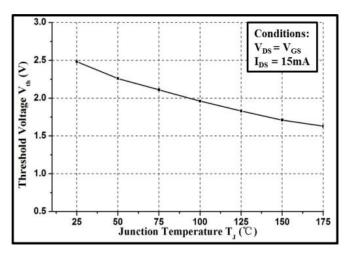


Figure 7. Body Diode Characteristics at 25°C

Figure 8. Body Diode Characteristics at 175°C



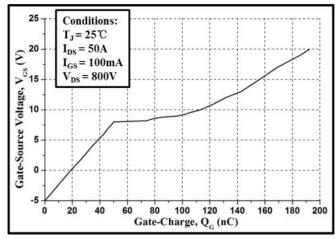
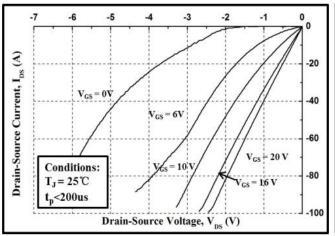


Figure 9. Gate Threshold Voltage vs. Temperature

Figure 10. Gate Charge Characteristic



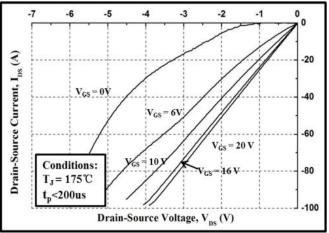


Figure 11. 3rd Quadrant Characteristics at 25° C

Figure 12. 3rd Quadrant Characteristics at 175° C

www.reasunos.com 5 / 7 Copyright Reasunos



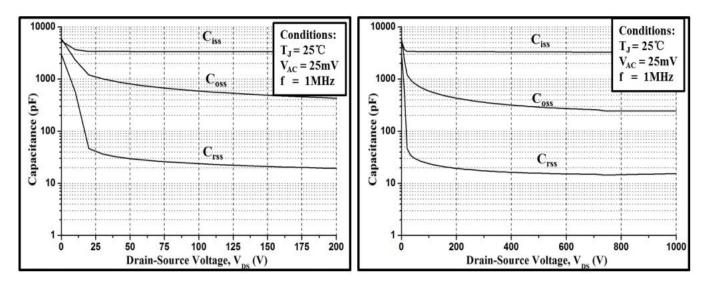
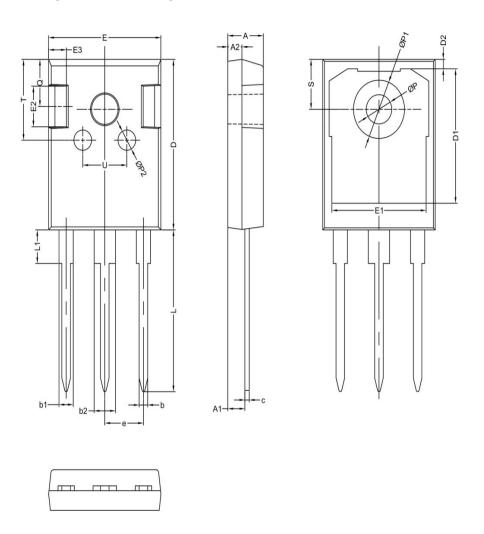


Figure 13. Capactances vs. Drain-Source Voltage

Figure 14. Capactances vs. Drain-Source Voltage

Package outline drawing(TO-247-3 Unit: mm)



符号	机械尺寸/mm					
	最小值	典型值	最大值			
Α	4.80	5.00	5.20			
A1	2.21	2.41	2.61			
A2	1.90	2.00	2.10			
b	1.10	1.20	1.35			
b1		2.00				
b2		3.00				
С	0.55	0.60	0.75			
D	20.80	21.00	21.20			
D1		16.55				
D2		1.20				
E	15.60	15.80	16.0			
E1		13.30				
E2		5.00				
E3		2.50				
е		5.44				
L	19.42	19.92	20.42			
L1		4.13				
P	3.50	3.60	3.70			
P1	-	-	7.40			
P2		2.50				
Q		5.80				
S	6.05	6.15	6.25			
Т		10.00				
U		6.20				



Disclaimers:

Reasunos Semiconductor Technology Co.Ltd (Reasunos) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information in current and complete. All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology Co.Ltd warrants performance of its hardware products to the speciffications at the time of sale. Testing, reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contr- actual agreement, testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology Co.Ltd does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using Reasunos's components. To minimize risk, customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology Co.Ltd does not warrant or convey any license eith- er expressed or implied under its patent rights, nor the rights of others. Reproduction of inform- ation in Reasunos's data sheets or data books is permissible only if reproduction is without modification oralteration. Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology Co.Ltd for that product or service voids all exp- ress or implied warrantees for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology Co.Ltd is not responsi- ble or liable for such statements.

Life Support Policy:

Reasunos Semiconductor Technology Co.Ltd's Products are not authorized for use as cri- tical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology Co.Ltd.

As used herein:

- 1. Life support devices or systems are devices or systems which: a.are intended for surgical implant into the human body, b.support or sustain life,
- c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.

2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.reasunos.com 7 / 7 Copyright Reasunos