



PRODUCT DATA SHEET



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Datasheet Resource

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

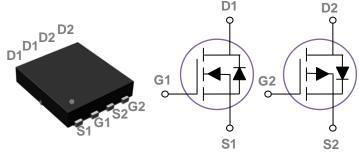
These N+P dual Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
30V	10m Ω	30A
-30V	20m $Ω$	-20A

Features

- Fast switching
- Green Device Available
- Suit for 4.5V Gate Drive Applications
- 100% EAS Guaranteed

PDFN5x6 Dual Pin Configuration



Applications

- DC Fan
- Motor Drive Applications
- Networking
- Half / Full Bridge Topology

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rat	ing	Units
V _{DS}	Drain-Source Voltage	30	-30	V
V_{GS}	Gate-Source Voltage	±20	±20	V
1_	Drain Current – Continuous (Tc=25°C)	30	-20	А
lp	Drain Current – Continuous (Tc=100°C)	14.7	-9.6	А
I _{DM}	Drain Current – Pulsed1	93.2	-60.8	Α
EAS	Single Pulse Avalanche Energy ^{2,6}	39.2	39.2	mJ
IAS	Single Pulse Avalanched Current ²	28	28	Α
D-	Power Dissipation (Tc=25°C)	17	.4	W
P _D	Power Dissipation – Derate above 25°C	0.1	0.14	
T _{STG}	Storage Temperature Range	-55 to	-55 to 150	
TJ	Operating Junction Temperature Range	-55 to	125	°C

Thermal Characteristics

Symbol	Symbol Parameter		Max.	Unit
RθJA	Thermal Resistance Junction to ambient		62.5	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case		7.2	°C/W



N-CH Electrical Characteristics (T_J=25 °C, unless otherwise)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.04		V/°C
I	Drain-Source Leakage Current	V _{DS} =30V , V _{GS} =0V , T _J =25°C			1	uA
I _{DSS}		V _{DS} =24V , V _{GS} =0V , T _J =125°C			10	uA
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	Static Drain-Source On-Resistance	V _{GS} =10V , I _D =10A		10	13	mΩ
	V _{GS} =4.5V , I _D =5A		13	25	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.0	1.5	2.5	V
$\Delta V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	VGS=VDS , ID =250UA		-4		mV/°C
gfs	Forward Transconductance	V _{DS} =5V , I _D =5A		6.4		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}		 7.4	
Q_{gs}	Gate-Source Charge ^{3, 4}	V _{DS} =15V , V _{GS} =4.5V , I _D =5A	 2.3	 nC
Q_{gd}	Gate-Drain Charge ^{3, 4}		 3	
T _{d(on)}	Turn-On Delay Time ^{3, 4}		 3.8	
Tr	Rise Time ^{3, 4}	V_{DD} =15 V , V_{GS} =10 V , R_{G} =6 Ω	 10	 no
T _{d(off)}	Turn-Off Delay Time ^{3, 4}	I _D =1A	 22	 ns
Tf	Fall Time ^{3, 4}		 6.6	
Ciss	Input Capacitance		 620	
Coss	Output Capacitance	V _{DS} =25V , V _{GS} =0V , F=1MHz	 85	 pF
Crss	Reverse Transfer Capacitance		 60	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	 2.8	 Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V . Force Current			30	Α
Іѕм	Pulsed Source Current	VG=VD=OV, Force Current			60	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C			1.2	V

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =28A., R_G =25 Ω ,Starting T_J =25 $^{\circ}$ C.
- 3. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 4. Essentially independent of operating temperature.



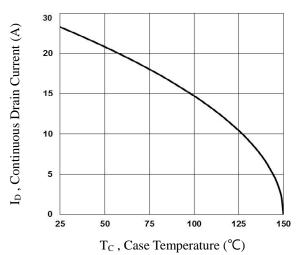


Fig.1 Continuous Drain Current vs. Tc

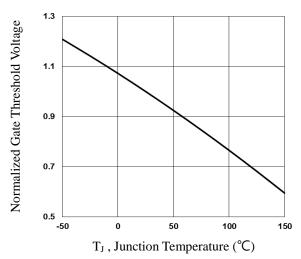


Fig.3 Normalized V_{th} vs. T_J

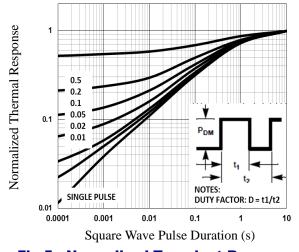


Fig.5 Normalized Transient Response

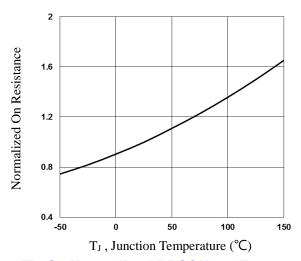


Fig.2 Normalized RDSON vs. T_J

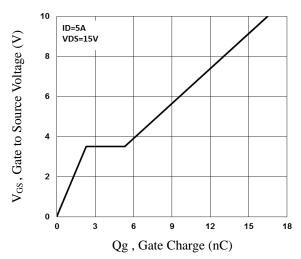


Fig.4 Gate Charge Waveform

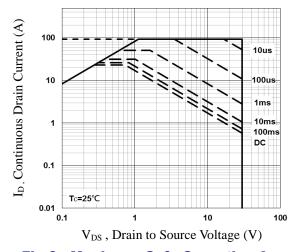


Fig.6 Maximum Safe Operation Area



P-CH Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D = - 250uA	-30			V
△BV _{DSS} /△T _J	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =-1mA		-0.03		V/°C
1	Drain-Source Leakage Current	V _{DS} = - 30V , V _{GS} =0V , T _J =25°C			-1	uA
I _{DSS}		V _{DS} = - 24V , V _{GS} =0V , T _J =125°C			-10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistance	Static Drain Source On Recistance	V _{GS} =-10V , I _D =-7A		20	30	mΩ
	V _{GS} = - 4.5V , I _D = - 4A		30	45	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	\/ \/ \ \ 050\	-1.0	- 1.6	- 2.5	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} =V _{DS} , I _D =-250uA		4		mV/°C
gfs	Forward Transconductance	V _{DS} =-10V , I _D =-3A		5.4		S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{7,8}		 8	
Q_{gs}	Gate-Source Charge ^{7,8}	V_{DS} =-15V , V_{GS} =-4.5V , I_{D} =-5A	 3.3	 nC
Q_gd	Gate-Drain Charge ^{7,8}		 2.3	
$T_{d(on)}$	Turn-On Delay Time ^{7,8}		 4.6	
Tr	Rise Time ^{7,8}	V_{DD} =-15 V , V_{GS} =-10 V , R_{G} =6 Ω	 14	 no
$T_{d(off)}$	Turn-Off Delay Time ^{7,8}	I _D =-1A	 34	 ns
Tf	Fall Time ^{7,8}		 18	
Ciss	Input Capacitance		 757	
Coss	Output Capacitance	V _{DS} =-15V , V _{GS} =0V , F=1MHz	 122	 pF
Crss	Reverse Transfer Capacitance		 88	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	V _G =V _D =0V . Force Current			-20	Α
I _{SM}	Pulsed Source Current	VG=VD=UV, Force Current			-40	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V

Note

- 5. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 6. V_{DD} =-25V, V_{GS} =-10V,L=0.1mH, I_{AS} =-28A., R_{G} =25 Ω ,Starting T_{J} =25 $^{\circ}$ C
- 7. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 8. Essentially independent of operating temperature.



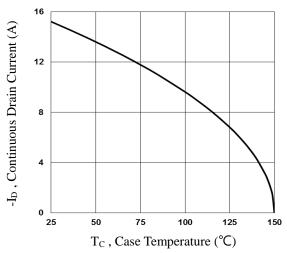


Fig.7 Continuous Drain Current vs. Tc

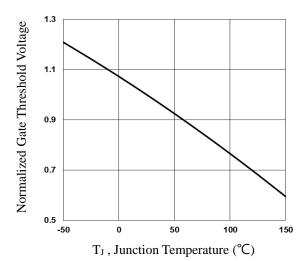


Fig.9 Normalized V_{th} vs. T_J

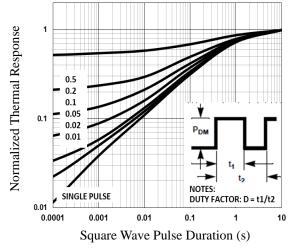


Fig.11 Normalized Transient Impedance

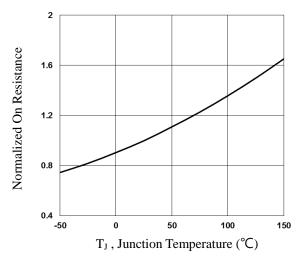


Fig.8 Normalized RDSON vs. TJ

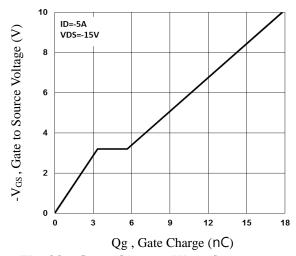


Fig.10 Gate Charge Waveform

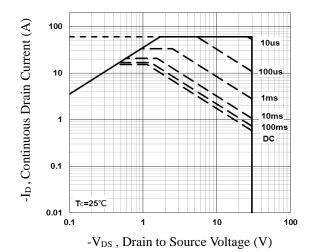
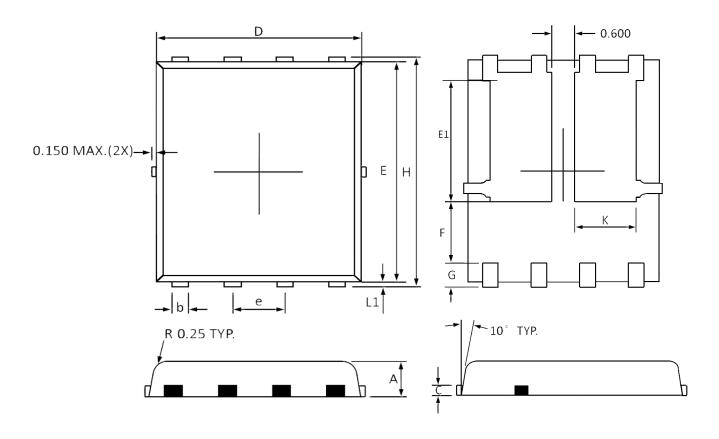


Fig.12 Maximum Safe Operation Area



PDFN5x6 Dual PACKAGE INFORMATION



Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min	Max	Min	Max
Α	0.800	1.200	0.031	0.047
b	0.300	0.510	0.012	0.020
С	0.250	0 Ref	0.010) Ref
D	4.800	5.400	0.189	0.213
E	5.450	5.960	0.215	0.235
E1	3.200	3.800	0.126	0.150
е	1.27	BSC	0.050	BSC
F	1.000	1.900	0.039	0.075
G	0.380	0.800	0.015	0.031
Н	5.850	6.300	0.230	0.248
L1	0.050	0.250	0.002	0.010
K	1.500	1.900	0.059	0.074



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