

## **IRFR4510TRPBF-VB Datasheet**

# N-Channel 100-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)			
100	0.0075 at V <sub>GS</sub> = 10 V	85			
	0.0095 at V <sub>GS</sub> = 4.5 V	75			

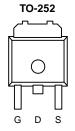
#### **FEATURES**

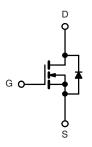
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested



#### **APPLICATIONS**

- Primary Side Switch
- · Isolated DC/DC Converter





N-Channel MOSFET

ABSOLUTE MAXIMUM RAT	TA = 20 0, unicos otrici	Symbol		i	
Parameter			Limit	Unit	
Drain-Source Voltage			100	V	
Gate-Source Voltage	$V_{GS}$	± 20	V		
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 25 °C	. I <sub>D</sub>	85		
Continuous Diain Current (1) = 130 C)	T <sub>C</sub> = 125 °C		75 <sup>a</sup>		
Pulsed Drain Current		I <sub>DM</sub>	300	Α	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	75		
Single Pulse Avalanche Energy <sup>b</sup>	- L=0.1 IIIH	E <sub>AS</sub>	280	mJ	
Maximum David Discipation	T <sub>C</sub> = 25 °C (TO-252 and TO-251)	P <sub>D</sub>	210	w	
Maximum Power Dissipation <sup>b</sup>	T <sub>A</sub> = 25 °C (TO-252)	'D	3.25		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b</sup>	Steady State	R <sub>thJA</sub>	40	50	°C/W	
Maximum Junction-to-Case	Sleady State	R <sub>thJC</sub>	0.85	1.1	C/VV	

#### Notes:

- a. Package limited.
- b. Surface mounted on 1" x 1" FR4 board.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

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<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS}$ $V_{GS} = 0 \text{ V, } I_{D} = 250  \mu\text{A}$				V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \mu A$			3	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1		
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50		
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			250		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	120			Α	
		$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$		0.0075			
	В	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		0.0095		Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C		0.017			
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C		0.022		1	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A	25			S	
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>			4550		pF	
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$		565			
Reverse Transfer Capacitance	C <sub>rss</sub>			205			
Total Gate Charge <sup>c</sup>	Qg			105	160		
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 85 \text{ A}$		17		nC	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	1		23		1	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>			12	25		
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_{L} = 0.6 \Omega$		90	135	ns	
Turn-Off DelayTime <sup>c</sup>	t <sub>d(off)</sub>	$I_D\cong 85$ A, $V_{GEN}=10$ V, $R_g=2.5~\Omega$		55	85		
Fall Time <sup>c</sup>	t <sub>f</sub>	1		130	195		
Source-Drain Diode Ratings and Cha	racteristics T <sub>C</sub>	= 25 °C <sup>b</sup>	•				
Continuous Current	Is				85		
Pulsed Current	I <sub>SM</sub>				240	Α	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 85 A, V <sub>GS</sub> = 0 V		1.0	1.5	V	
Reverse Recovery Time	t <sub>rr</sub>			85	140	ns	
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = 50 A, dI/dt = 100 A/μs		4.5	7	Α	
Reverse Recovery Charge	Q <sub>rr</sub>			0.17	0.35	μC	

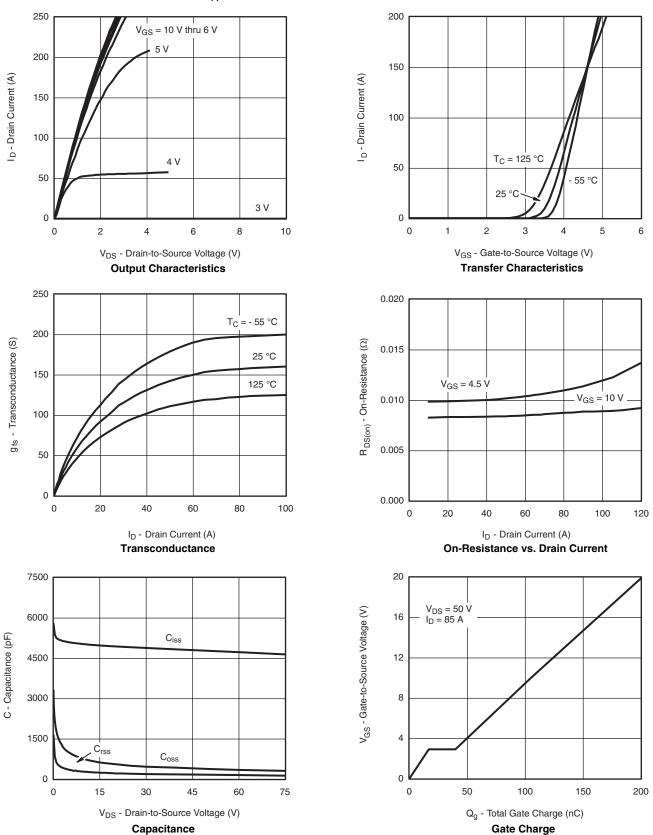
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

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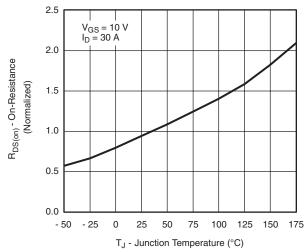


**TYPICAL CHARACTERISTICS**  $T_A = 25 \, ^{\circ}C$ , unless otherwise noted

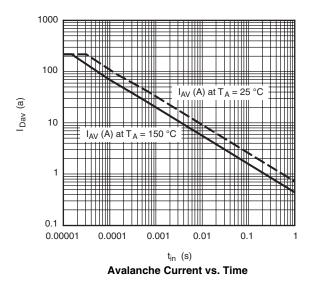




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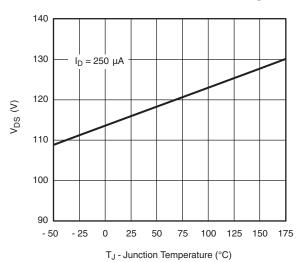


#### On-Resistance vs. Junction Temperature



T<sub>J</sub> = 150 °C T<sub>J</sub> = 25 °C T<sub>J</sub>

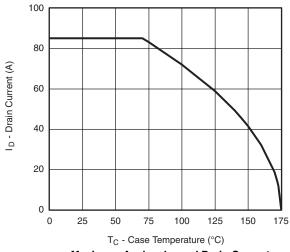
Source-Drain Diode Forward Voltage

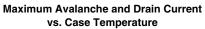


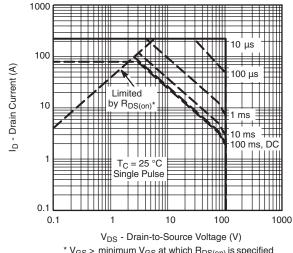
T<sub>J</sub> - Drain-Source Breakdown vs. Junction-Temperature



#### THERMAL RATINGS

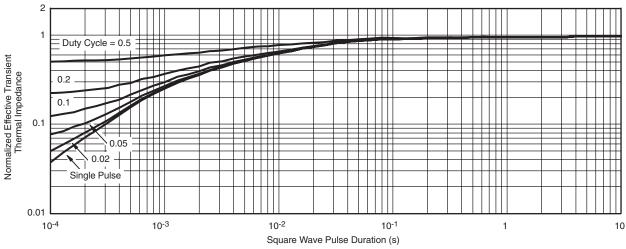






\*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

Safe Operating Area



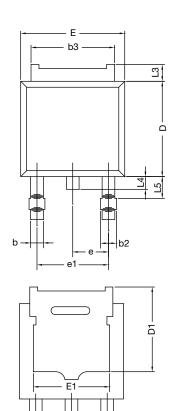
Normalized Thermal Transient Impedance, Junction-to-Case

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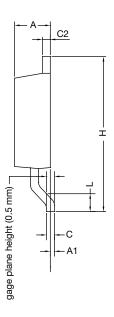
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# **TO-252AA CASE OUTLINE**



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	MILLIMETERS		INC	HES	
DIM.	MIN.	MAX.	MIN.	MAX.	
Α	2.18	2.38	0.086	0.094	
A1	-	0.127	-	0.005	
b	0.64	0.88	0.025	0.035	
b2	0.76	1.14	0.030	0.045	
b3	4.95	5.46	0.195	0.215	
С	0.46	0.61	0.018	0.024	
C2	0.46	0.89	0.018	0.035	
D	5.97	6.22	0.235	0.245	
D1	5.21	-	0.205	-	
Е	6.35	6.73	0.250	0.265	
E1	4.32	1	0.170	-	
Н	9.40	10.41	0.370	0.410	
е	2.28 BSC		0.090 BSC		
e1	4.56	4.56 BSC		BSC	
L	1.40	1.78	0.055	0.070	
L3	0.89	1.27	0.035	0.050	
L4	-	1.02	-	0.040	
L5	1.14	1.52	0.045	0.060	
ECN: X12-0247-Rev. M, 24-Dec-12					

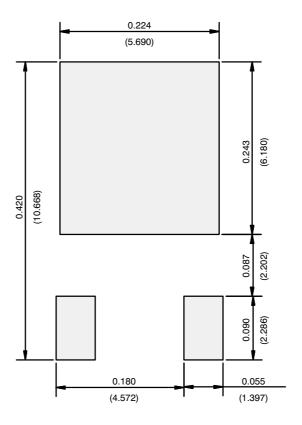
ECN: X12-0247-Rev. M, 24-Dec-12 DWG: 5347

#### Note

• Dimension L3 is for reference only.



## **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)



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