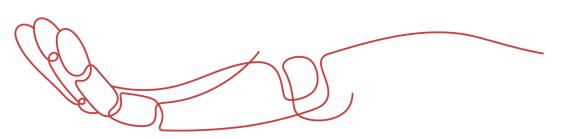




# **PRODUCT DATA SHEET**



To learn more about JGSEMI, please visit our website at







Datasheet

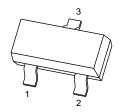
urces Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO\_questions@jgsemi.com.



SOT-23 Plastic-Encapsulate MOSFETs

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	Ι <sub>D</sub>
-50 V	8Ω@-10V	
-50 V	10Ω@ <i>-</i> 5V	-0.13A

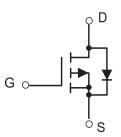


- 1. GATE
- 2. SOURCE
- 3. DRAIN

**SOT-23** 

## **FEATURE**

- Energy Efficient
- Low Threshold Voltage
- High-speed Switching
- Miniature Surface Mount Package Saves Board Space



#### **APPLICATION**

 DC-DC converters,load switching, power management in portable and battery-powered products such as computers, printers, cellular and cordless telephones.

## MAXIMUM RATINGS (T<sub>a</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	-50	V
Gate-Source Voltage	$V_{GS}$	±20	V
Continuous Drain Current	I <sub>D</sub>	-0.13	Α
Pulsed Drain Current (note 1) @tp <10 μs	I <sub>DM</sub>	-0.52	Α
Power Dissipation	P <sub>D</sub>	225	mW
Thermal Resistance from Junction to Ambient (note 2)	$R_{\theta JA}$	556	°C/W
Junction Temperature	TJ	125	°C
Storage Temperature	T <sub>STG</sub>	-55~+150	°C
Maximum Lead Temperature for Soldering Purposes , Duration for 5 Seconds	TL	260	°C



## MOSFET ELECTRICAL CHARACTERISTICS $T_a$ =25 $^{\circ}$ C unless otherwise specified

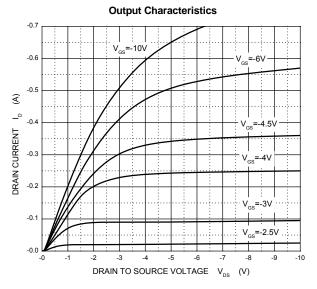
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit			
STATIC CHARACTERISTICS									
Drain-source breakdown voltage	V (BR)DSS	V <sub>GS</sub> = 0V, I <sub>D</sub> =-250μA	-50			V			
Zero gate voltage drain current	Ipss	V <sub>DS</sub> =-50V,V <sub>GS</sub> = 0V			-15	μA			
Zero gate voltage drain current	IDSS	V <sub>DS</sub> =-25V,V <sub>GS</sub> = 0V			-0.1	μA			
Gate-body leakage current	Igss	V <sub>GS</sub> =±20V, V <sub>DS</sub> = 0V			±5	μΑ			
Gate threshold voltage (note 3)	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250µA	-0.9	-1.6	-2	V			
Drain source on registence (note 2)	Prov. )	V <sub>GS</sub> =-5V, I <sub>D</sub> =-0.1A		5.8	10	Ω			
Drain-source on-resistance (note 3)	RDS(on)	V <sub>GS</sub> =-10V, I <sub>D</sub> =-0.1A		4.5	8	Ω			
Forward transconductance (note 1)	<b>g</b> FS	V <sub>DS</sub> =-25V; I <sub>D</sub> =-100mA	50			mS			
DYNAMIC CHARACTERISTICS (note	4)								
Input capacitance	C <sub>iss</sub>			30		pF			
Output capacitance	Coss	V <sub>DS</sub> =5V,V <sub>GS</sub> =0V,f =1MHz		10		pF			
Reverse transfer capacitance	C <sub>rss</sub>			5		pF			
SWITCHING CHARACTERISTICS (not	te 4)								
Turn-on delay time	td(on)			2.5		ns			
Turn-on rise time	tr	V <sub>DD</sub> =-15V,		1		ns			
Turn-off delay time	td(off)	R <sub>L</sub> =50Ω, I <sub>D</sub> =-2.5A		16		ns			
Turn-off fall time	<b>t</b> f			8		ns			
SOURCE-DRAIN DIODE CHARACTE	RISTICS								
Continuous Current	Is				-0.13	Α			
Pulsed Current	I <sub>SM</sub>				-0.52	Α			
Diode forward voltage (note 3)	VsD	I <sub>S</sub> =-0.13A, V <sub>GS</sub> = 0V			-2.2	V			

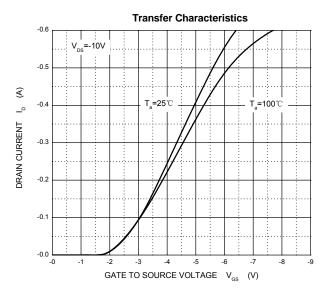
#### Notes:

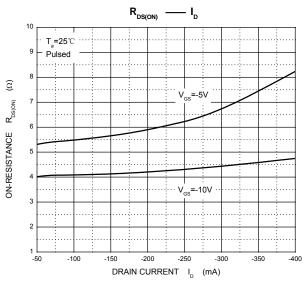
- 1. Repetitive rating : Pulse width limited by junction temperature.
- 2. Surface mounted on FR4 board , t≤10s.
- 3. Pulse Test : Pulse Width≤300µs, Duty Cycle≤2%.
- 4. Guaranteed by design, not subject to producting.

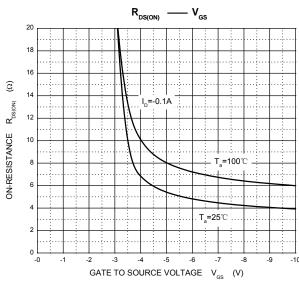


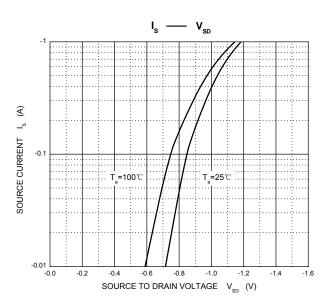
## **Typical Characteristics**

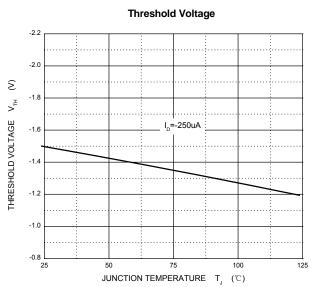












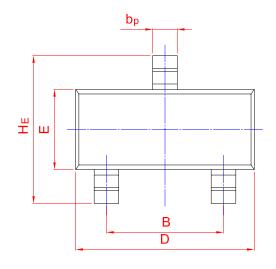


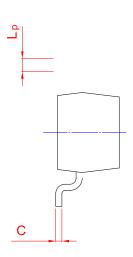
## **PACKAGE OUTLINE**

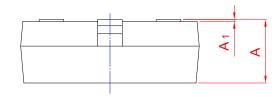
Plastic surface mounted package; 3 leads

**SOT-23** 









UI	VIT	Α	В	bp	С	D	E	HE	<b>A</b> 1	Lp
m	m	1.40 0.95	2.04 1.78	0.50 0.35	0.19 0.08	3.10 2.70	1.65 1.20	3.00 2.20	0.100 0.013	



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