



PRODUCT DATA SHEET



To learn more about JGSEMI, please visit our website at



Datasheet



Resources

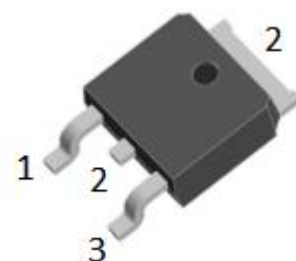


Samples

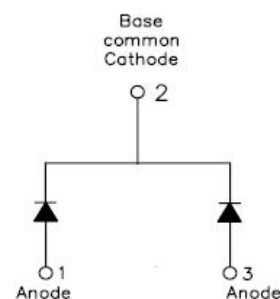
Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.

Features

- 150°C T_J operation
- Center tap configuration
- Low forward voltage drop
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- This is a Pb – Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request



DPAK



Applications

- Switching power supply
- Converters
- Free-Wheeling diodes
- Reverse battery protection
- Battery charging

Maximum Ratings

Characteristics	Symbol	Condition	Max.	Units
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	-	45	V
Average Rectified Forward Current	$I_F (AV)$	50% duty cycle @T _C = 135°C, rectangular wave form	10(Peg Leg) 20(Peg Device)	A
Peak One Cycle Non-Repetitive Surge Current(peg leg)	I_{FSM}	8.3 ms, half Sine pulse	150	A

Electrical Characteristics

Characteristics	Symbol	Condition	Typ.	Max.	Units
Forward Voltage Drop (Peg Leg) *	V_{F1}	@ 20A, Pulse, T _J = 25 °C	0.74	0.84	V
	V_{F2}	@ 20A, Pulse, T _J = 125 °C	0.70	0.72	V
Reverse Current (Peg Leg) *	I_{R1}	@V _R = rated V _R , T _J = 25 °C	0.03	1	mA
	I_{R2}	@V _R = rated V _R , T _J = 125 °C	9	15.0	mA
Junction Capacitance(Peg Leg)	C_T	@V _R = 5V, T _C = 25 °C, f _{SIG} = 1MHz	280	400	pF
Series Inductance(Peg Leg)	L_S	Measured lead to lead 5 mm from package body	8.0	-	nH
Voltage Rate of Change	dv/dt	-	-	10,000	V/μs

* Pulse width < 300 μs, duty cycle < 2%

Thermal-Mechanical Specifications

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	T_J	-	-55 to +150	°C
Storage Temperature	T_{stg}	-	-55 to + 150	°C
Typical Thermal Resistance Junction to Case(Peg Leg)	$R_{\theta JC}$	-	5	°C/W
Approximate Weight	wt	-	0.39	g
Case Style	DPAK			

Ratings and Characteristics Curves

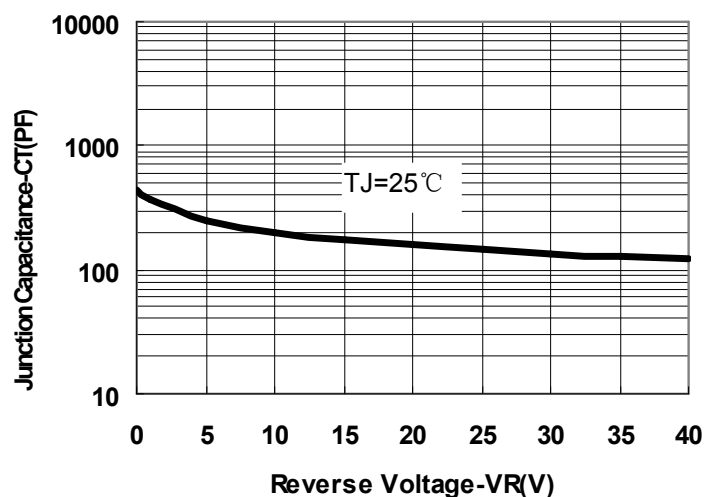


Fig.1-Typical Junction Capacitance

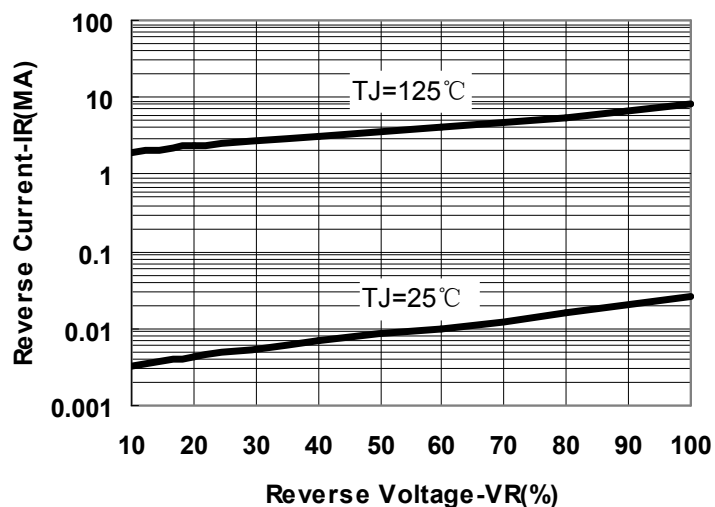


Fig.2-Typical Reverse Characteristics

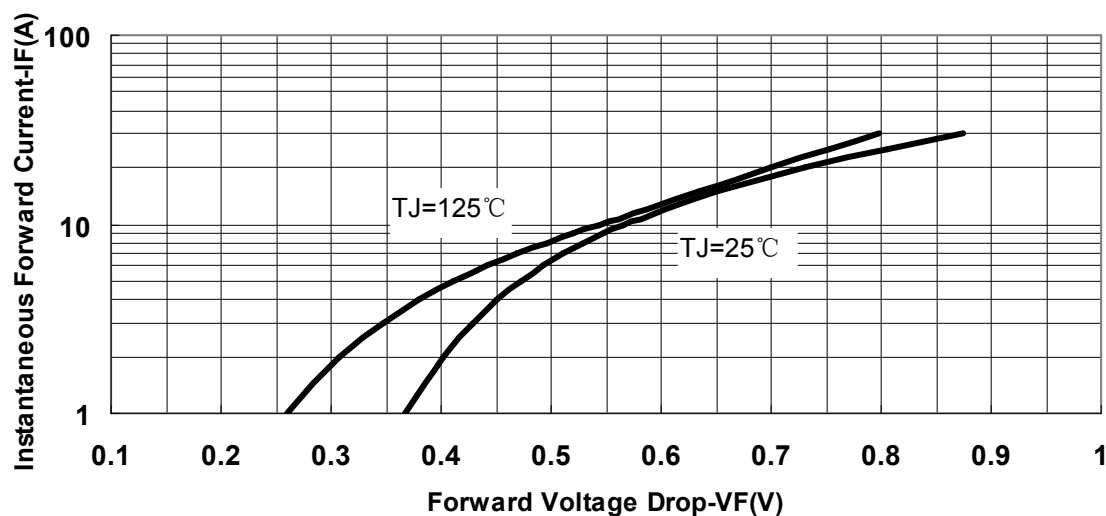
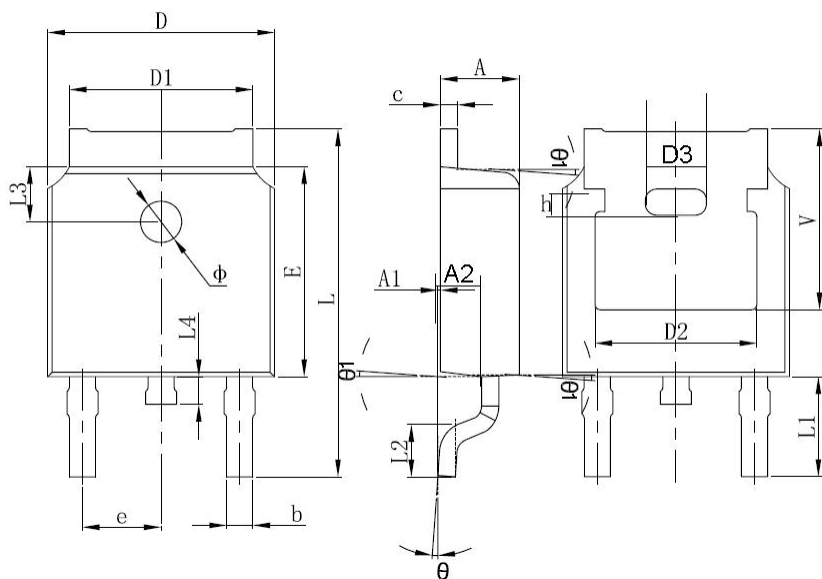


Fig.3-Typical Instantaneous Forward Voltage Characteristics

Mechanical Dimensions DPAK


SYMBOL	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	2.20	2.40	0.087	0.094
A1	0.00	0.127	0.000	0.005
b	0.66	0.86	0.026	0.034
c	0.46	0.60	0.018	0.024
D	6.50	6.70	0.256	0.264
D1	5.13	5.46	0.202	0.215
D2	4.83 REF.		0.190 REF.	
E	6.00	6.20	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.70	10.40	0.381	0.409
L1	2.90 REF.		0.144 REF.	
L2	1.40	1.70	0.055	0.067
L3	1.60 REF.		0.063 REF.	
L4	0.60	1.00	0.024	0.039
Φ	1.10	1.30	0.043	0.051
θ	0°	8°	0°	8°
h	0.00	0.30	0.000	0.012
V	5.35 REF.		0.211 REF.	

Attention

- 1, Any and all JGSEMI products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical or material damage. Consult with your JGSEMI representative nearest you before using any JGSEMI products described or contained herein in such applications.
- 2, JGSEMI assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all JGSEMI products described or contained herein.
- 3, Specifications of any and all JGSEMI products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- 4, In the event that any or all JGSEMI products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- 5, No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of JGSEMI Semiconductor CO., LTD.
- 6, Any and all information described or contained herein are subject to change without notice due to product technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the JGSEMI product that you intend to use.