



N 沟道增强型场效应晶体管
N-CHANNEL MOSFET
FHD100N08B

主要参数 MAIN CHARACTERISTICS

ID	100A
VDSS	80 V
Rdson-typ (@Vgs=10V)	6.5 mΩ
Qg-typ	137nC

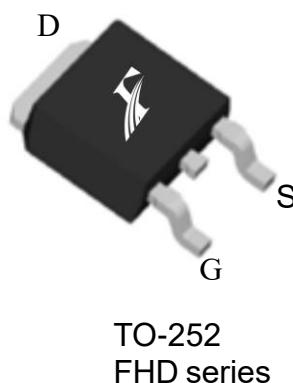
用途 APPLICATIONS

功率开关	Switch mode power supplies
直流电机控制	DC motor control

产品特性 FEATURES

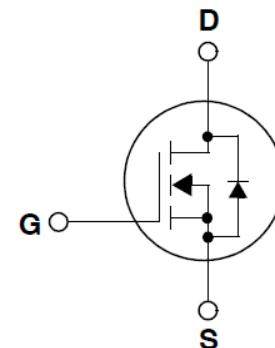
低栅极电荷	Low gate charge
低 Crss (典型值 315pF)	Low Crss (typical 315pF)
开关速度快	Fast switching
100% 经过 Rg 测试	100% Rg tested
100% 经过雪崩测试	100% avalanche tested
100% 经过热阻测试	100% DVDS tested
Trench 工艺	Trench process
RoHS 产品	RoHS product

封装形式 Package



TO-252
FHD series

等效电路 Equivalent Circuit



绝对最大额定值 ABSOLUTE RATINGS (Tc=25°C)

项目 Parameter	符号 Symbol	数值 Value	单位 Unit
		FHD100N08B	
最高漏极一源极直流电压 Drain-Source Voltage	Vds	80	V
连续漏极电流* Drain Current -continuous *	I _D (Tc=25°C)	100	A
	I _D (Tc=100°C)	68	A
最大脉冲漏极电流 (注 1) Drain Current – pulse (note 1)	I _{DM}	400	A
最高栅源电压 Gate-Source Voltage	V _{GS}	±20	V
单脉冲雪崩能量 (注 2) Single Pulsed Avalanche Energy (note 2)	E _{AS}	364.5	mJ
雪崩电流 (注 1) Avalanche Current (note 1)	I _{AR}	27	A
重复雪崩能量 (注 1) Repetitive Avalanche Current (note 1)	E _{AR}	23	mJ
二极管反向恢复最大电压变化速率 (注 3) Peak Diode Recovery dv/dt (note 3)	dv/dt	5.0	V/ns
耗散功率 Power Dissipation	P _D (TC=25°C)	70	W
	-Derate above 25°C	0.56	W/°C
最高结温及存储温度 Operating and Storage Temperature Range	T _J , T _{STG}	-55~+150	°C
引线最高焊接温度 Maximum Lead Temperature for Soldering Purposes	T _L	300	°C

*漏极电流由最高结温限制

*Drain current limited by maximum junction temperature

电特性 ELECTRICAL CHARACTERISTICS

项目 Parameter	符号 Symbol	测试条件 Tests conditions	最小 Min	典型 Typ	最大 Max	单位 Units	
关态特性 Off -Characteristics							
漏—源击穿电压 Drain-Source Voltage	BVDSS	Id=250μA, VGS=0V	80	-	-	V	
击穿电压温度特性 Breakdown Voltage Temperature Coefficient	ΔBVDSS/Δ TJ	Id=250μA, referenced to 25°C	-	0.08	-	V/°C	
零栅压下漏极漏电流 Zero Gate Voltage Drain Current	Idss	VDS=80V, VGS=0V, Tc=25°C	-	-	1	μA	
		VDS=64V, Tc=125°C	-	-	50	μA	
栅极体漏电流 Gate-body leakage current	IGSS (F/R)	VDS=0V, VGS =±20V	-	-	±100	nA	
通态特性 On-Characteristics							
阈值电压 Gate Threshold Voltage	VGS(th)	VDS = VGS , Id=250μA	2.0	-	4.0	V	
静态导通电阻 Static Drain-Source On-Resistance	RDS(ON)	VGS =10V , Id=50A	-	6.5	7.2	mΩ	
正向跨导 Forward Transconductance	gfs	VDS = 10V, Id=50A (note 4)	-	62	-	S	
动态特性 Dynamic Characteristics							
栅电阻 Gate Resistance	Rg	f=1.0MHz, VDS OPEN	-	3.9	-	Ω	
输入电容 Input capacitance	Ciss	VDS=40V, VGS =0V, f=1.0MHz	-	6943	-	pF	
输出电容 Output capacitance	Coss		-	424	-		
反向传输电容 Reverse transfer capacitance	Crss		-	315	-		
开关特性 Switching Characteristics							
延迟时间 Turn-On delay time	td(on)	VDD=40V, Id=30A, RG=4.7Ω VGS =10V (note 4, 5)	-	28	-	ns	
上升时间 Turn-On rise time	tr		-	76	-	ns	
延迟时间 Turn-Off delay time	td(off)		-	128	-	ns	
下降时间 Turn-Off Fall time	tf		-	51	-	ns	
栅极电荷总量 Total Gate Charge	Qg	VDS =64V , Id=30A , VGS =10V (note 4, 5)	-	137	-	nC	
栅—源电荷 Gate-Source charge	Qgs		-	34	-	nC	
栅—漏电荷 Gate-Drain charge	Qgd		-	73	-	nC	
漏—源二极管特性及最大额定值 Drain-Source Diode Characteristics and Maximum Ratings							
正向最大连续电流 Maximum Continuous Drain -Source Diode Forward Current	Is		-	-	100	A	
正向最大脉冲电流 Maximum Pulsed Drain-Source Diode Forward Current	ISM		-	-	400	A	
正向压降 Drain-Source Diode Forward Voltage	VSD	VGS=0V, Is=50A	-	-	1.4	V	
反向恢复时间 Reverse recovery time	trr	VGS=0V, Is=50A ,dI/dt=100A/μs (note 4)	-	43	-	ns	
反向恢复电荷 Reverse recovery charge	Qrr		-	72	-	nC	

热特性 THERMAL CHARACTERISTIC

项目 Parameter	符号 Symbol	最大值 Max	单位 Unit
结到管壳的热阻 Thermal Resistance, Junction to Case	R _{th(j-c)}	1.78	°C/W
结到环境的热阻 Thermal Resistance, Junction to Ambient	R _{th(j-A)}	40	°C/W

注释:

- 1: 脉冲宽度由最高结温限制
- 2: L=1mH, I_{AS}=27A, V_{DD}=50V, R_G=25 Ω, 起始结温 T_J=25°C
- 3: I_{SD} ≤ 100A, di/dt ≤ 300A/μs, V_{DD} ≤ BV_{DSS}, 起始结温 T_J=25°C
- 4: 脉冲测试: 脉冲宽度 ≤ 300μs, 占空比 ≤ 2%
- 5: 基本与工作温度无关

Notes:

- 1: Pulse width limited by maximum junction temperature
- 2: L=1mH, I_{AS}=27A, V_{DD}=50V, R_G=25 Ω, Starting T_J=25°C
- 3: I_{SD} ≤ 100A, di/dt ≤ 300A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J=25°C
- 4: Pulse Test: Pulse Width ≤ 300μs, Duty Cycle≤2%
- 5: Essentially independent of operating temperature

Typical Performance Characteristics

Fig. 1. On-state characteristics

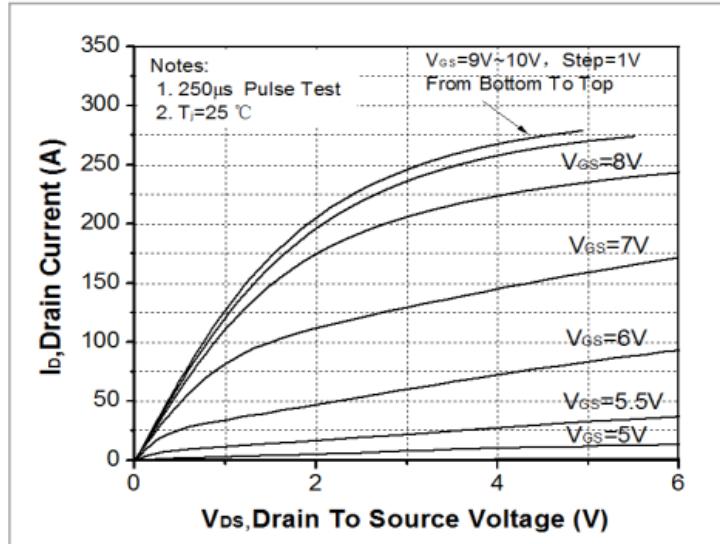


Fig. 2. Transfer Characteristics

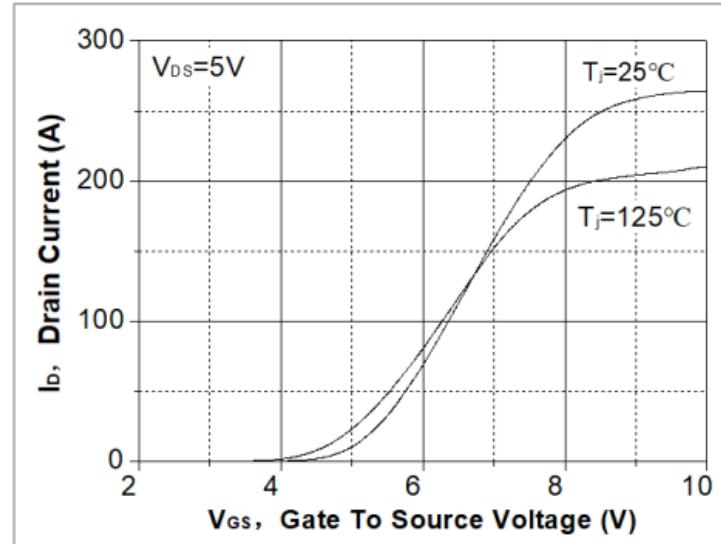


Fig. 3. On-resistance variation vs. drain current and gate voltage

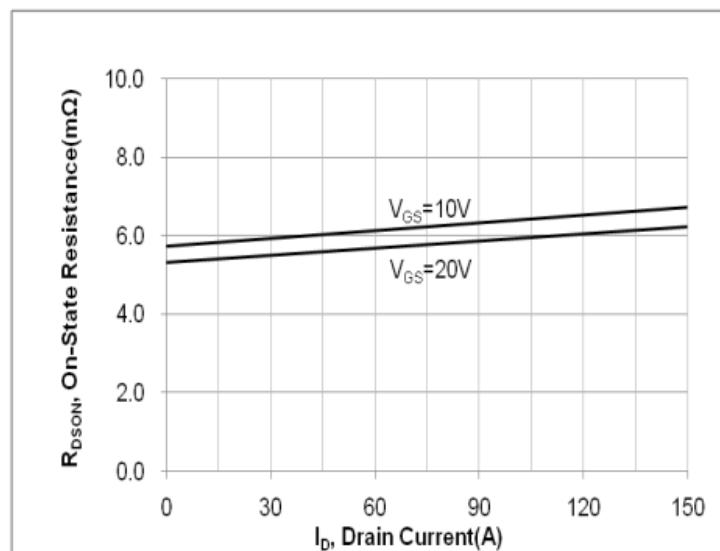


Fig. 4. On-state current vs. diode forward voltage

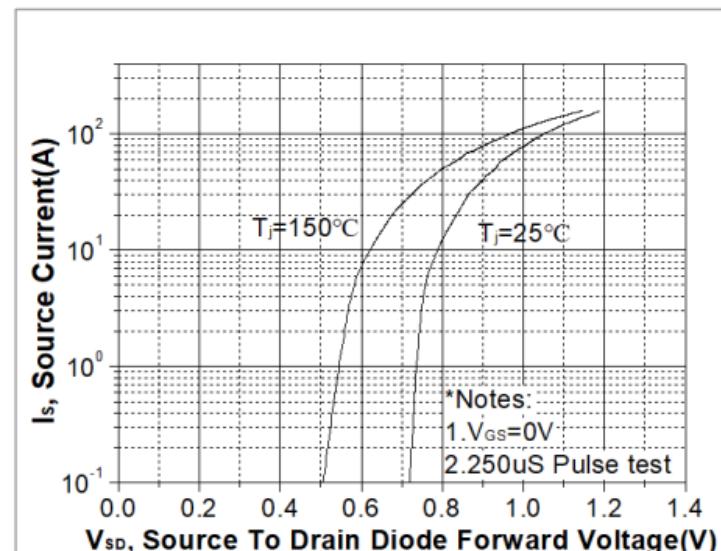


Fig 5. Breakdown voltage variation vs. junction temperature

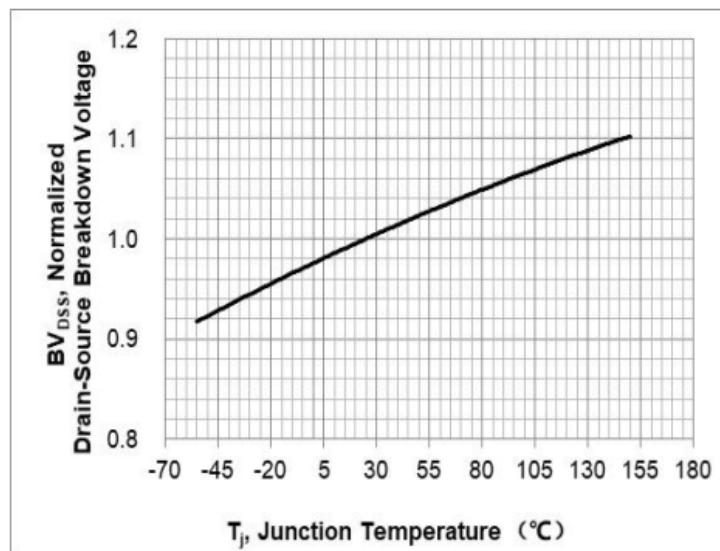


Fig. 6. On-resistance variation vs. junction temperature

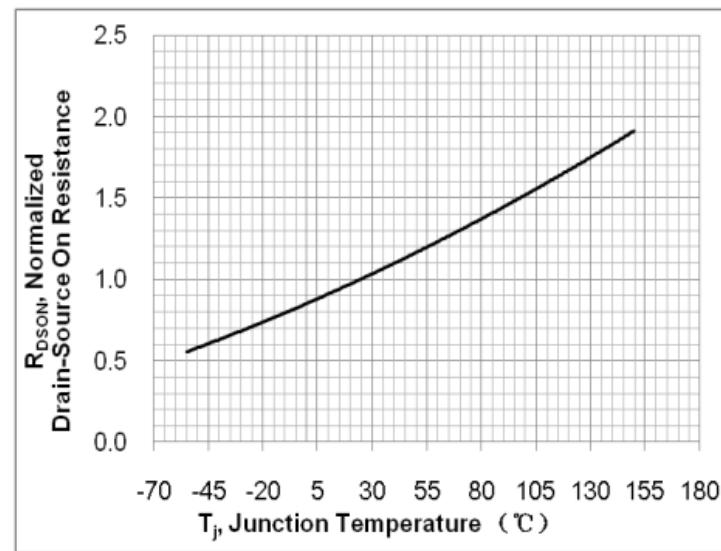


Fig. 7. Gate charge characteristics

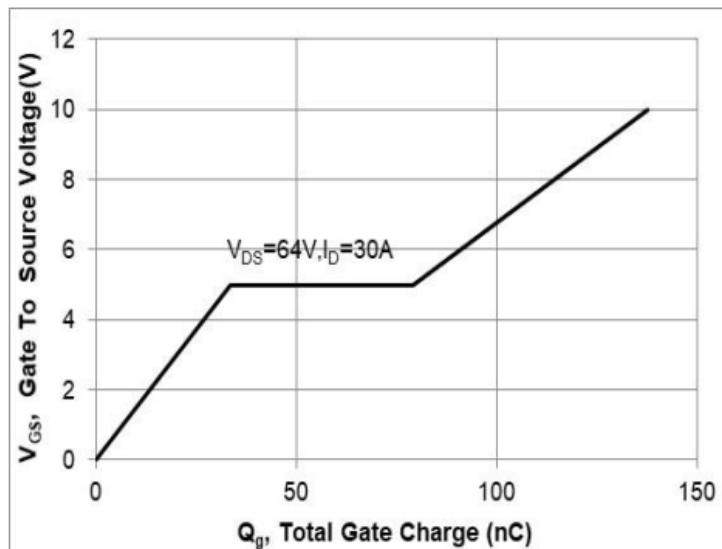


Fig. 8. Capacitance Characteristics

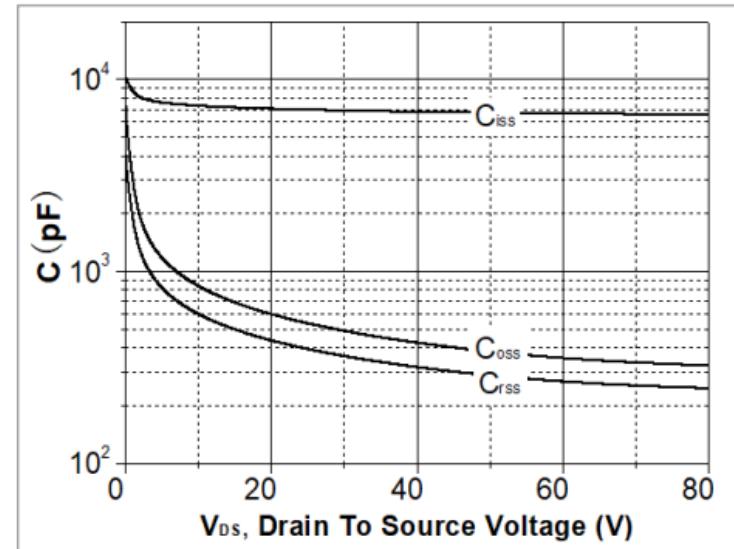


Fig. 9. Maximum safe operating area (TO-220&TO-263)

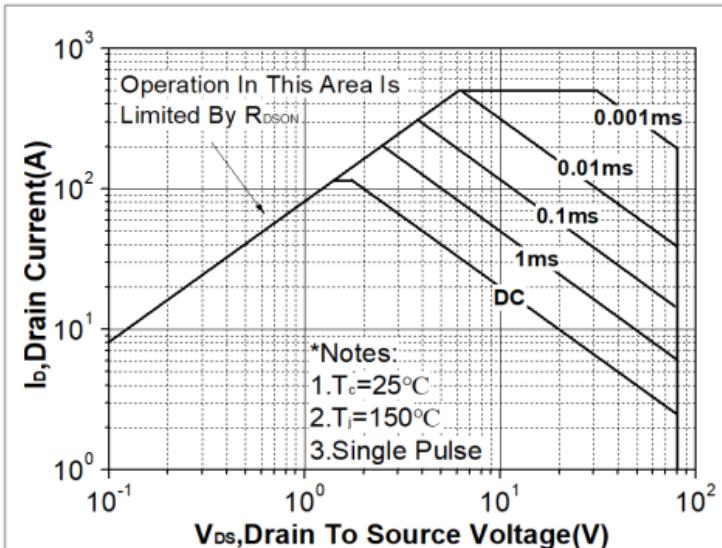


Fig. 10. Maximum drain current vs. case temperature(TO-220&TO-263)

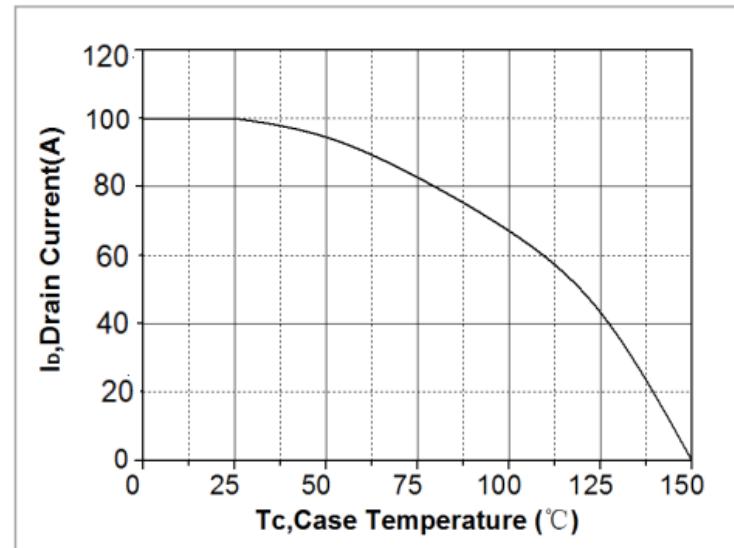
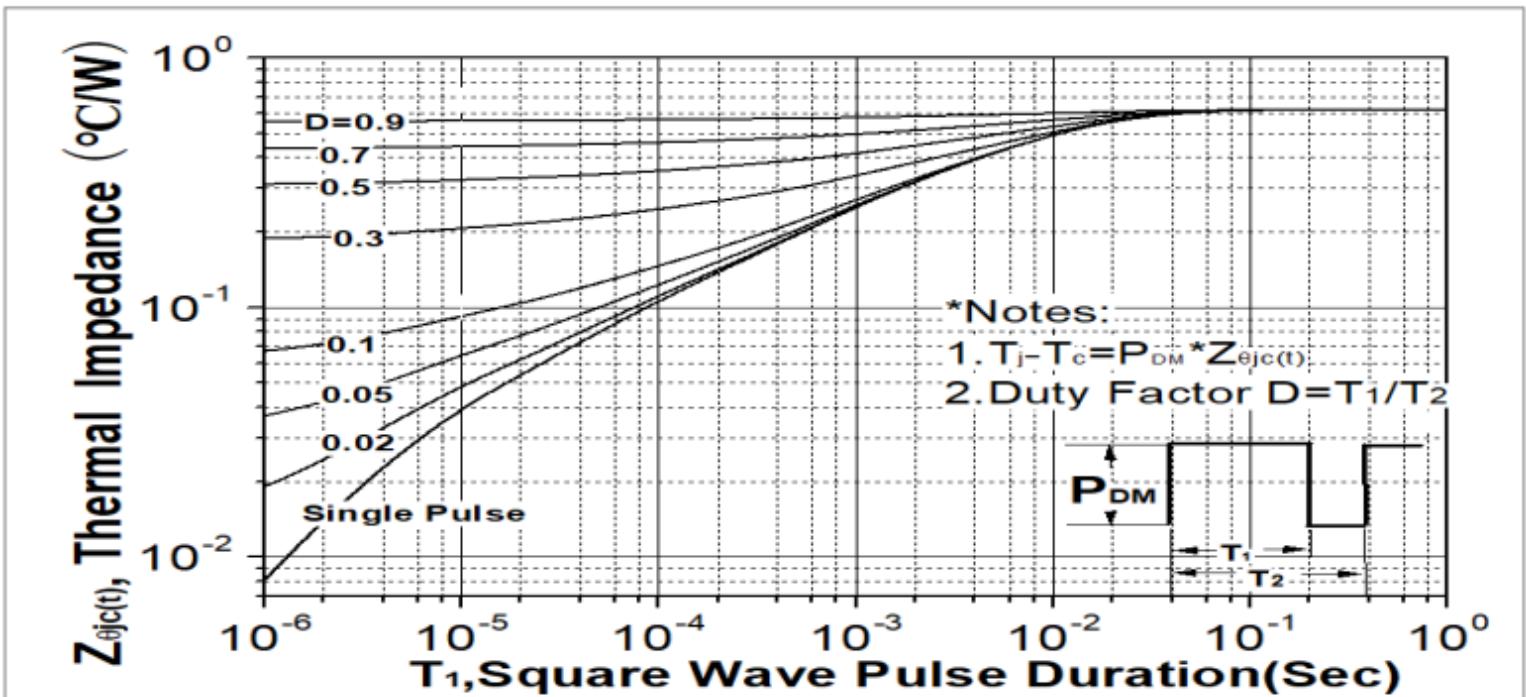


Fig. 11. Transient thermal response curve(TO-220&TO-263)



Test Circuit & Waveform

Fig. 12. Gate charge test circuit & waveform

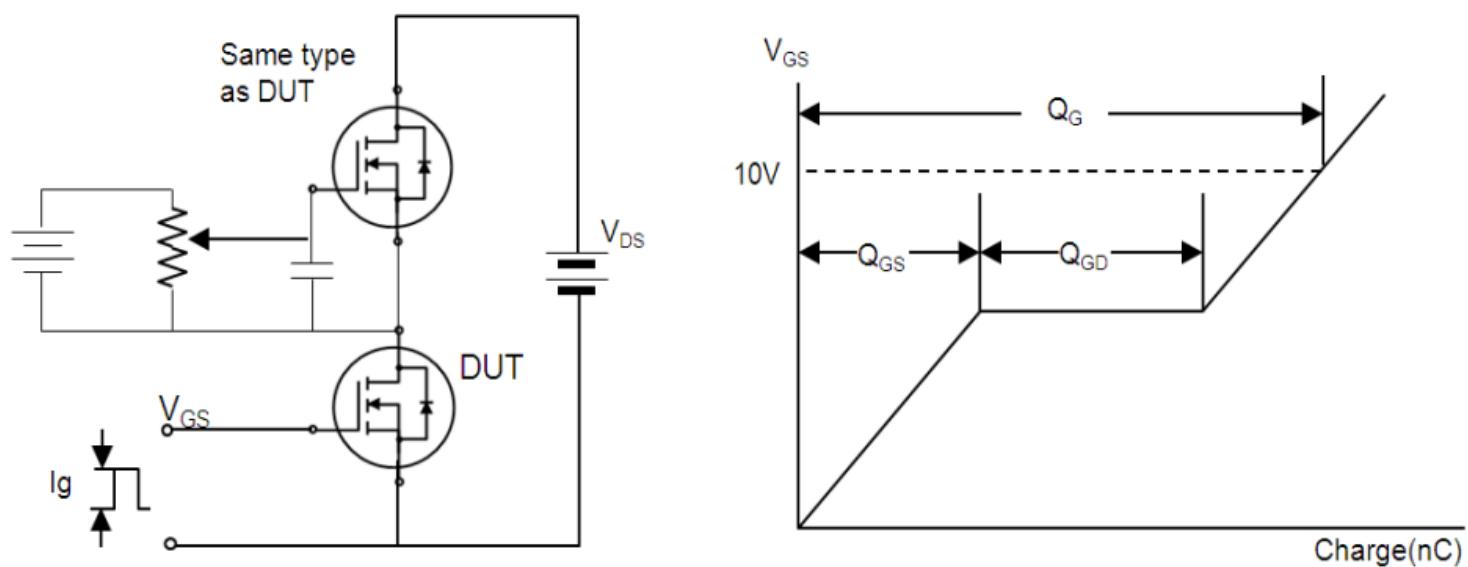


Fig. 13. Switching time test circuit & waveform

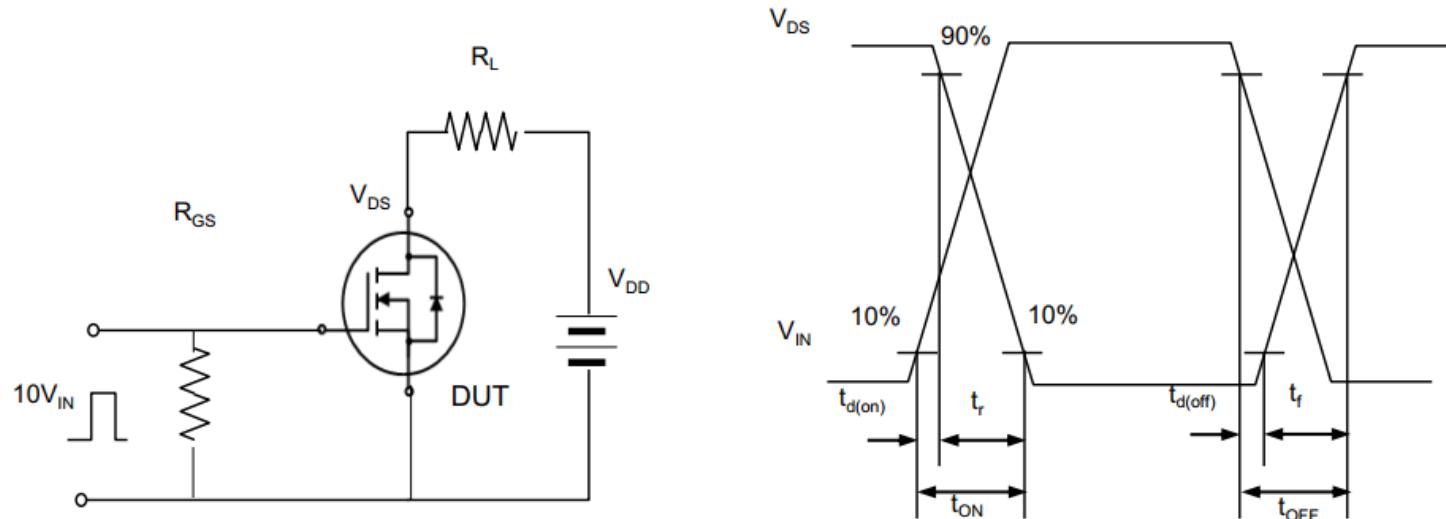
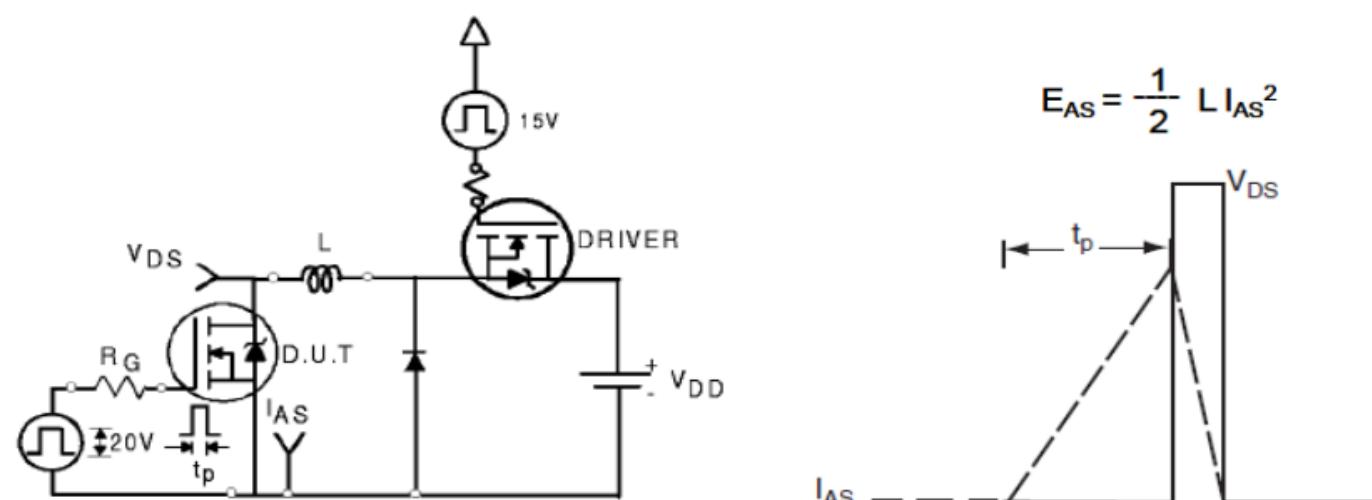
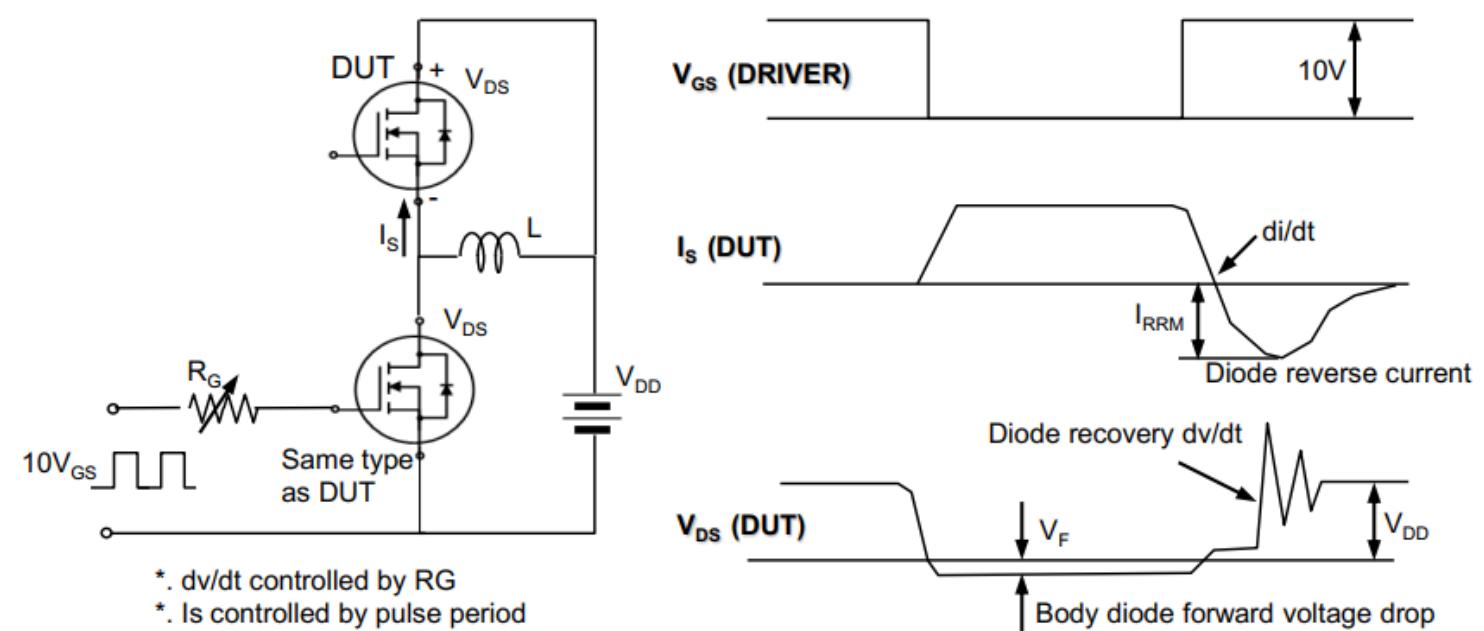


Fig. 14. Unclamped Inductive switching test circuit & waveform

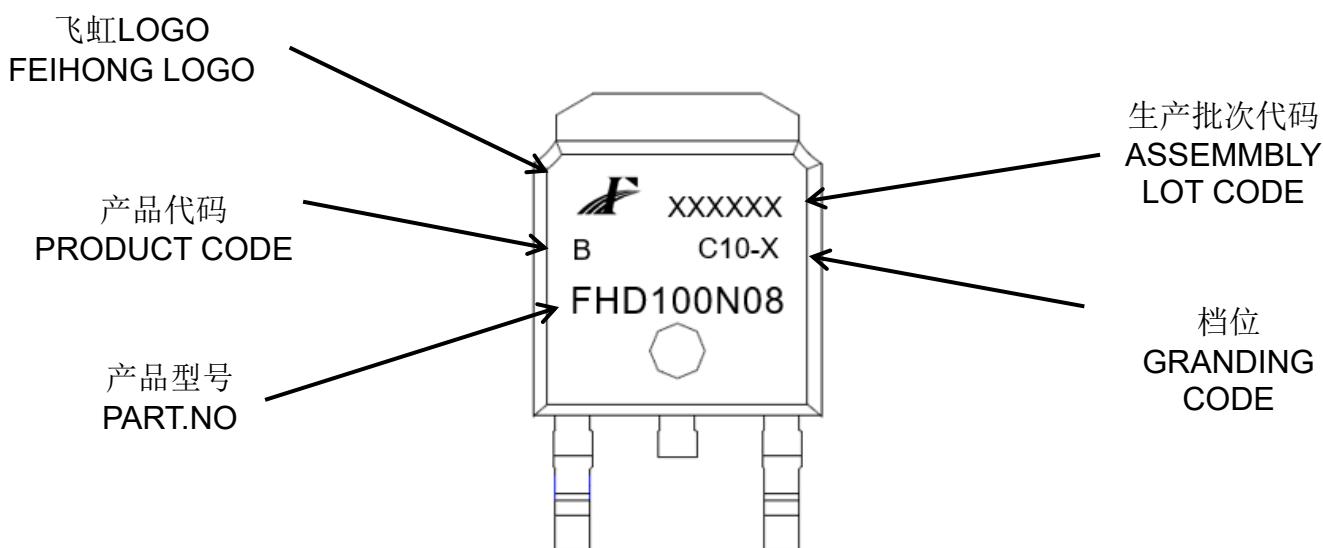


$$E_{AS} = \frac{1}{2} L I_{AS}^2$$

Fig. 15. Peak diode recovery dv/dt test circuit & waveform



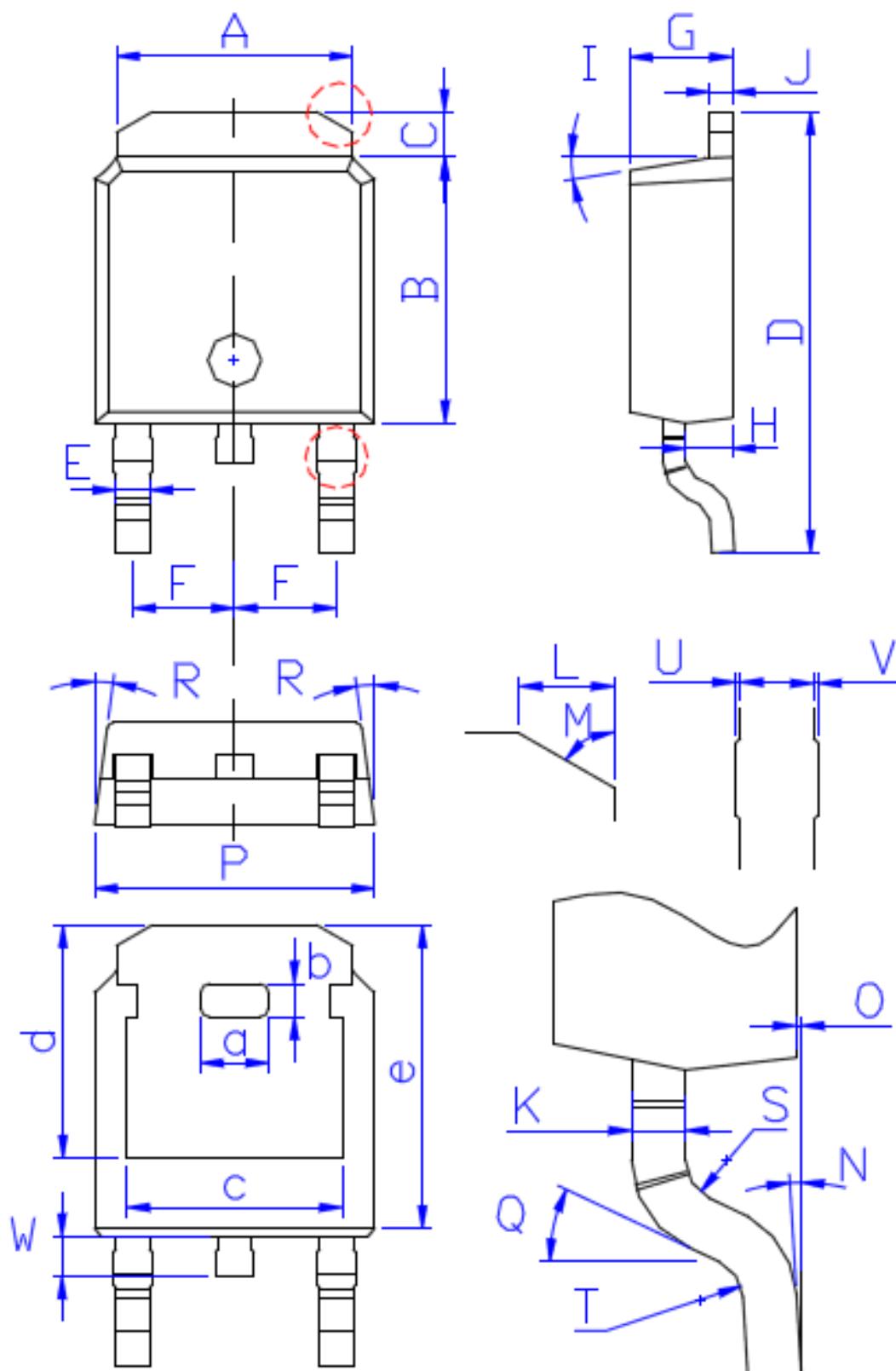
印记 Marking:



外形尺寸:

Package Dimension:

TO-252



DIM	MILLIMETERS
A	5.34±0.30
B	6.00±0.30
C	1.05±0.30
D	9.95±0.30
E	0.76±0.15
F	2.28±0.15
G	2.30±0.30
H	1.06±0.30
I	(4-10)°
J	0.51±0.15
K	0.52±0.15
L	0.80±0.30
M	60°
N	(0-10)°
O	0.05±0.05
P	6.60±0.30
Q	25°
R	(4-8.5)°
S	R0.40
T	R0.40
U	0.05±0.05
V	0.05±0.05
W	0.90±0.30
a	1.80±0.30
b	0.75±0.30
c	4.85±0.30
d	5.30±0.30
e	6.90±0.30

(Units: mm)