

IMW65R057M1H-VB Datasheet N-Channel 650V (D-S) SiC Power MOSFET

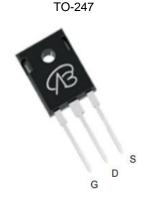
PRODUCT SUMMARY				
V _{DS} (V)	650			
R _{DS(on)} at 25 °C (Ω)	V _{GS} = 18 V	0.05		
Q _g (nC)	90			

FEATURES

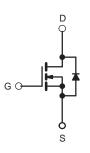
- Low figure-of-merit (FOM) Ron x Qa
- Low input capacitance (Ciss)
- Reduced switching and conduction losses
- Ultra low gate charge (Q_a)
- Avalanche energy rated (UIS)

APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- DC/DC converter



Top View



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER			SYMBOL	LIMIT	UNIT	
Drain-Source Voltage			V_{DS}	650	V	
Gate-Source Voltage			V_{GS}	-10 / +22		
Continuous Drain Current (T _J = 150 °C)	\/ ot 19 \/	$T_{\rm C} = 25 ^{\circ}{\rm C}$ $T_{\rm C} = 100 ^{\circ}{\rm C}$	-	40		
	V _{GS} at 18 V	T _C = 100 °C	I _D	32	Α	
Pulsed Drain Current ^a			I _{DM}	120	1	
Linear Derating Factor				2.1	W/°C	
Single Pulse Avalanche Energy ^b			E _{AS}	225	mJ	
Maximum Power Dissipation			P_{D}	187	W	
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C	
Drain-Source Voltage Slope	T _J = 125 °C		dV/dt	50	V/ns	
Reverse Diode dV/dt ^d			αν/αι	15	V/IIS	
Soldering Recommendations (Peak Temperature) c	for 10 s			260	°C	

- a. Repetitive rating; pulse width limited by maximum junction temperature. b. $V_{DD}=100$ V, starting $T_{J}=25$ °C, L = 0.5mH, $R_{g}=25$ Ω , $I_{AS}=30$ A.

- c. 1.6 mm from case. d. $I_{SD} \le I_D$, dl/dt = 100 A/ μ s, starting T_J = 25 °C.



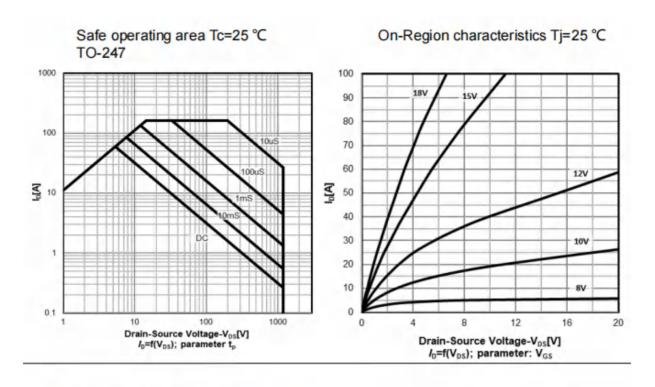
THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYP.	MAX.	UNIT		
Maximum Junction-to-Ambient	R _{thJA}	=	40	°C/W		
Maximum Junction-to-Case (Drain)	R _{thJC}	-	0.8	C/VV		

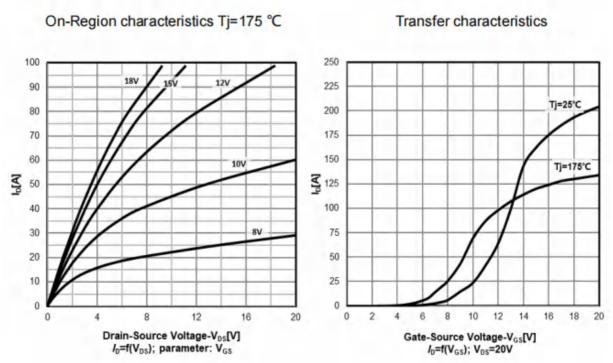
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static		*					•
Drain-Source Breakdown Voltage	V_{DS}	V _{GS} =	= 0 V, I _D = 1 mA	650	-	-	٧
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	Reference	e to 25 °C, I _D = 1 mA	-	0.70	-	V/°C
Gate-Source Threshold Voltage (N)	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}$, $I_D = 10 \text{ mA}$		-	5	V
	V _{GS} = +22 V		V _{GS} = +22 V	-	-	100	nA
Gate-Source Leakage	I_{GSS}	,	V _{GS} = -10 V		-	100	μA
	_	V _{DS} =	= 650V, V _{GS} = 0 V	-	10	-	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 650 V	/, V _{GS} = 0 V, T _J = 125 °C	-	-	100	μA
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = 18 V	I _D = 20A	-	0.05	-	Ω
Forward Transconductance	9fs	V _{DS}	_s = 0 V, I _D = 30 A	-	12	-	S
Dynamic		1					
Input Capacitance	C _{iss}		$V_{GS} = 0 \text{ V},$ $V_{DS} = 600 \text{ V},$ f = 1 MHz		2000	-	pF
Output Capacitance	Coss				175	-	
Reverse Transfer Capacitance	C _{rss}				9	-	
Effective Output Capacitance, Energy Related ^a	C _{o(er)}	- V _{DS} = 0 V to 400 V, V _{GS} = 0 V		-	156	-	
Effective Output Capacitance, Time Related ^b	C _{o(tr)}			-	268	-	
Total Gate Charge	Qg				70	-	nC
Gate-Source Charge	Q _{gs}	$V_{GS} = -5/18 \text{ V}$ $I_D = 20 \text{ A}, V_{DS} = 400 \text{ V}$		-	20	-	
Gate-Drain Charge	Q _{gd}			-	23	-	1
Turn-On Delay Time	t _{d(on)}	V_{DD} = 400 V, I_{D} = 20A, V_{GS} = -5/18 V , R_{g} = 2 Ω		-	12	15	
Rise Time	t _r			-	10	13	ns
Turn-Off Delay Time	t _{d(off)}			-	20	-	
Fall Time	t _f			-	10	-	
Gate Input Resistance	R_g	f = 1	f = 1 MHz, open drain		2.2	-	Ω
Drain-Source Body Diode Characteristic	s						
Continuous Source-Drain Diode Current	I _S	MOSFET symbol showing the integral reverse p - n junction diode		-	-	40	
Pulsed Diode Forward Current	I _{SM}			-	-	120	- A
Diode Forward Voltage	V _{SD}	T _J = 25 °C, I _S = 20 A, V _{GS} = 0		-	-	4.1	V
Reverse Recovery Time	t _{rr}	T _J = 25 °C, I _F = I _S = 20 A, dl/dt = 1000 A/µs, V _R = 400 V		-	20	-	ns
Reverse Recovery Charge	Q _{rr}			-	60	-	μC
Reverse Recovery Current	I _{RRM}				10	_	A

Notes

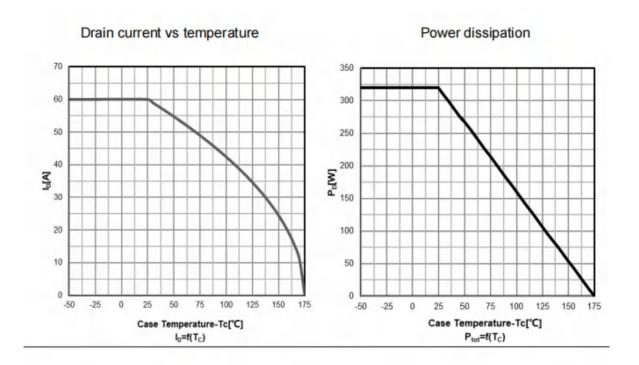
- a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 60 % V_{DSS} . b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 60 % V_{DSS} .

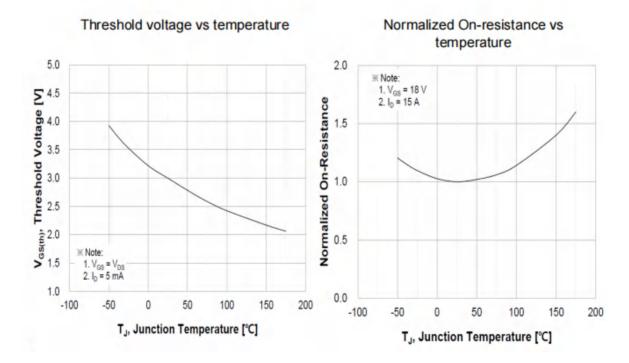




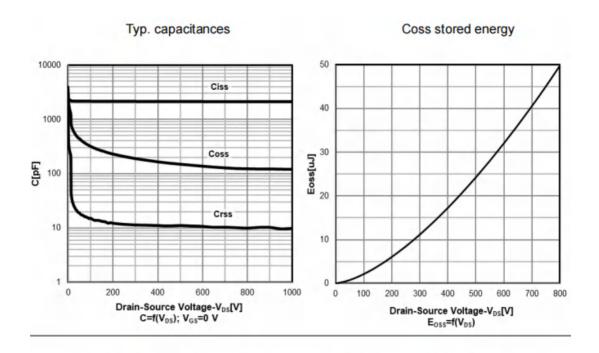


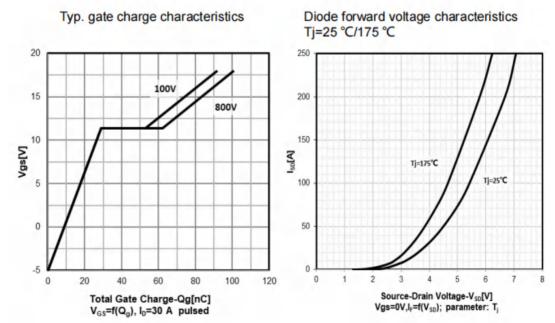








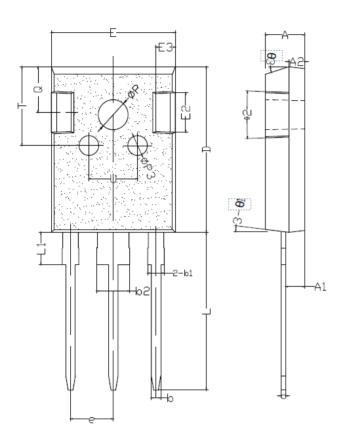


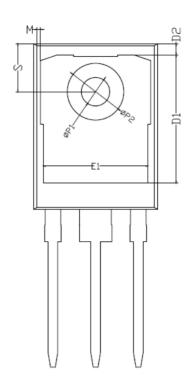


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TO-247 PACKAGE OUTLINE DIMENSIONS







SYMBOL	mm				
	MIN	NOM	MAX		
*A	4.90	5.00	5.10		
*A1	2.31	2.41	2.51		
A2	1.90	2.00	2.10		
*b	1.15	1.20	1.25		
*b1	1.95	2.10	2.25		
*b2	2.95	3.10	3.25		
*c	0.55	0.60	0.65		
*D	20.90	21.00	21.10		
D1	16.35	16.55	16.75		
D2	1.05	1.20	1.35		



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