

IGL65R055D2-VB Datasheet

650 V (D-S) GaN Enhancement mode transistor

PRODUCT SUMMARY

V_{DS} (V)	650	
$R_{DS(on)}$ at 25 °C (Ω)	$V_{GS} = 6\text{ V}$	0.110

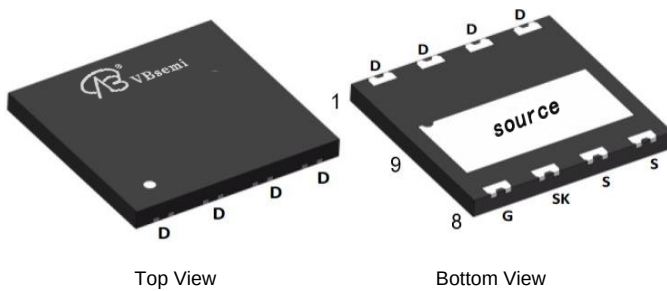
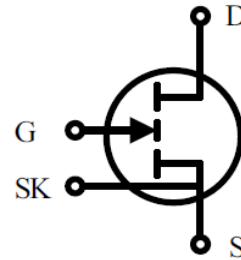
FEATURES

- Ultra High Switching Frequency
- Enhancement Mode Transistor
- No reverse-recovery charge
- Ultra low gate charge (Q_g)
- ESD Protection

APPLICATIONS

- Server and telecom power supplies
- Switch mode power supplies (SMPS)
- Power factor correction power supplies (PFC)
- Fast Battery Charging

DFN8*8



ABSOLUTE MAXIMUM RATINGS ($T_C = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)

PARAMETER			SYMBOL	LIMIT	UNIT
Drain-Source Voltage			$V_{DS,max}$	650	V
Gate-Source Voltage			V_{GS}	-1.4~+7	
Continuous Drain Current ($T_J = 150\text{ }^{\circ}\text{C}$)	V_{GS} at 6 V	$T_C = 25\text{ }^{\circ}\text{C}$	I_D	20	A
		$T_C = 125\text{ }^{\circ}\text{C}$		10	
Pulsed Drain Current ^a			I_{DM}	30	
Drain-Source Voltage transient ^b			$V_{DS,transient}$	800	V
Drain-Source Voltage Pulse ^c			$V_{DS,pulse}$	750	V
Maximum Power Dissipation			P_D	119	W
Operating Junction and Storage Temperature Range			T_J, T_{stg}	-55 to +150	$^{\circ}\text{C}$
Drain-Source Voltage Slope	$T_J = 125\text{ }^{\circ}\text{C}$		dV/dt	150	V/ns
Drain-Source Voltage Slope ^d	$T_J = 25\text{ }^{\circ}\text{C}$			200	
Soldering Recommendations (Peak Temperature) ^c	for 10 s			260	$^{\circ}\text{C}$

Notes

- Repetitive rating; pulse width limited by maximum junction temperature.
- Non-repetitive events, $t_{pulse} < 200\text{ }\mu\text{s}$.
- Repetitive pulse, $t_{pulse} < 100\text{ ns}$.
- $I_{SD} \leq I_D$, $dI/dt = 100\text{ A}/\mu\text{s}$, starting $T_J = 25\text{ }^{\circ}\text{C}$.

THERMAL RESISTANCE RATINGS

PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum Junction-to-Ambient	R_{thJA}	-	62	°C/W
Maximum Junction-to-Case (Drain)	R_{thJC}	-	1.05	

SPECIFICATIONS ($T_J = 25\text{ °C}$, unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Voltage	V_{DS}	$-55\text{ °C} \leq T_J \leq 150\text{ °C}$	-	-	650	V
V_{DS} Temperature Coefficient	$\Delta V_{DS}/T_J$	Reference to 25 °C , $I_D = 1\text{ mA}$	-	0.60	-	V/°C
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 17.2\text{ mA}$	1.2	-	2.5	V
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = 6\text{ V}$	-	50	-	μA
		$V_{DS} = 0\text{ V}$, $V_{GS} = 7\text{ V}$	-	-	1	mA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 650\text{ V}$, $V_{GS} = 0\text{ V}$	-	1	25	μA
		$V_{DS} = 650\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 150\text{ °C}$	-	10	-	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 6\text{ V}$, $I_D = 3.5\text{ A}$	-	0.110	-	Ω
Gate Resistance	R_g	$F = 5\text{ MHz}$, Open drain	-	3.5	-	Ω
Dynamic						
Input Capacitance	C_{iss}	$V_{GS} = 0\text{ V}$, $V_{DS} = 400\text{ V}$, $f = 100\text{ KHz}$	-	120	-	pF
Output Capacitance	C_{oss}		-	37	-	
Reverse Transfer Capacitance	C_{rss}		-	0.4	-	
Effective Output Capacitance, Energy Related ^a	$C_{o(er)}$	$V_{DS} = 0\text{ V to } 400\text{ V}$, $V_{GS} = 0\text{ V}$	-	60	-	pF
Effective Output Capacitance, Time Related ^b	$C_{o(tr)}$		-	80	-	
Total Gate Charge	Q_g	$V_{GS} = 6\text{ V}$, $I_D = 5\text{ A}$, $V_{DS} = 400\text{ V}$	-	3.3	-	nC
Gate-Source Charge	Q_{gs}		-	0.3	-	
Gate-Drain Charge	Q_{gd}		-	1.2	-	
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 400\text{ V}$, $I_D = 5\text{ A}$, $V_{GS} = 6\text{ V}$, $R_g = 3\text{ }\Omega$	-	3	-	ns
Rise Time	t_r		-	4	-	
Turn-Off Delay Time	$t_{d(off)}$		-	5	-	
Fall Time	t_f		-	4	-	
Output Charge	Q_{oss}	$V_{DS} = 0\text{ V to } 400\text{ V}$, $V_{GS} = 0\text{ V}$	-	33.5	-	pF
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I_S	$V_{GS} = 6\text{ V}$	-	-	10	A
Pulsed Diode Forward Current	I_{SM}		-	-	30	
Diode Forward Voltage	V_{SD}	$T_J = 25\text{ °C}$, $I_S = 3\text{ A}$, $V_{GS} = 0\text{ V}$	-	2.4	-	V
Reverse Recovery Time	t_{rr}	$T_J = 25\text{ °C}$, $I_F = I_S = 5\text{ A}$, $dI/dt = 100\text{ A}/\mu\text{s}$, $V_R = 400\text{ V}$	-	0	-	ns
Reverse Recovery Charge	Q_{rr}		-	0	-	μC

Notes

- a. $C_{oss(er)}$ is a fixed capacitance that gives the same energy as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .
b. $C_{oss(tr)}$ is a fixed capacitance that gives the same charging time as C_{oss} while V_{DS} is rising from 0 % to 80 % V_{DSS} .

TYPICAL CHARACTERISTICS($T_c=25^{\circ}\text{C}$, unless otherwise noted)

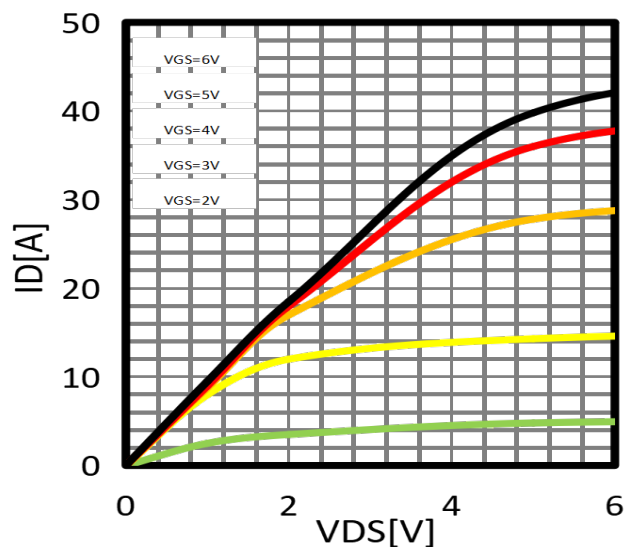


Fig.1: Output Characteristics

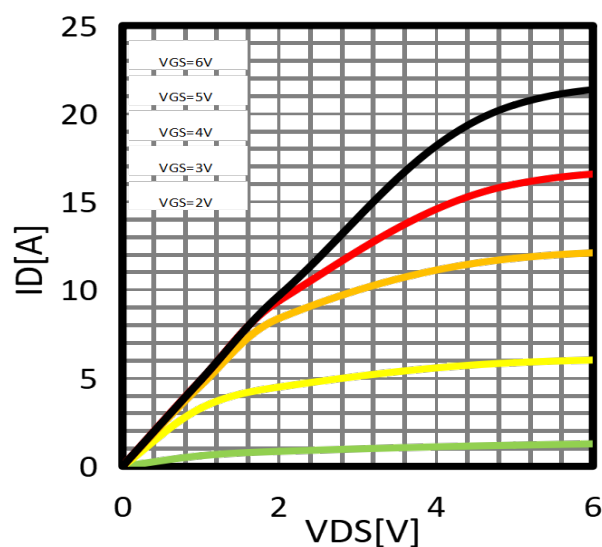


Fig.2: Output Characteristics(125°C)

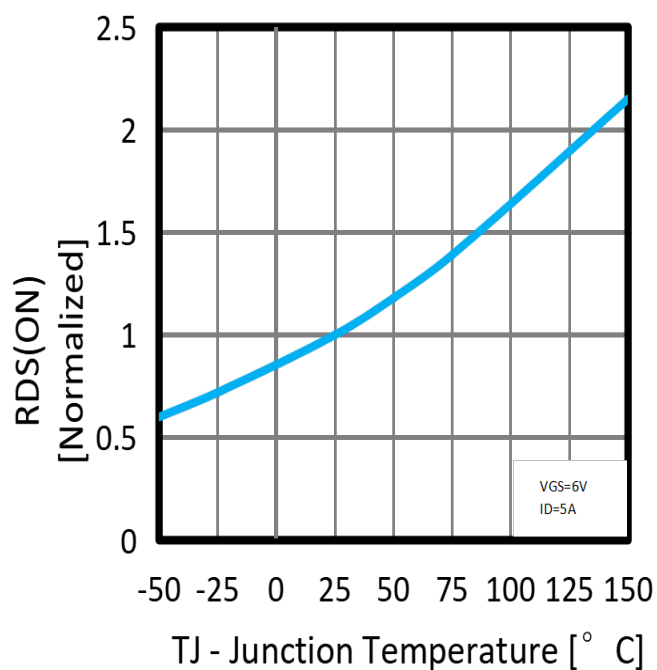


Fig.3: Drain-source on-state resistance

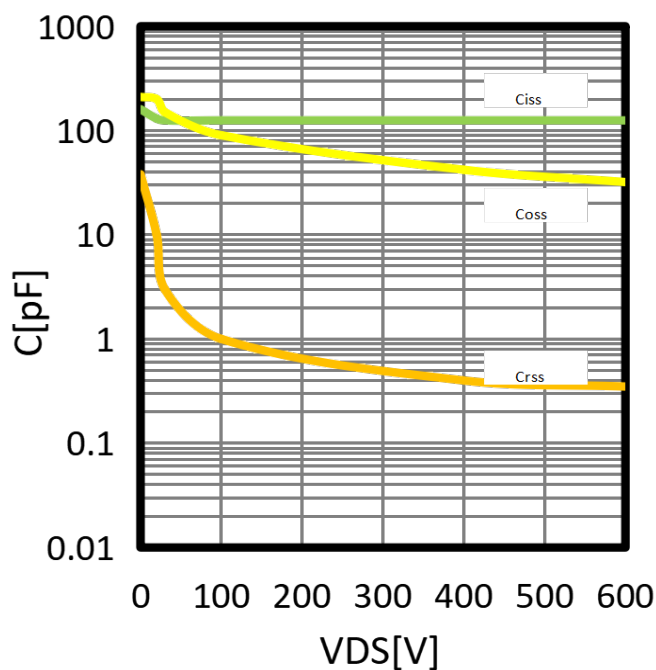


Fig.4: Typical Capacitance

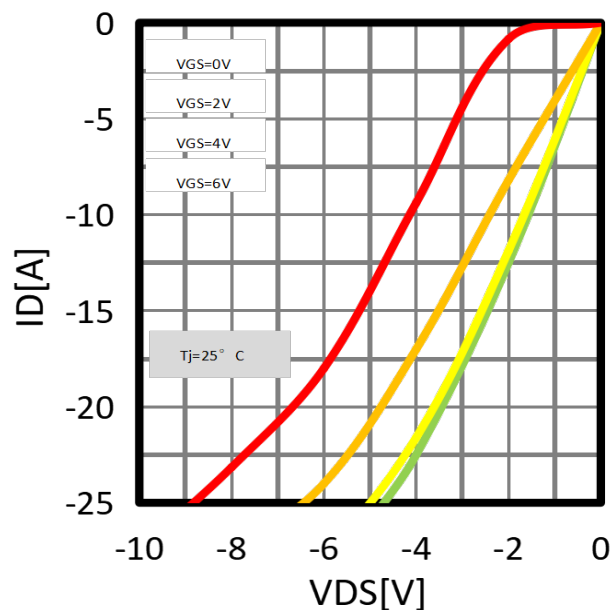


Fig.5:Typ.channel reverse Characteristics

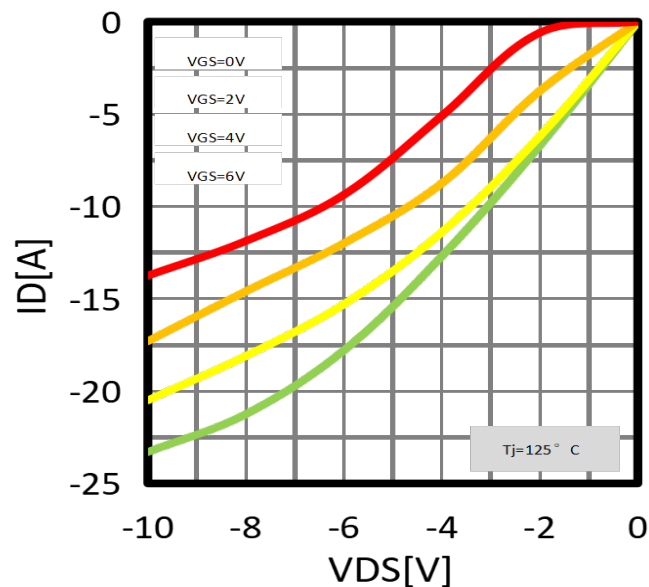


Fig.6:Typ.channel reverse Characteristics

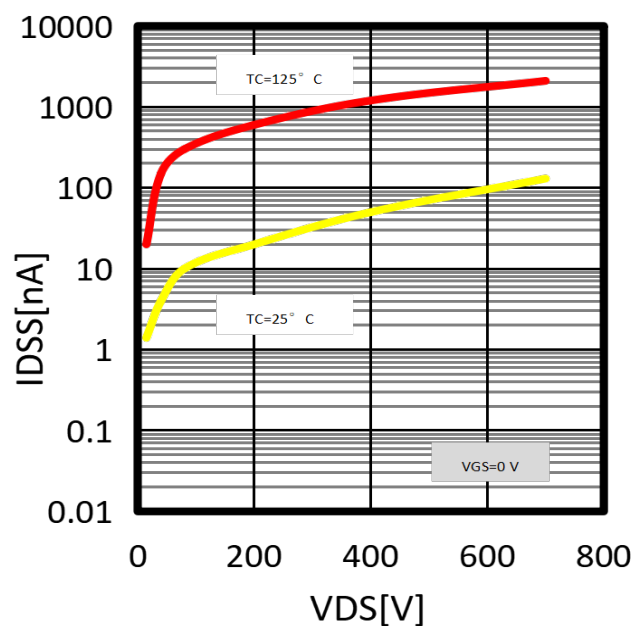


Fig.7:Drain-to-Source Leakage Characteristics

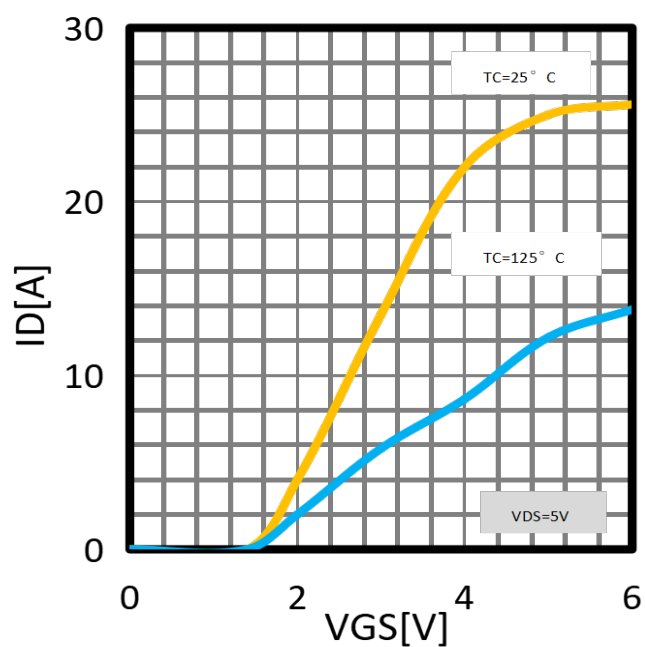


Fig.8:Transfer Characteristics

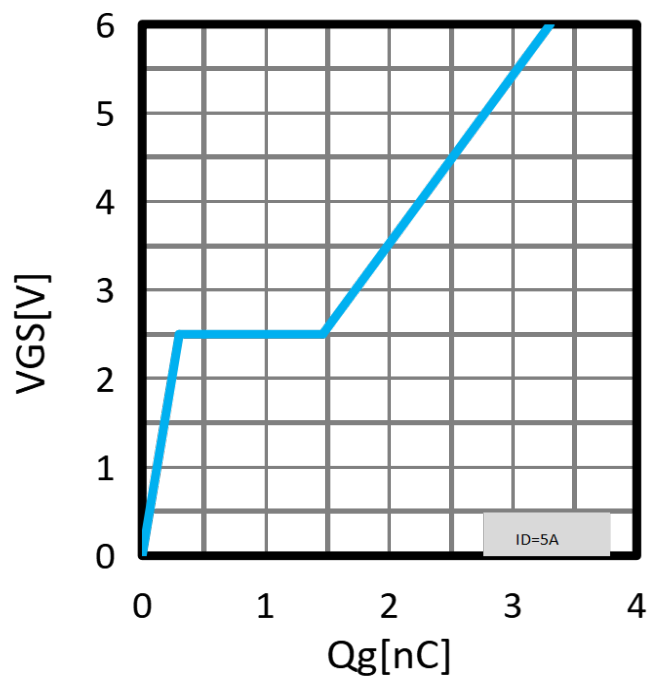


Fig.9:Gate Charge

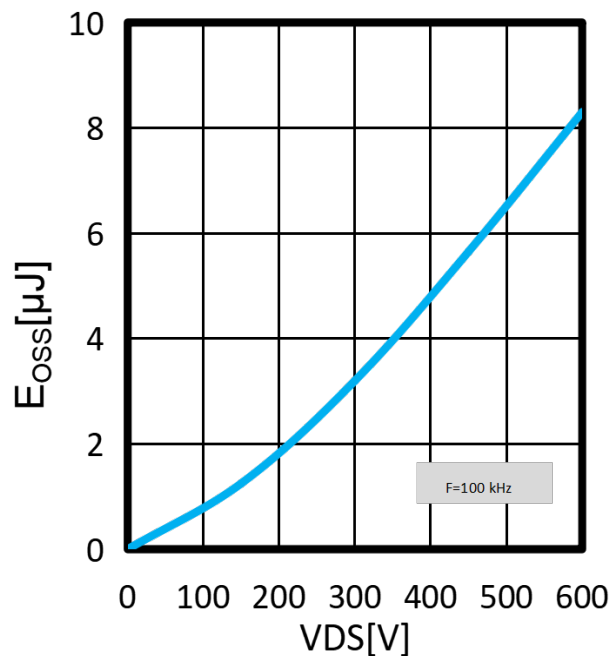


Fig.10:Typical Coss Stored Energy

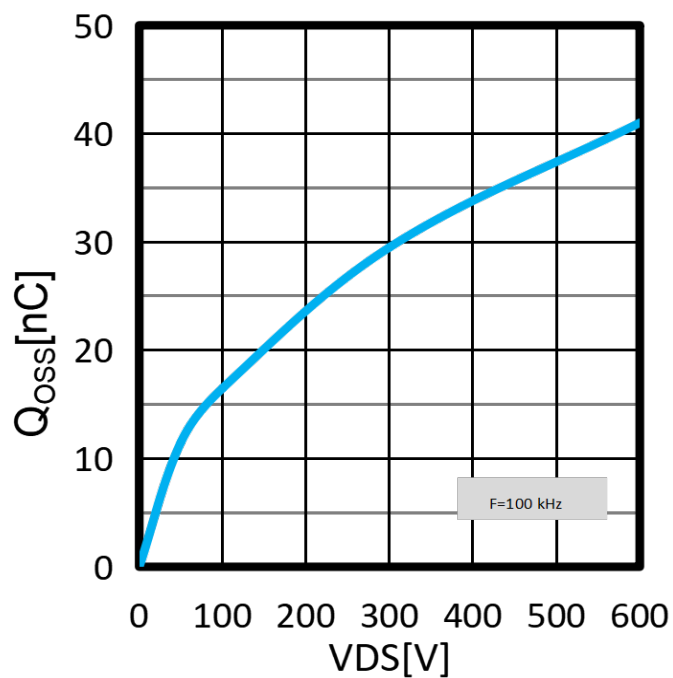


Fig.11:Typical Output Charge

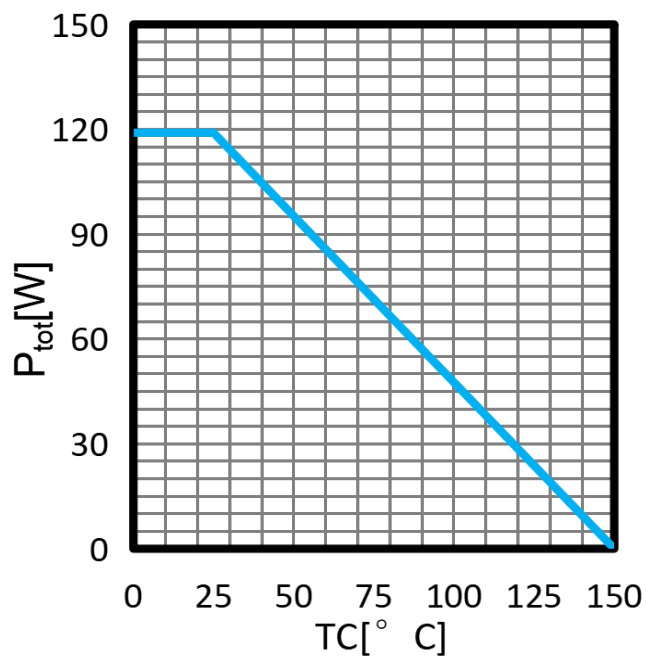


Fig.12:Power Dissipation

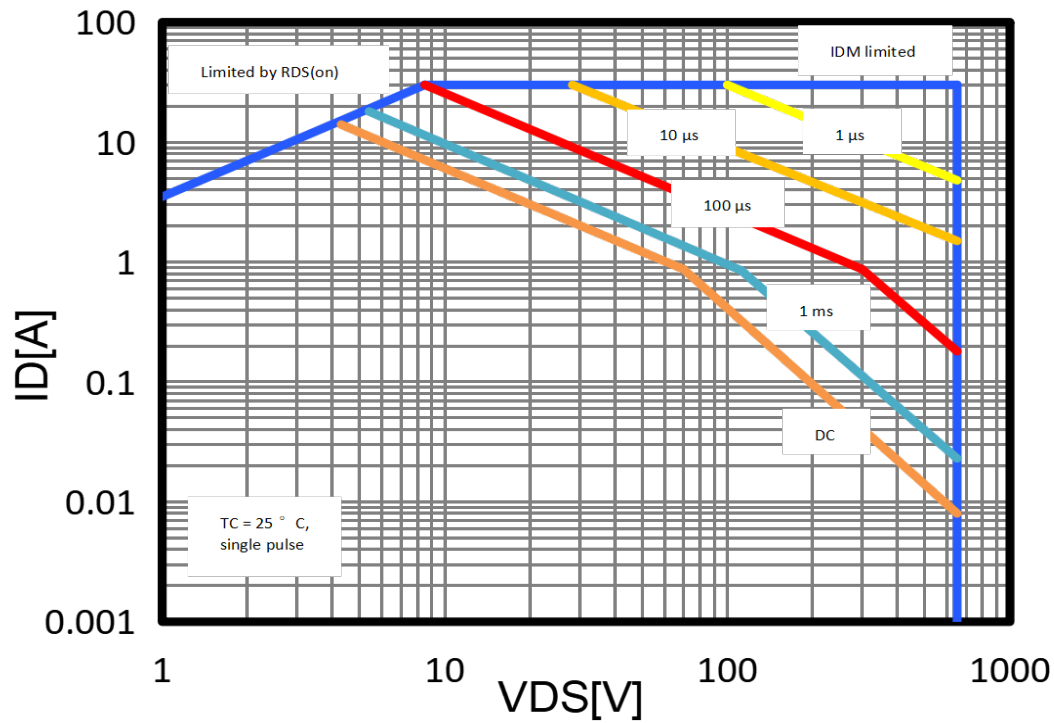


Fig.13:Safe Operating Area

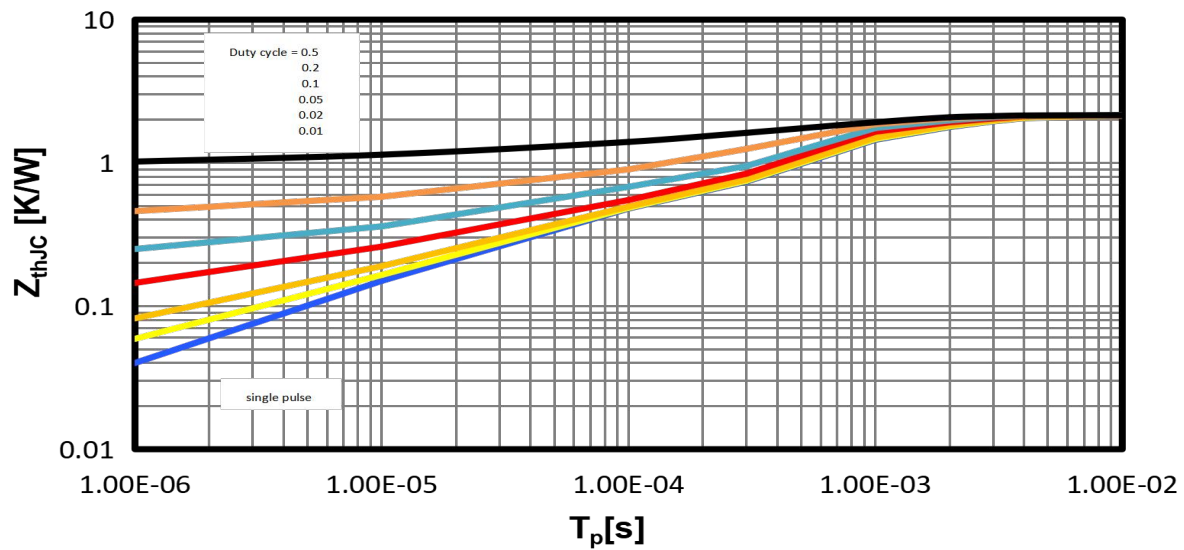
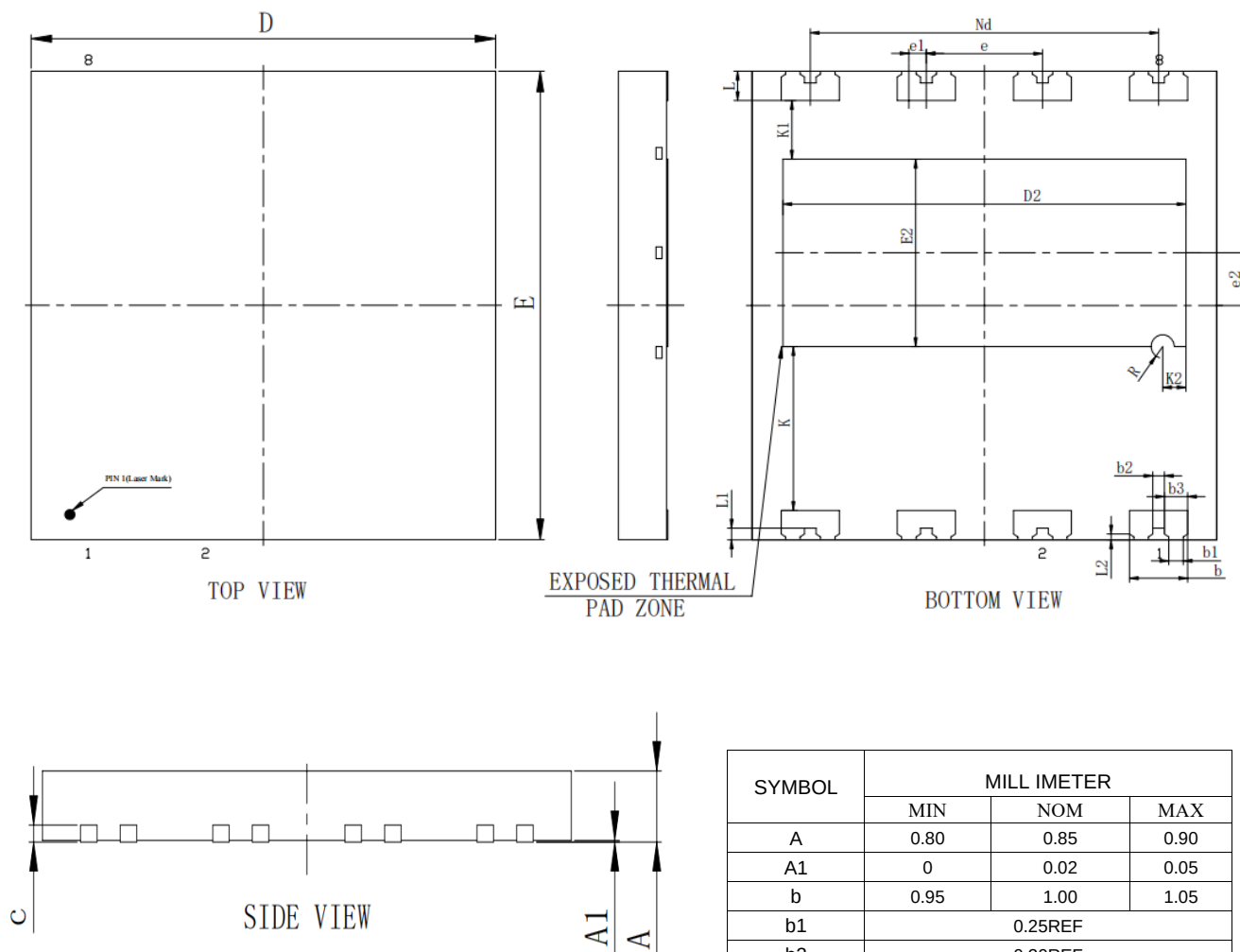


Fig.14:Transient Thermal Impedance

DFN 8x8 Package Information



SYMBOL	MILL IMETER		
	MIN	NOM	MAX
A	0.80	0.85	0.90
A1	0	0.02	0.05
b	0.95	1.00	1.05
b1	0.25REF		
b2	0.20REF		
b3	0.35	0.40	0.45
C	0.203REF		
D	7.90	8.00	8.10
D2	6.84	6.94	7.04
Nd	6.00BSC		
e	2.00BSC		
e1	0.30BSC		
e2	0.90BSC		
E	7.90	8.00	8.10
E2	3.10	3.20	3.30
L	0.45	0.50	0.55
L1	0.20REF		
L2	0.10REF		
K	2.80REF		
K1	1.00REF		
K2	0.40REF		
R	0.15	0.20	0.25

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