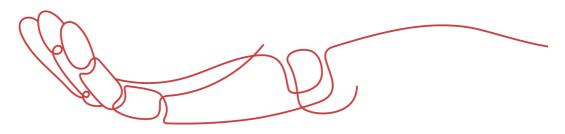


PRODUCT DATA SHEET



To learn more about JGSEMI, please visit our website at



Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



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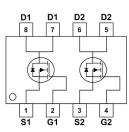
Dual N-Ch 20V Fast Switching MOSFETs

Product Summary

BVDSS	RDSON	ID
20V	20mΩ	6.0A

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent Cdv/dt effect decline
- ★ Advanced high cell density Trench technology





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units		
V _{DS}	Drain-Source Voltage	20			
V _{GS}	Gate-Source Voltage	±12			
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	ntinuous Drain Current, V _{GS} @ 4.5V ¹ 6.0			
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 4.5V ¹	4.8	A		
I _{DM}	Pulsed Drain Current ²	25	A		
P _D @T _A =25°C	Total Power Dissipation ³	n ³ 1.65			
Tstg	Storage Temperature Range	-55 to 150			
TJ	Operating Junction Temperature Range	-55 to 150	°C		

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹		78	°C/W
Rejc	Thermal Resistance Junction-Case ¹		-	°C/W



Electrical Characteristics (T_J=25 °C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
Off Charac	cteristic			1	I	1	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250µA	20	-	-	V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =20V, V _{GS} =0V,	-	-	1.0	μA	
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} =±12V	-	-	±100	nA	
On Charac	cteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250µA	0.4	0.7	1.0	V	
P	Static Drain-Source on-Resistance	V _{GS} =4.5V, I _D =6A	-	20	28		
$R_{DS(on)}$	note2	V _{GS} =2.5V, I _D =5A	-	25.5	38	mΩ	
Dynamic O	Characteristics						
Ciss	Input Capacitance		-	358	-	pF	
Coss	Output Capacitance	- V _{DS} =10V, V _{GS} =0V,	-	69.3	-	pF	
Crss	Reverse Transfer Capacitance	f=1.0MHz	-	58.5	-	pF	
Qg	Total Gate Charge		-	5.6	-	nC	
Q_gs	Gate-Source Charge	→ V _{DS} =10V, I _D =3A, → V _{GS} =4.5V	-	0.8	-	nC	
Q_gd	Gate-Drain("Miller") Charge	VGS-4.5V	-	1	-	nC	
Switching	Characteristics						
t _{d(on)}	Turn-on Delay Time	N/ 40)/	-	16	-	ns	
tr	Turn-on Rise Time	- V _{DS} =10V,	-	51	-	ns	
t _{d(off)}	Turn-off Delay Time	 I_D=6A, R_{GEN}=3Ω, V_{GS}=4.5V 	-	21	-	ns	
t _f	Turn-off Fall Time	- V _{GS} -4.5V	-	19	-	ns	
Drain-Sou	rce Diode Characteristics and Maxir	num Ratings					
ls	Maximum Continuous Drain to Source Diode Forward Current		-	-	6	А	
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	24	A	
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} =0V, I _S =6A	-	-	1.2	V	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

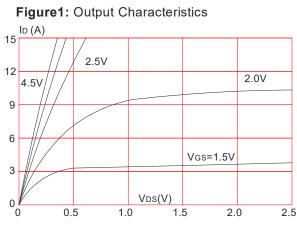
2. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%

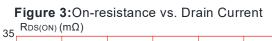
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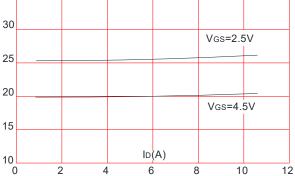


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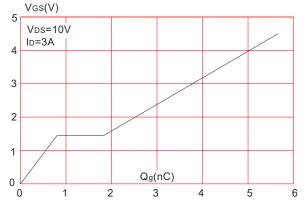
Typical Performance Characteristics

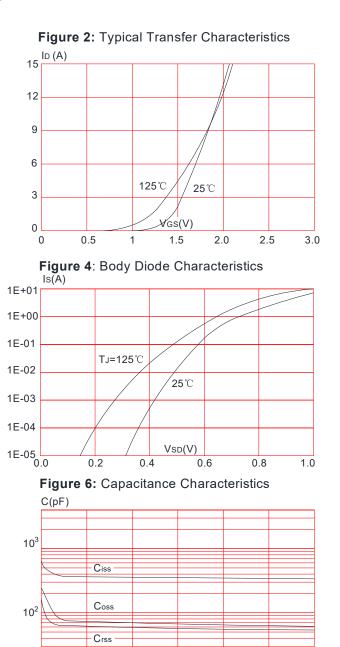












VDS(V)

15

20

25

30

10

10¹

0

5



Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

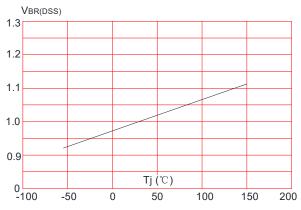


Figure 9: Maximum Safe Operating Area

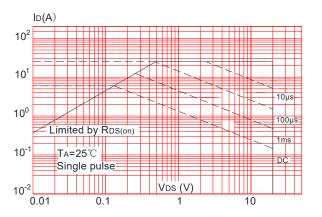


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

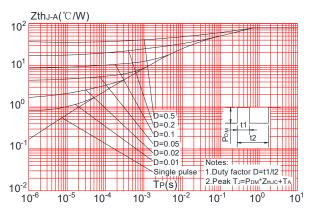


Figure 8: Normalized on Resistance vs. Junction Temperature

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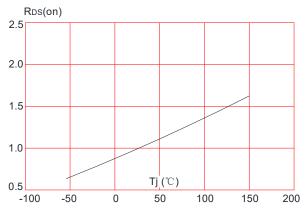
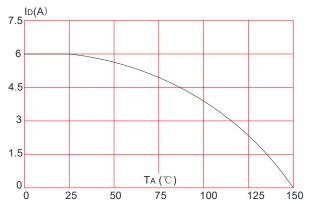
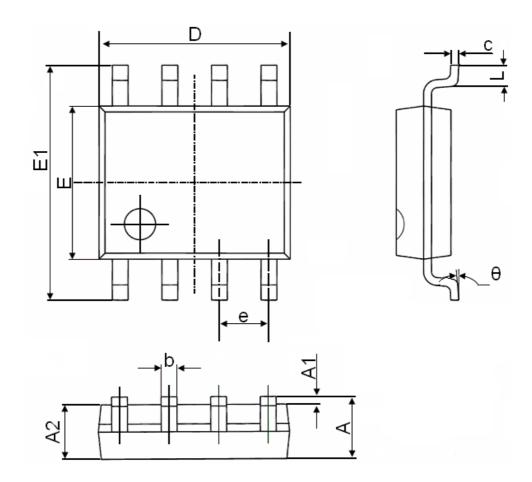


Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature





Package Mechanical Data-SOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	1.270(BSC)		(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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