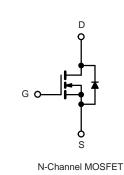


AP9980GJ-HF-VB Datasheet

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)		
100	0.036 at V _{GS} = 10 V	35		

TO-251



FEATURES

- TrenchFET[®] Power MOSFET
- 175 °C Junction Temperature
- PWM Optimized
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

• Primary Side Switch

ABSOLUTE MAXIMUM RATINGS ($T_A =$	25 °C, unless othe	rwise noted)			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	100	V	
Gate-Source Voltage	V _{GS}	± 20	V		
Continuous Drain Current $(T_1 = 175 \text{ °C})^b$	T _C = 25 °C T _C = 125 °C	- I _D	35		
Continuous Drain Current $(T_J = 175 \text{ °C})^2$	T _C = 125 °C		30		
Pulsed Drain Current	I _{DM}	110	А		
Continuous Source Current (Diode Conduction)	۱ _S	23			
Avalanche Current	I _{AS}	3			
Single Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	18	mJ	
Maximum Power Dissipation	T _C = 25 °C	PD	96 ^b	w	
	T _A = 25 °C	'D	3 ^a		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	R _{thJA}	15	18	°C/W	
Junction-to-Ambient ^a	Steady State		40	50		
Junction-to-Case (Drain)		R _{thJC}	0.85	1.1		

Notes:

a. Surface mounted on 1" x 1" FR4 board.

b. See SOA curve for voltage derating.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ. ^a	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$	100			V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	2		4	v	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = \pm 20 V$			± 100	nA	
		V _{DS} = 100 V, V _{GS} = 0 V			1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$			50	μA	
		$V_{DS} = 80 \text{ V}$, $V_{GS} = 0 \text{ V}$, $T_{J} = 175 \text{ °C}$			250		
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	35			А	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3 \text{ A}$		0.036		Ω	
	D	V _{GS} = 10 V, I _D = 3 A, T _J = 125 °C		0.040			
Drain-Source On-State Resistance ^b	R _{DS(on)}	V _{GS} = 10 V, I _D = 3 A, T _J = 175 °C		0.050			
		V _{GS} = 6 V, I _D = 3 A		0.039			
Forward Transconductanceb	9 _{fs}	V _{DS} = 15 V, I _D = 3 A		35		S	
Dynamic ^a							
Input Capacitance	C _{iss}			4000		pF	
Output Capacitance	C _{oss}	V_{GS} = 0 V, V_{DS} = 25 V, F = 1 MHz		500			
Reverse Transfer Capacitance	C _{rss}			180			
Total Gate Charge ^c	Qg			34			
Gate-Source Charge ^c	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 3 \text{ A}$		8		nC	
Gate-Drain Charge ^c	Q _{gd}			12		1	
Gate Resistance	R _g		0.5		2.9	Ω	
Turn-On Delay Time ^c	t _{d(on)}			15	25	ns	
Rise Time ^c	t _r	$V_{DD} = 50 \text{ V}, \text{ R}_{L} = 5.2 \Omega$		50	75		
Turn-Off Delay Time ^c	t _{d(off)}	$I_D \cong$ 3 A, V_{GEN} = 10 V, R_g = 2.5 Ω		30	45		
Fall Time ^c	t _f			60	90		
Source-Drain Diode Ratings and Char	acteristics (1	r _C = 25 °C)					
Pulsed Current	I _{SM}				5	А	
Diode Forward Voltage ^b	V _{SD}	I _F = 3 A, V _{GS} = 0 V		0.9	1.5	V	
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 3 A, dl/dt = 100 A/μs		180	250	ns	

Notes:

a. Guaranteed by design, not subject to production testing.

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

c. Independent of operating temperature.

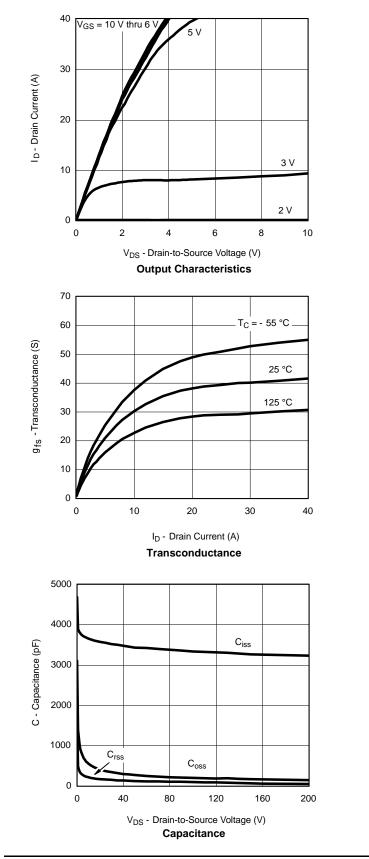
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

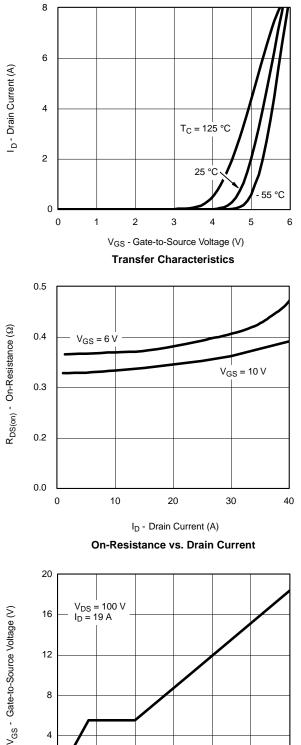
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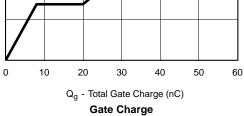
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







4

0

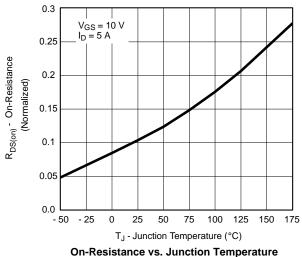


T_J = 25 °C

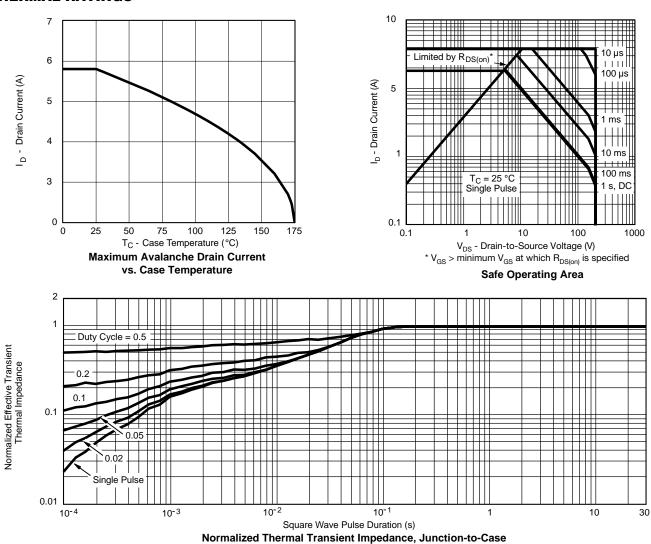
0.9

1.2

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







100

10

1

0

0.3

T_J = 150 °C

0.6

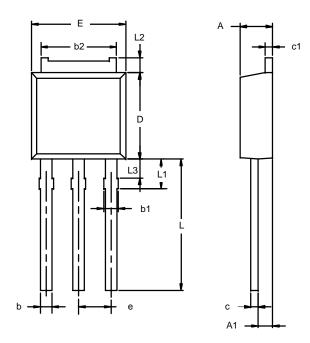
Source-Drain Diode Forward Voltage

V_{SD} - Source-to-Drain Voltage (V)

I_S - Source Current (A)



TO-251AA (DPAK)



Note: Dimension L3 is for reference only.

	MILLIN	IETERS	INC	HES	
Dim	Min	Max	Min	Max	
Α	2.21	2.38	0.087	0.094	
A1	0.89	1.14	0.035	0.045	
b	0.71	0.89	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.43	0.206	0.214	
С	0.46	0.58	0.018	0.023	
c1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.48	6.73	0.255	0.265	
е	2.28	BSC	0.090 BSC		
L	8.89	9.53	0.350	0.375	
L1	1.91	2.28	0.075	0.090	
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.045	0.060	
ECN: S-03946—Rev. E, 09-Jul-01 DWG: 5346					



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