

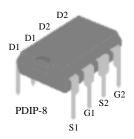
AP9973GD-VB Datasheet Dual N-Channel 60 V (D-S) MOSFET

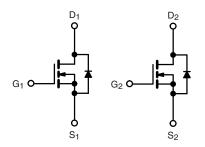
PRODUCT SUMMARY				
V _{DS} (V)	60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.033			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.045			
I _D (A) per leg	7			
Configuration	Dual			

FEATURES

- TrenchFET® power MOSFET
- \bullet 100 % R_g and UIS tested







N-Channel MOSFET N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage	Drain-Source Voltage		60	V	
Gate-Source Voltage		V_{GS}	± 20		
Continuous Drain Current	T _C = 25 °C	1	7		
Continuous Drain Current	T _C = 125 °C	Ι _D	4		
Continuous Source Current (Diode Conduction) a		I _S	3.6	Α	
Pulsed Drain Current ^b		I _{DM}	28		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	18		
Single Pulse Avalanche Energy	L = 0.1 IIII1	E _{AS}	16.2	mJ	
Maximum Power Dissipation ^b	T _C = 25 °C	D	4	W	
	T _C = 125 °C	P_{D}	1.3	۷V	
Operating Junction and Storage Temperature Ra	ange	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mount ^c	R_{thJA}	110	°C/W	
Junction-to-Foot (Drain)		R_{thJF}	34	C/VV	

Notes

- a. Package limited.
- b. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- c. When mounted on 1" square PCB (FR4 material).



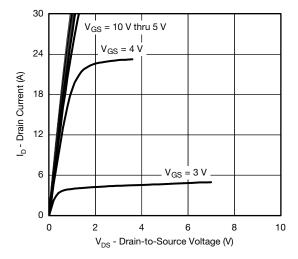
SPECIFICATIONS ($T_C = 25 ^{\circ}C$, PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static						1 0		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		60	_	-		
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		1.5	2.0	2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
Zero Gate Voltage Drain Current		V _{GS} = 0 V	V _{DS} = 60 V	-	-	1		
	I _{DSS}	V _{GS} = 0 V	V _{DS} = 60 V, T _J = 125 °C	ı	-	50	μΑ	
		$V_{GS} = 0 V$	V _{DS} = 60 V, T _J = 175 °C	-	-	150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	20	-	-	Α	
		V _{GS} = 10 V	I _D = 4.5 A	ı	0.033	-	Ω	
Drain-Source On-State Resistance a	R _{DS(on)}	V _{GS} = 10 V	I _D = 4.5 A, T _J = 125 °C	1	0.066	-		
2.4 664.66 6 6.4.6	1 103(011)	V _{GS} = 10 V	$I_D = 4.5 \text{ A}, T_J = 175 ^{\circ}\text{C}$	-	0.081	-		
		$V_{GS} = 4.5 \text{ V}$	I _D = 4 A	-	0.045	-		
Forward Transconductance f	9fs	V _{DS} :	= 15 V, I _D = 4.5 A	-	15	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{DS} = 25 V, f = 1 MHz	ı	600	750	pF	
Output Capacitance	Coss	$V_{GS} = 0 V$		-	110	140		
Reverse Transfer Capacitance	C _{rss}			-	50	62		
Total Gate Charge ^c	Qg		V _{DS} = 30 V, I _D = 5.3 A	-	11.7	18	nC	
Gate-Source Charge ^c	Q _{gs}	V _{GS} = 10 V		-	1.8	2.7		
Gate-Drain Charge ^c	Q _{gd}	1		-	2.8	4.2		
Gate Resistance	R _g	f = 1 MHz		1.3	-	6	Ω	
Turn-On Delay Time ^c	t _{d(on)}	$V_{DD}=30~V,~R_L=6.8~\Omega$ $I_D\cong4.4~A,~V_{GEN}=10~V,~R_g=1~\Omega$		-	7	11		
Rise Time ^c	t _r			-	3.3	5	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	22.4	33.5		
Fall Time ^c	t _f			-	2.1	3.2		
Source-Drain Diode Ratings and Chara	acteristics ^b					•	•	
Pulsed Current ^a	I _{SM}			-	-	28	Α	
Forward Voltage	V_{SD}	I _F = 2 A, V _{GS} = 0 V		-	0.75	1.1	٧	

Notes

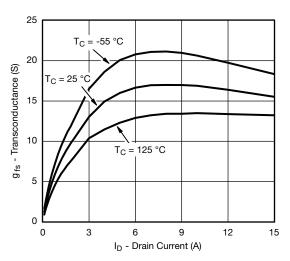
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %. b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.



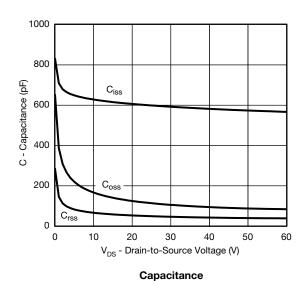
TYPICAL CHARACTERISTICS ($T_A = 25 \, ^{\circ}\text{C}$, unless otherwise noted)

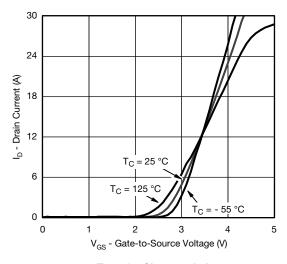


Output Characteristics

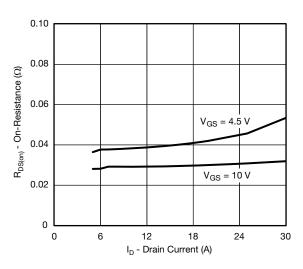


Transconductance

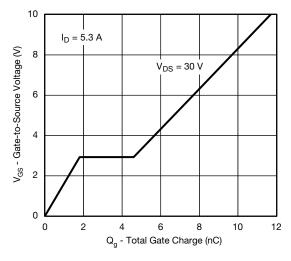




Transfer Characteristics



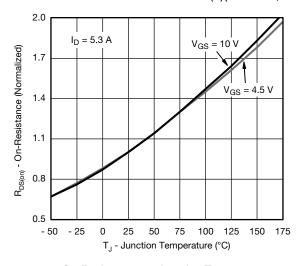
On-Resistance vs. Drain Current



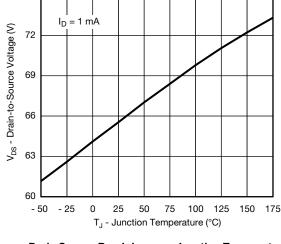
Gate Charge



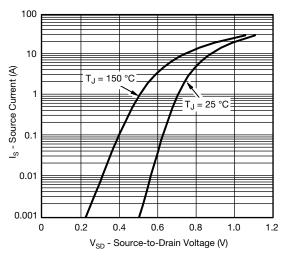
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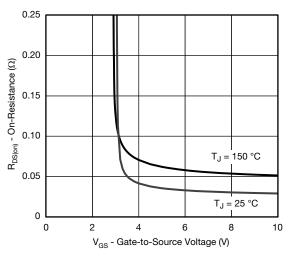
On-Resistance vs. Junction Temperature



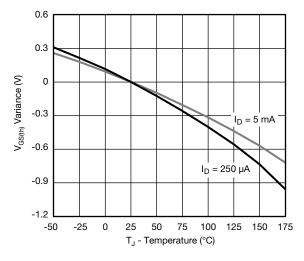
Drain Source Breakdown vs. Junction Temperature



Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

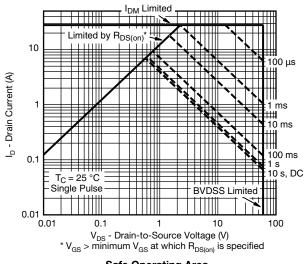


Threshold Voltage

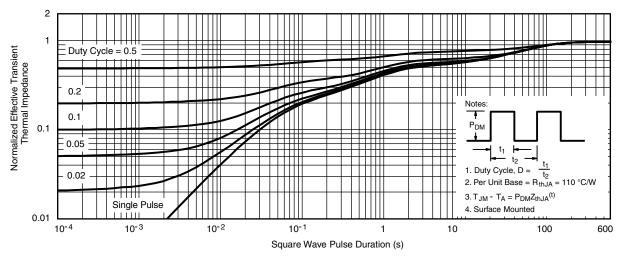


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THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



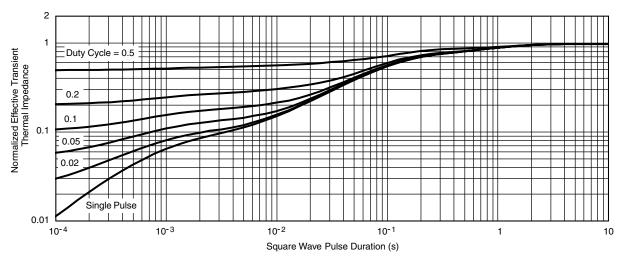
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



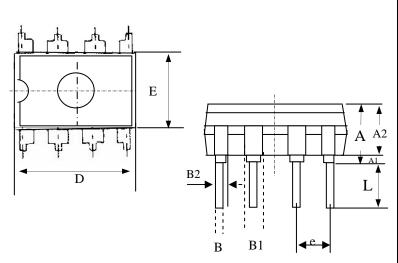
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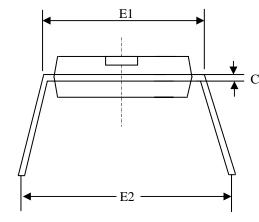
Normalized Thermal Transient Impedance, Junction-to-Foot



Package Outline: PDIP-8



SYMBOLS	Millimeters			
	MIN	MAX		
A	3.60	4.50	5.40	
A1	0.38			
A2	2.90	3.95	5.00	
В	0.36	0.46	0.56	
B1	1.10	1.45	1.80	
B2	0.76	0.98	1.20	
C	0.20	0.28	0.36	
D	9.00	9.60	10.20	
E	6.10	6.65	7.20	
E1	7.62	7.94	8.26	
E2	8.30	9.65	11.00	
e	2.540 BSC			
L	3.18			



- 1.All Dimensions Are in Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.



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