

## B70NF03L-VB TO262 Datasheet N-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A) <sup>a, e</sup>	Q <sub>g</sub> (Typ)			
30	0.0034 at V <sub>GS</sub> = 10 V	90	82 nC			
30	0.0070 at V <sub>GS</sub> = 4.5 V	70	02 IIC			

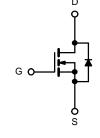
#### **FEATURES**

- TrenchFET® Power MOSFET
- 100 %  $\rm R_{\rm g}$  and UIS Tested Compliant to RoHS Directive 2011/65/EU









Top View

N-Channel MOSFET

#### **APPLICATIONS**

- OR-ing
- Server
- DC/DC

Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		
Gate-Source Voltage		V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C		90 <sup>a, e</sup>		
Continuous Drain Current (T <sub>.1</sub> = 175 °C)	T <sub>C</sub> = 70 °C		65 <sup>e</sup>		
Continuous Diam Current (1 j = 173 C)	T <sub>A</sub> = 25 °C	I <sub>D</sub> _	28.8 <sup>b, c</sup>	A	
	T <sub>A</sub> = 70 °C		27 <sup>b, c</sup>		
Pulsed Drain Current	I <sub>DM</sub>	300			
alanche Current Pulse		I <sub>AS</sub>	36		
Single Pulse Avalanche Energy	L = 0.1 mH	E <sub>AS</sub>	104	mJ	
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	1	90 <sup>a, e</sup>	Δ.	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	3.13 <sup>b, c</sup>	A	
	T <sub>C</sub> = 25 °C		250 <sup>a</sup>		
	T <sub>C</sub> = 70 °C		175	14/	
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub> —	3.75 <sup>b, c</sup>	W	
	T <sub>A</sub> = 70 °C		2.63 <sup>b, c</sup>		
Operating Junction and Storage Temperature Ra	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 175	°C		

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Тур.	Max.	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 sec	R <sub>thJA</sub>	32	40	°C/W	
Maximum Junction-to-Case	Steady State	R <sub>thJC</sub>	0.5	0.6	C/VV	

#### Notes:

- a. Based on T<sub>C</sub> = 25 °C.
  b. Surface mounted on 1" x 1" FR4 board.

- b. Striate informed on 1 X 1 114 board.
  c. t = 10 sec.
  d. Maximum under steady state conditions is 90 °C/W.
  e. Calculated based on maximum junction temperature. Package limitation current is 90 A.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			L				
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 250 A		35			
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_D = 250  \mu A$		- 7.5		mV/°C	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1.5		2.5	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oata Valtana Basis Oursest		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	90			Α	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 20 A		0.0034			
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		0.0070		Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 28.8 A		160		S	
Dynamic <sup>b</sup>						•	
Input Capacitance	C <sub>iss</sub>			3500			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1725		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			970			
Total Gate Charge	0	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 28.8 \text{ A}$		171	257		
	$Q_g$			81.5	123	50	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 28.8 \text{ A}$		34		nC	
Gate-Drain Charge	Q <sub>gd</sub>			29			
Gate Resistance	R <sub>g</sub>	f = 1 MHz		1.4	2.1	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			18	27		
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.625 $\Omega$		11	17	=	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 24 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		70	105		
Fall Time	t <sub>f</sub>			10	15		
Turn-On Delay Time	t <sub>d(on)</sub>			55	83	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 0.67 $\Omega$		180	270		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong 22.5$ A, $V_{GEN}$ = 4.5 V, $R_g$ = 1 $\Omega$		55	83		
Fall Time	t <sub>f</sub>			12	18	1	
<b>Drain-Source Body Diode Characteristic</b>	cs		l	_			
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			90	^	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				90	A	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = 22 A		0.8	1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			52	78	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			70.2	105	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 20 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		27			
Reverse Recovery Rise Time	t <sub>b</sub>	t <sub>b</sub>		25		ns	

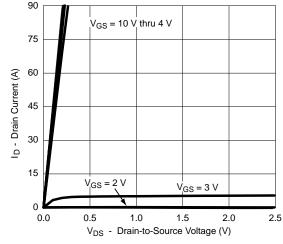
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

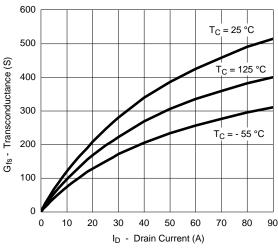
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



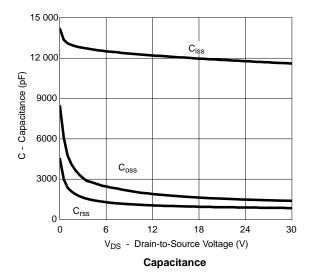
#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

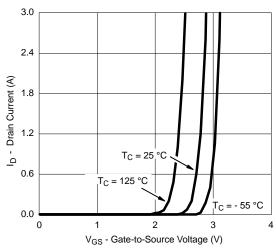


#### **Output Characteristics**

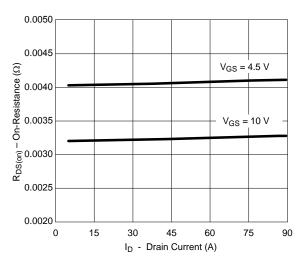


Transconductance

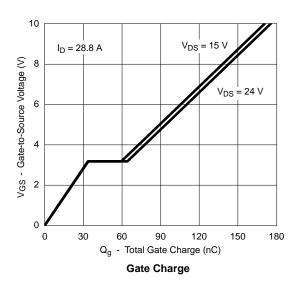




Transfer Characteristics

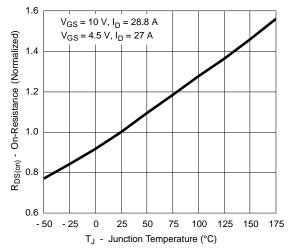


R<sub>DS(on)</sub> vs. Drain Current

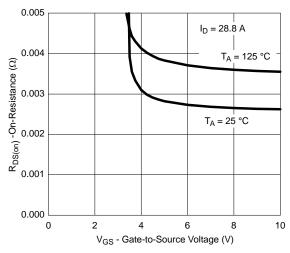




#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



On-Resistance vs. Junction Temperature



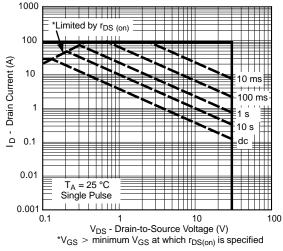
 $R_{DS(on)}$  vs.  $V_{GS}$  vs. Temperature



Forward Diode Voltage vs. Temperature



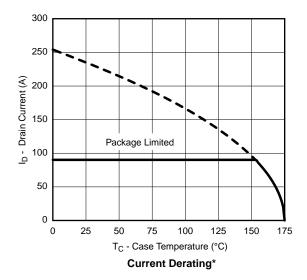
Threshold Voltage

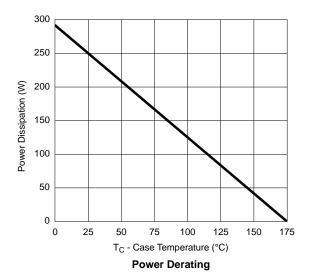


Safe Operating Area, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





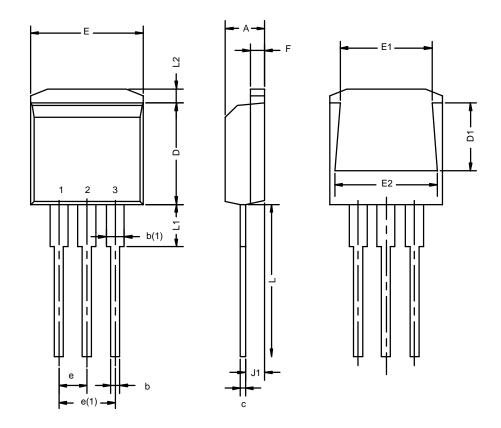
\*The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 175 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Case



#### **TO-262: 3-LEAD**



	MILLIMETERS*		INC	HES		
Dim	Min	Max	Min	Max		
Α	4.32	4.70	0.170	0.185		
b	0.64	1.00	0.025	0.039		
b(1)	1.14	1.40	0.045	0.055		
С	0.36	0.50	0.014	0.020		
D	8.64	9.65	0.340	0.380		
D1	5.59	6.10	0.220	0.240		
е	2.41	2.67	0.095	0.105		
e(1)	4.95	5.33	0.195	0.210		
E	10.03	10.41	0.395	0.410		
E1	7.87	8.64	0.310	0.340		
E2	9.02	9.53	0.355	0.375		
F	1.14	1.40	0.045	0.055		
J1	2.41	2.79	0.095	0.110		
L	13.08	14.22	0.515	0.560		
L1	-	3.81	-	0.150		
L2	1.02	1.40	0.040	0.055		
ECN: T-02234—Rev. C, 14-Oct-02 DWG: 5855						

 $<sup>^{\</sup>star}$ Use millimeters as the primary measurement



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