

# BSZ160N10NS3 G-VB Datasheet N-Channel 100-V (D-S) MOSFET

| PRODUCT SUMMARY          |                                 |                    |  |  |  |
|--------------------------|---------------------------------|--------------------|--|--|--|
| V <sub>(BR)DSS</sub> (V) | $r_{DS(on)}(\Omega)$            | I <sub>D</sub> (A) |  |  |  |
| 100                      | 0.010 at V <sub>GS</sub> = 10 V | 50                 |  |  |  |

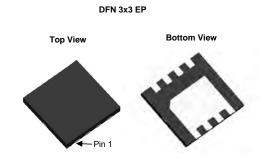
#### **FEATURES**

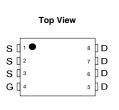
- TrenchFET® Power MOSFET
- 175 °C Junction Temperature
- Low Thermal Resistance Package
- 100 % R<sub>g</sub> Tested

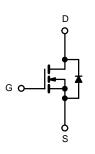


### **APPLICATIONS**

• Isolated DC/DC Converters







N-Channel MOSFET

| <b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>C</sub> = 25 °C, unless otherwise noted |                                     |                                   |                  |     |  |
|--|-------------------------------------|-----------------------------------|------------------|-----|--|
| Parameter  | Symbol                              | Limit                             | Unit             |     |  |
| Drain-Source Voltage   | V <sub>DS</sub>                     | 100                               | V                |     |  |
| Gate-Source Voltage  | V <sub>GS</sub>                     | ± 20                              | v                |     |  |
| Continuous Drain Current (T <sub>.1</sub> = 175 °C)                            | T <sub>C</sub> = 25 °C              | 1-                                | 50               |     |  |
| Continuous Diam Current (1) = 173 C)   | T <sub>C</sub> = 125 °C             | I <sub>D</sub>                    | 45               | A   |  |
| Pulsed Drain Current   |                                     | I <sub>DM</sub>                   | 60               |     |  |
| Avalanche Current  | L = 0.1 mH                          | I <sub>AS</sub>                   | 51               |     |  |
| Single Pulse Avalanche Energy <sup>b</sup>                                     | L = 0.1 IIII1                       | E <sub>AS</sub>                   | 60               | mJ  |  |
| Maximum Barran Bissin etianh   | T <sub>C</sub> = 25 °C              | D.                                | 355 <sup>c</sup> | 10/ |  |
| Maximum Power Dissipation <sup>b</sup>   | T <sub>A</sub> = 25 °C <sup>d</sup> | P <sub>D</sub>                    | 3.35             | W   |  |
| Operating Junction and Storage Temperature Range                               |                                     | T <sub>J</sub> , T <sub>stq</sub> | - 55 to 175      | °C  |  |

| THERMAL RESISTANCE RATINGS |                                 |                   |       |      |  |  |
|----------------------------|---------------------------------|-------------------|-------|------|--|--|
| Parameter                  |                                 | Symbol            | Limit | Unit |  |  |
| Junction-to-Ambient        | PCB Mount (TO-263) <sup>d</sup> | R <sub>thJA</sub> | 40    | °C/W |  |  |
| Junction-to-Case (Drain)   | se (Drain)                      |                   | 0.4   | C/VV |  |  |

#### Notes:

- a. Package limited.
- b. Duty cycle  $\leq$  1 %.
- c. See SOA curve for voltage derating.
- d. When Mounted on 1" square PCB (FR-4 material).

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| Parameter                                     | Symbol  | Symbol Test Conditions  |     | Тур.  | Max.  | Unit |  |
|---|---|---|-----|-------|-------|------|--|
| Static  |   |   |     |       |       |      |  |
| Drain-Source Breakdown Voltage                | $V_{(BR)DSS}$ $V_{DS} = 0 \text{ V}, I_D = 250 \mu\text{A}$ |   | 100 |       |       | V    |  |
| Gate-Threshold Voltage                        | V <sub>GS(th)</sub>   | $V_{DS} = V_{GS}, I_D = 250 \mu A$                                      | 2   |       | 4     | V    |  |
| Gate-Body Leakage                             | I <sub>GSS</sub>  | $V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$                       |     |       | ± 100 | nA   |  |
| Zero Gate Voltage Drain Current               |   | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V                          |     |       | 1     |      |  |
|   | I <sub>DSS</sub>  | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C |     |       | 50    | μΑ   |  |
|   |   | V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C |     |       | 250   |      |  |
| On-State Drain Current <sup>a</sup>           | I <sub>D(on)</sub>  | $V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$                         | 120 |       |       | Α    |  |
|   |   | $V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$                             |     | 0.010 |       |      |  |
| Drain-Source On-State Resistance <sup>a</sup> | r <sub>DS(on)</sub>   | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 125 °C  |     | 0.013 |       | Ω    |  |
|   |   | V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A, T <sub>J</sub> = 175 °C  |     | 0.027 |       |      |  |
| Forward Transconductance <sup>a</sup>         | 9 <sub>fs</sub>   | V <sub>DS</sub> = 15 V, I <sub>D</sub> = 30 A                           | 25  |       |       | S    |  |
| Dynamic <sup>b</sup>                          |   |   |     |       |       |      |  |
| Input Capacitance                             | C <sub>iss</sub>  |   |     | 3200  |       | pF   |  |
| Output Capacitance                            | C <sub>oss</sub>  | $V_{GS} = 0 \text{ V}, V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$        |     | 410   |       |      |  |
| Reverse Transfer Capacitance                  | C <sub>rss</sub>  |   |     | 210   |       |      |  |
| Total Gate Charge <sup>c</sup>                | Qg  |   |     | 90    | 130   |      |  |
| Gate-Source Charge <sup>c</sup>               | $Q_{gs}$  | $V_{DS} = 100 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 58 \text{ A}$     |     | 23    |       | nC   |  |
| Gate-Drain Charge <sup>c</sup>                | $Q_{gd}$  |   |     | 34    |       |      |  |
| Gate Resistance                               | $R_{g}$   | 0.5   |     | 1.3   | 3.1   | Ω    |  |
| Turn-On Delay Time <sup>c</sup>               | t <sub>d(on)</sub>  |   |     | 24    | 35    |      |  |
| Rise Time <sup>c</sup>                        | t <sub>r</sub>  | $V_{DD} = 100 \text{ V}, R_L = 1.5 \Omega$                              |     | 220   | 330   |      |  |
| Turn-Off Delay Time <sup>c</sup>              | t <sub>d(off)</sub>   | $I_D \cong 58 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 2.5$             |     | 45    | 70    | ns   |  |
| Fall Time <sup>c</sup>                        | t <sub>f</sub>  | Ω   |     | 200   | 300   |      |  |
| Source-Drain Diode Ratings and Cha            | aracteristics 7   | C <sub>C</sub> = 25 °C <sup>b</sup>                                     |     |       |       |      |  |
| Continuous Current                            | Is  |   |     |       | 58    |      |  |
| Pulsed Current                                | I <sub>SM</sub>   |   |     |       | 110   | Α    |  |
| Forward Voltage <sup>a</sup>                  | V <sub>SD</sub>   | I <sub>F</sub> = 58 A, V <sub>GS</sub> = 0 V                            |     | 1.0   | 1.5   | V    |  |
| Reverse Recovery Time                         | t <sub>rr</sub>   | . 55  |     | 130   | 200   | ns   |  |
| Peak Reverse Recovery Current                 | I <sub>RM(REC)</sub>  | I <sub>F</sub> = 30 A, di/dt = 100 A/μs                                 |     | 8     | 12    | Α    |  |
| Reverse Recovery Charge                       | Q <sub>rr</sub>   |   |     | 0.52  | 1.2   | μC   |  |

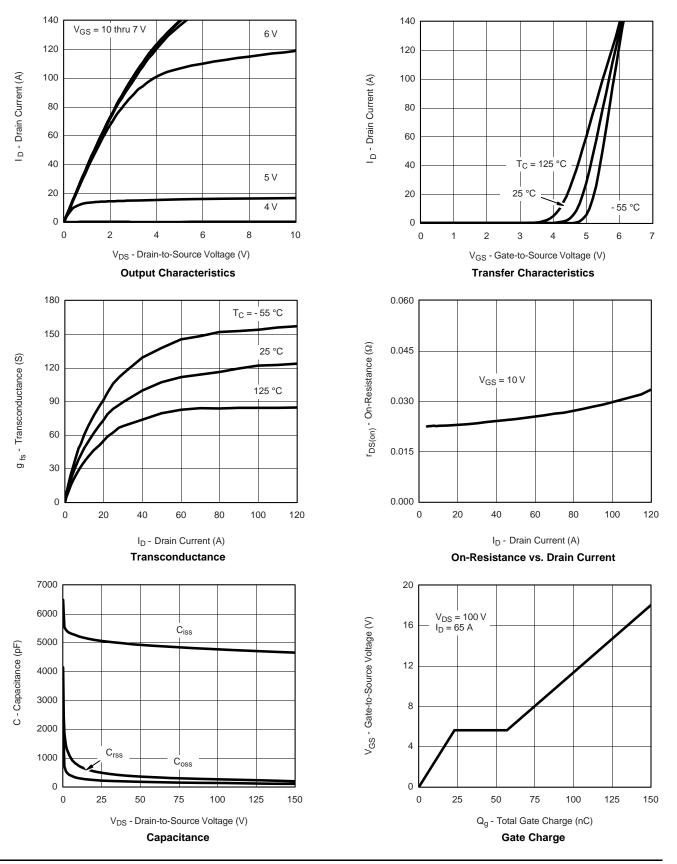
#### Notes

- a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



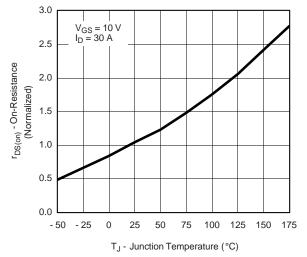
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



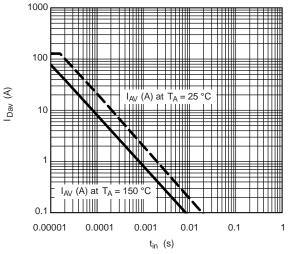
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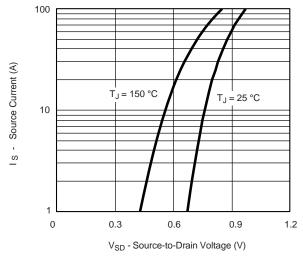
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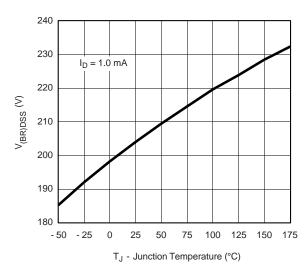
On-Resistance vs. Junction Temperature



**Avalanche Current vs. Time** 



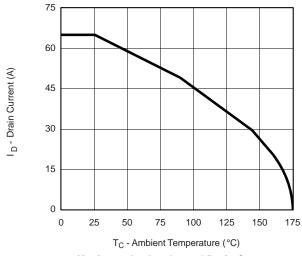
Source-Drain Diode Forward Voltage



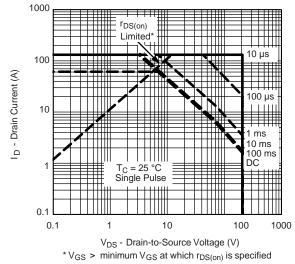
Drain Source Breakdown vs. Junction Temperature



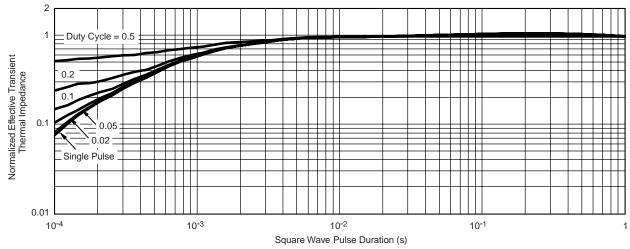
#### **THERMAL RATINGS**



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area

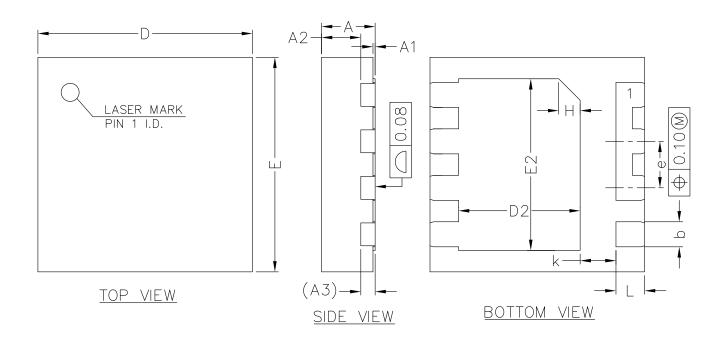


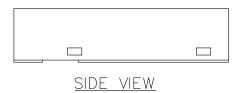
Normalized Thermal Transient Impedance, Junction-to-Case

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COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN     | NOM  | MAX  |  |
|--------|---------|------|------|--|
| А      | 0.70    | 0.75 | 0.80 |  |
| A1     | 0.00    | 0.02 | 0.05 |  |
| A2     | 0.50    | 0.55 | 0.60 |  |
| А3     | 0.20REF |      |      |  |
| Ь      | 0.30    | 0.35 | 0.40 |  |
| D      | 2.90    | 3.00 | 3.10 |  |
| Е      | 2.90    | 3.00 | 3.10 |  |
| D2     | 1.60    | 1.70 | 1.80 |  |
| E2     | 2.30    | 2.40 | 2.50 |  |
| е      | 0.55    | 0.65 | 0.75 |  |
| K      | 0.40    | 0.50 | 0.60 |  |
| L      | 0.35    | 0.40 | 0.45 |  |

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