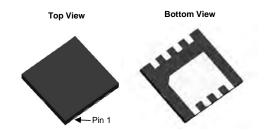


BSZ165N04NS G-VB Datasheet N-Channel 40 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A) ^f	Q _g (Typ.)		
40	0.0045 at V _{GS} = 10 V	40 ^g	9.8 nC		
40	0.0062 at V _{GS} = 4.5 V	40 ^g	9.0110		

DFN 3x3 EP



FEATURES

- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Capable of Operating with 5 V Gate Drive

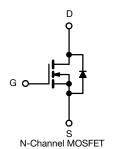


APPLICATIONS

- Synchronous Rectification
- Synchronous Buck Converters
- **ORing**
- Load Switching
- Motor Drive Switch

Top View





ABSOLUTE MAXIMUM RATIN	IGS (T _A = 25 °C	, unless oth	erwise noted)		
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V_{DS}	40	v		
Gate-Source Voltage		V_{GS}			± 20
	T _C = 25 °C	I _D	40 ^g		
Continuous Drain Current /T 150 °C	T _C = 70 °C		40 ^g		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C		19.3 ^{a, b}		
	T _A = 70 °C		15.5 ^{a, b}	A	
Pulsed Drain Current (t = 100 μs)		I _{DM}	100	A	
Continuous Source-Drain Diode Current	T _C = 25 °C	- I _S	40 ^g		
Continuous Source-Drain Diode Current	T _A = 25 °C		3.1 ^{a, b}		
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	20		
Single Pulse Avalanche Energy	L = 0.1 IIII	E _{AS}	20	mJ	
	T _C = 25 °C		52		
Maximum Dawar Dissination	T _C = 70 °C		33	w	
Maximum Power Dissipation	T _A = 25 °C	- P _D	3.7 ^{a, b}	VV	
	T _A = 70 °C		2.4 ^{a, b}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260			

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{a, e}	t ≤ 10 s	R _{thJA}	26	33	°C/W	
Maximum Junction-to-Case (Drain)	Steady State	R _{thJC}	1.9	2.4		

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s. c. The DFN 3 x 3 EP is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- d. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components. e. Maximum under steady state conditions is 81 °C/W.
- f. Based on T_C = 25 °C.g. Package limited.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			I.	"			
Drain-Source Breakdown Voltage	V_{DS}	V _{GS} = 0 V, I _D = 250 μA	40			٧	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 ·· A		56		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 6			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1.1		2.2	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zana Oaka Wallana Busin Ouwant		V _{DS} = 40 V, V _{GS} = 0 V			1	μА	
Zero Gate Voltage Drain Current	DSS	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			Α	
Durin Course On Otata Basistana a		V _{GS} = 10 V, I _D = 20 A		0.0045		Ω	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, I_D = 10 \text{ A}$		0.0062			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 20 A		65		S	
Dynamic ^b	<u>'</u>		I.	"			
Input Capacitance	C _{iss}			1330		pF	
Output Capacitance	C _{oss}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		1200			
Reverse Transfer Capacitance	C _{rss}			66			
Tabal Oata Obania	Qg	$V_{DS} = 20 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		21.3	32		
Total Gate Charge				9.8	15	nC	
Gate-Source Charge	Q_{gs}	$V_{DS} = 20 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		3.2			
Gate-Drain Charge	Q_{gd}			2.5			
Output Charge	Q _{oss}	V _{DS} = 20 V, V _{GS} = 0 V		32	48	1	
Gate Resistance	R_{g}	f = 1 MHz	0.2	0.9	1.5	Ω	
Turn-On Delay Time	t _{d(on)}			22	44		
Rise Time	t _r	$V_{DD} = 20 \text{ V, R}_{L} = 2 \Omega$		65	120	1	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		24	45		
Fall Time	t _f			9	18		
Turn-On Delay Time	t _{d(on)}			11	22	ns	
Rise Time	t _r	$V_{DD} = 20 \text{ V}, R_L = 2 \Omega$		11	22		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		22	44		
Fall Time	t _f			9	18	1	
Drain-Source Body Diode Characteristic	s		1	"			
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			40		
Pulse Diode Forward Current (t = 100 μs)	I _{SM}				100	A	
Body Diode Voltage	V _{SD}	I _S = 4 A, V _{GS} = 0 V		0.75	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			31	60	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			17	34	nC	
Reverse Recovery Fall Time	t _a	$I_F = 10 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		13		1	
Reverse Recovery Rise Time	t _b			18		ns	

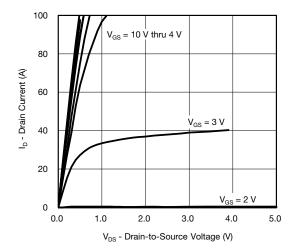
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

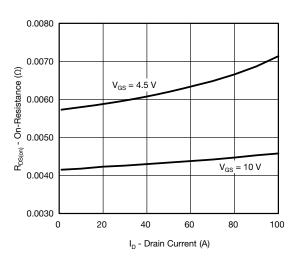
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



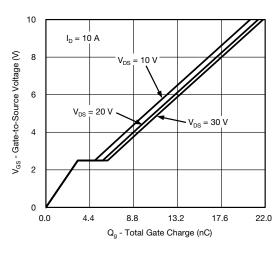
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



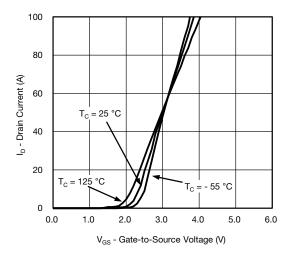
Output Characteristics



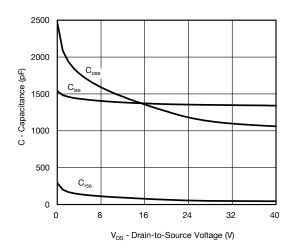
On-Resistance vs. Drain Current and Gate Voltage



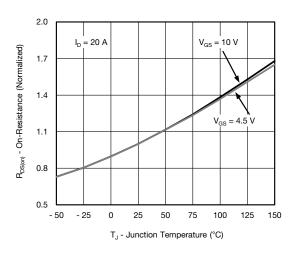
Gate Charge



Transfer Characteristics



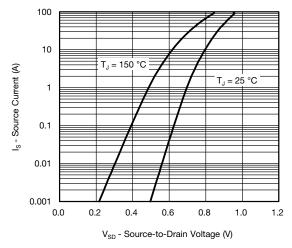
Capacitance



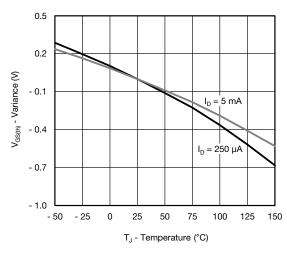
On-Resistance vs. Junction Temperature



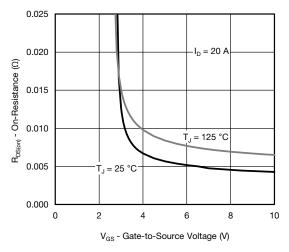
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



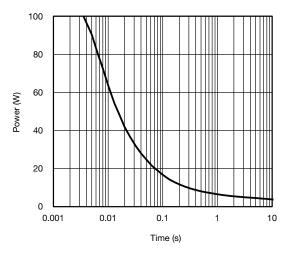




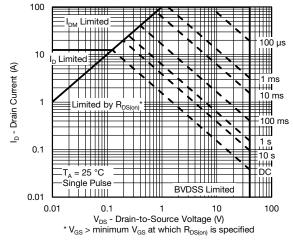
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



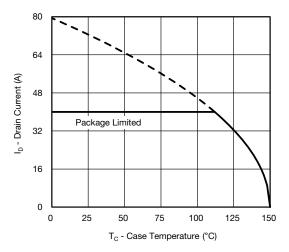
Single Pulse Power, Junction-to-Ambient



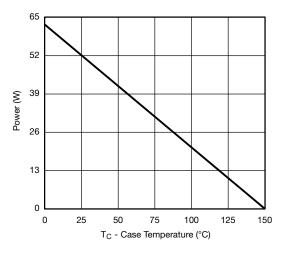
Safe Operating Area, Junction-to-Ambient



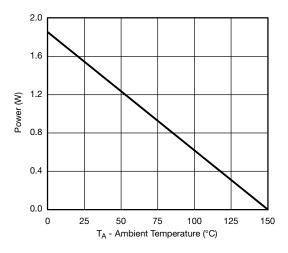
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*







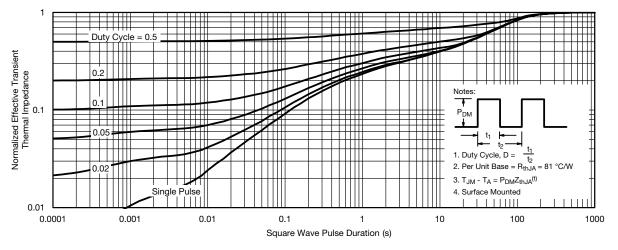
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max.)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

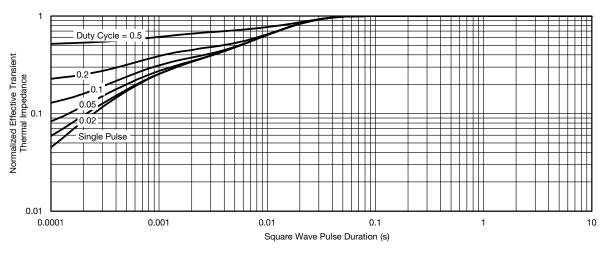
6



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

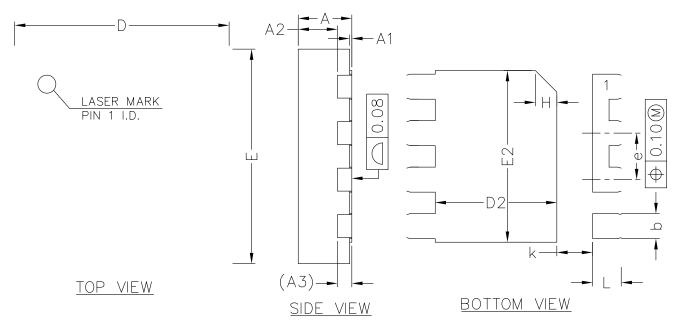


Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case







COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX		
А	0.70	0.75	0.80		
A1	0.00	0.02	0.05		
A2	0.50	0.55	0.60		
А3	0.20REF				
Ь	0.30	0.35	0.40		
D	2.90	3.00	3.10		
Ε	2.90	3.00	3.10		
D2	1.60	1.70	1.80		
E2	2.30	2.40	2.50		
е	0.55	0.65	0.75		
K	0.40	0.50	0.60		
L	0.35	0.40	0.45		



Disclaimer

All products due to improve reliability, function or design or for other reasons, product specifications and data are subject to change without notice.

Taiwan VBsemi Electronics Co., Ltd., branches, agents, employees, and all persons acting on its or their representatives (collectively, the "Taiwan VBsemi"), assumes no responsibility for any errors, inaccuracies or incomplete data contained in the table or any other any disclosure of any information related to the product.(www.VBsemi.com)

Taiwan VBsemi makes no guarantee, representation or warranty on the product for any particular purpose of any goods or continuous production. To the maximum extent permitted by applicable law on Taiwan VBsemi relinquished: (1) any application and all liability arising out of or use of any products; (2) any and all liability, including but not limited to special, consequential damages or incidental; (3) any and all implied warranties, including a particular purpose, non-infringement and merchantability guarantee.

Statement on certain types of applications are based on knowledge of the product is often used in a typical application of the general product VBsemi Taiwan demand that the Taiwan VBsemi of. Statement on whether the product is suitable for a particular application is non-binding. It is the customer's responsibility to verify specific product features in the products described in the specification is appropriate for use in a particular application. Parameter data sheets and technical specifications can be provided may vary depending on the application and performance over time. All operating parameters, including typical parameters must be made by customer's technical experts validated for each customer application. Product specifications do not expand or modify Taiwan VBsemi purchasing terms and conditions, including but not limited to warranty herein.

Unless expressly stated in writing, Taiwan VBsemi products are not intended for use in medical, life saving, or life sustaining applications or any other application. Wherein VBsemi product failure could lead to personal injury or death, use or sale of products used in Taiwan VBsemi such applications using client did not express their own risk. Contact your authorized Taiwan VBsemi people who are related to product design applications and other terms and conditions in writing.

The information provided in this document and the company's products without a license, express or implied, by estoppel or otherwise, to any intellectual property rights granted to the VBsemi act or document. Product names and trademarks referred to herein are trademarks of their respective representatives will be all.

Material Category Policy

Taiwan VBsemi Electronics Co., Ltd., hereby certify that all of the products are determined to be oHS compliant and meets the definition of restrictions under Directive of the European Parliament 2011/65 / EU, 2011 Nian. 6. 8 Ri Yue restrict the use of certain hazardous substances in electrical and electronic equipment (EEE) - modification, unless otherwise specified as inconsistent.(www.VBsemi.com)

Please note that some documents may still refer to Taiwan VBsemi RoHS Directive 2002/95 / EC. We confirm that all products identified as consistent with the Directive 2002/95 / EC European Directive 2011/65 /.

Taiwan VBsemi Electronics Co., Ltd. hereby certify that all of its products comply identified as halogen-free halogen-free standards required by the JEDEC JS709A. Please note that some Taiwanese VBsemi documents still refer to the definition of IEC 61249-2-21, and we are sure that all products conform to confirm compliance with IEC 61249-2-21 standard level JS709A.