

产品规格手册

PRODUCT SPECIFICATION





N-Channel Enhancement Mode Power MOSFET

Description

The 5N06 uses advanced trench technology and desi gn to provide excellent $R_{\text{DS}(\text{ON})}$ with low gate charge. It can be used in a wide variety of applications.

General Features

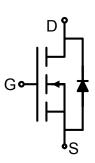
V_{DS} =60V,I_D =5A

 $R_{DS(ON)}$ < 55m Ω @ V_{GS} =10V (Typ: 46m Ω)

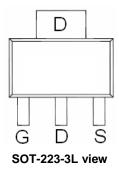
 $R_{DS(ON)}$ < 80m Ω @ V_{GS} =4.5V (Typ: 60m Ω)

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



Schematic diagram



Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	5	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	3.5	Α
Pulsed Drain Current	I _{DM}	20	Α
Maximum Power Dissipation	P _D	2	W
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	62.5	°C/W

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Electrical Characteristics (T_A=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•	•	•	
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	69	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.2	2	2.5	V
Danier Courses On Otata Basistana	-	V _{GS} =10V, I _D =4.5A		46	55	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4A		60	80	
Forward Transconductance	g FS	V _{DS} =5V,I _D =4.5A	11	-	-	S
Dynamic Characteristics (Note4)	<u> </u>		•	•	1	
Input Capacitance	C _{lss}	V _{DS} =25V,V _{GS} =0V, F=1.0MHz		450		PF
Output Capacitance	Coss			60		PF
Reverse Transfer Capacitance	C _{rss}			25		PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	4.7	_	nS
Turn-on Rise Time	t _r	V_{Ds} =30V, I_D =4.5A V_{GS} =10V, R_{GEN} =3 Ω	-	2.3	-	nS
Turn-Off Delay Time	t _{d(off)}		-	15.7	-	nS
Turn-Off Fall Time	t _f		-	1.9	-	nS
Total Gate Charge	Qg	V _{DS} =30V,I _D =4.5A, V _{GS} =10V	-	8.5	-	nC
Gate-Source Charge	Q_{gs}		-	1.6	-	nC
Gate-Drain Charge	Q_{gd}		-	2.2	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =5A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	5	Α

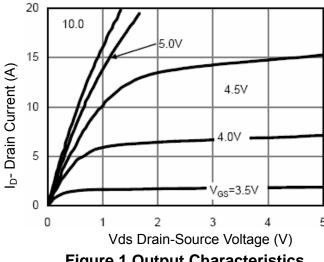
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

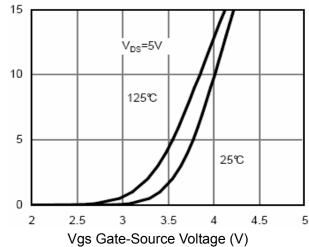
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Typical Electrical and Thermal Characteristics (Curves)







Ip- Drain Current (A)

Resistance(mΩ)

Figure 2 Transfer Characteristics

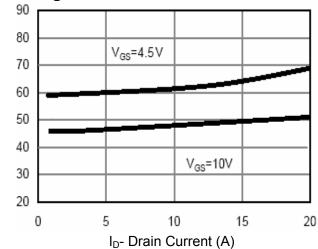


Figure 3 Rdson- Drain Current

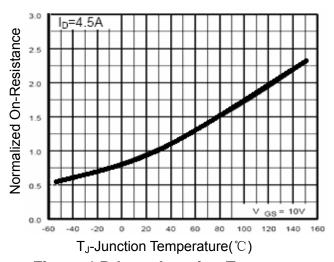


Figure 4 Rdson-Junction Temperature

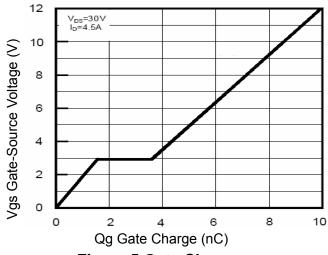


Figure 5 Gate Charge

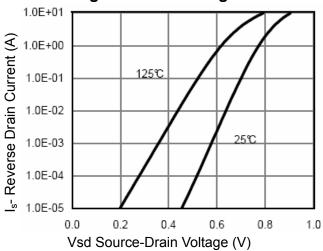


Figure 6 Source- Drain Diode Forward

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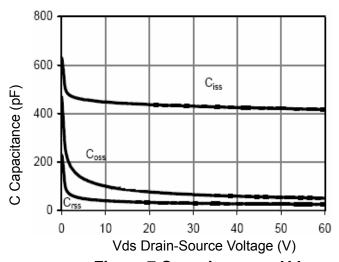


Figure 7 Capacitance vs Vds

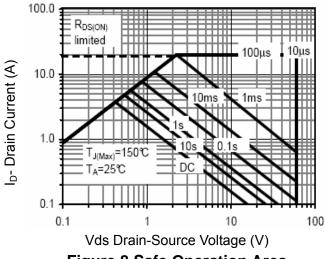


Figure 8 Safe Operation Area

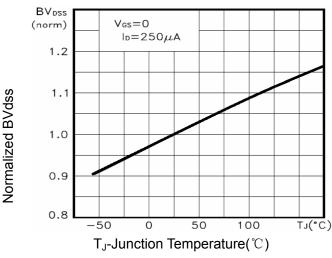


Figure 9 BV_{DSS} vs Junction Temperature

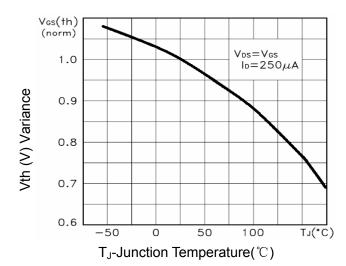


Figure 10 V_{GS(th)} vs Junction Temperature

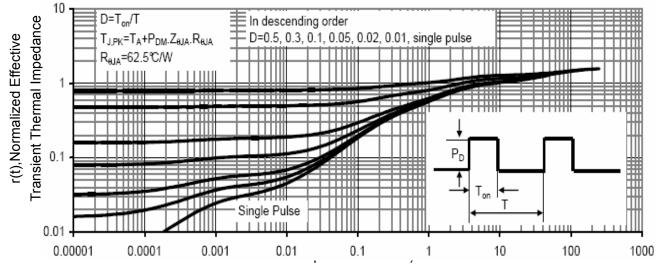


Figure 11 Normalized Maximum Transient Thermal Impedance

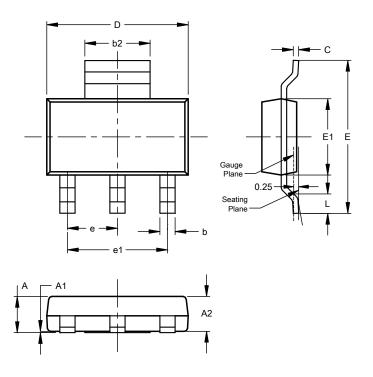
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Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)

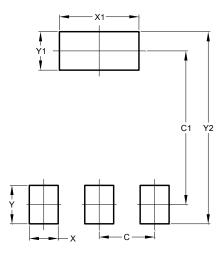


SOT223 (Type DN)				
Dim	Min	Max	Тур	
Α		1.70		
A1	0.01	0.15		
A2	1.50	1.68	1.60	
b	0.60	0.80	0.70	
b2	2.90	3.10		
С	0.20	0.32		
D	6.30	6.70		
Е	6.70	7.30		
E1	3.30	3.70		
е			2.30	
e1			4.60	
L	0.85			
All Dimensions in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
С	2.30
C1	6.40
Х	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

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