

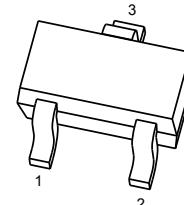
NPN 100mA 50V Digital Transistor (Bias Resistor Built-in Transistor)

### ●Features

- 1) Built-In Biasing Resistors,  
 $R_1 = 1\text{k}\Omega$ ,  $R_2 = 10\text{k}\Omega$
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) Only the on/off conditions need to be set for operation, making the circuit design easy.

### ●Outline

SOT-323

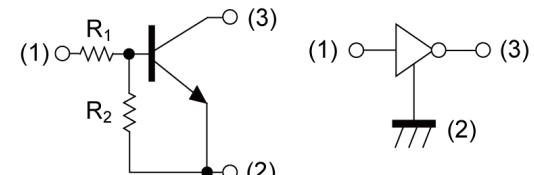


### ●Application

INVERTER, INTERFACE, DRIVER

MARKING:E21

Parameter	Value
$V_{CC}$	50V
$I_C(\text{MAX.})$	100mA
$R_1$	$1.0\text{k}\Omega$
$R_2$	$10\text{k}\Omega$



(1) IN (BASE)  
(2) GND (EMITTER)  
(3) OUT (COLLECTOR)

### ●Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Values	Unit
Supply voltage	$V_{CC}$	50	V
Input voltage	$V_{IN}$	-5 to 10	V
Output current	$I_O$	100	mA
Collector current	$I_C(\text{MAX.})^{\ast 1}$	100	mA
Power dissipation	$P_D^{\ast 2}$	200	mW
Junction temperature	$T_j$	150	°C
Range of storage temperature	$T_{stg}$	-55 to +150	°C

● Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input voltage	$V_{I(\text{off})}$	$V_{CC} = 5\text{V}, I_O = 100\mu\text{A}$	-	-	0.3	V
	$V_{I(\text{on})}$	$V_O = 0.3\text{V}, I_O = 20\text{mA}$	3.0	-	-	
Output voltage	$V_{O(\text{on})}$	$I_O = 10\text{mA}, I_I = 0.5\text{mA}$	-	100	300	mV
Input current	$I_I$	$V_I = 5\text{V}$	-	-	7.2	mA
Output current	$I_O(\text{off})$	$V_{CC} = 50\text{V}, V_I = 0\text{V}$	-	-	500	nA
DC current gain	$G_I$	$V_O = 5\text{V}, I_O = 5\text{mA}$	33	-	-	-
Input resistance	$R_1$	-	0.7	1.0	1.3	kΩ
Resistance ratio	$R_2/R_1$	-	8	10	12	-
Transition frequency	$f_T^{*1}$	$V_{CE} = 10\text{V}, I_E = -5\text{mA}, f = 100\text{MHz}$	-	250	-	MHz

\*1 Characteristics of built-in transistor

\*2 Each terminal mounted on a reference land.

● Electrical characteristic curves ( $T_a = 25^\circ\text{C}$ )

Fig.1 Input voltage vs. output current (ON characteristics)

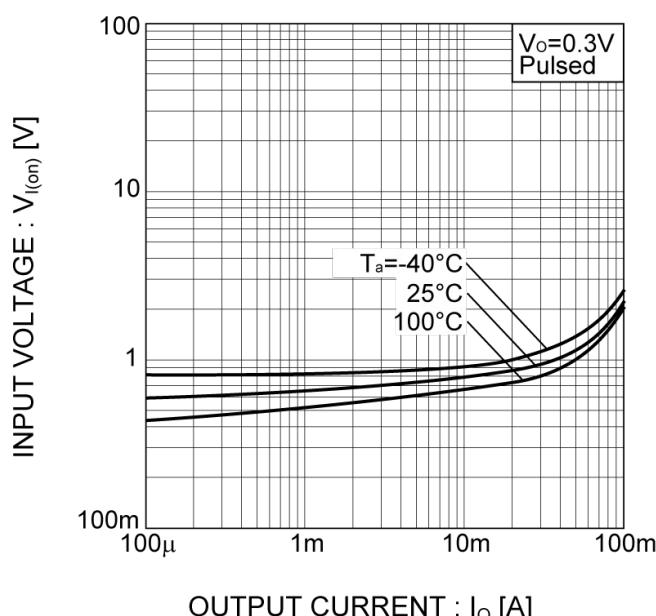


Fig.2 Output current vs. input voltage (OFF characteristics)

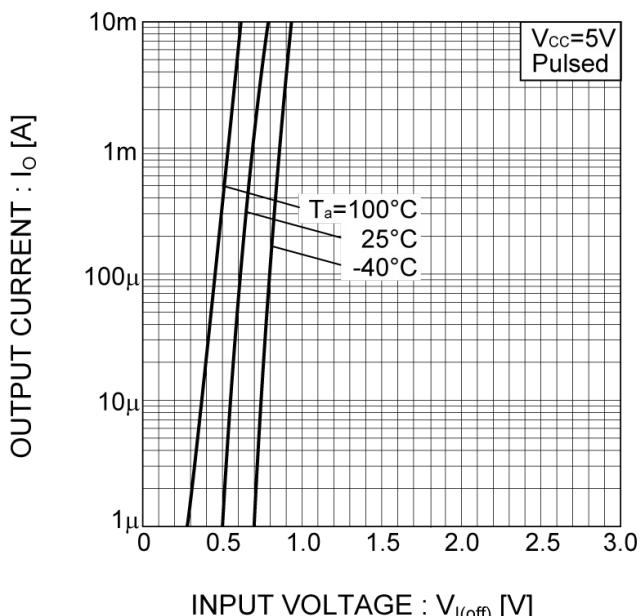


Fig.3 Output current vs. output voltage

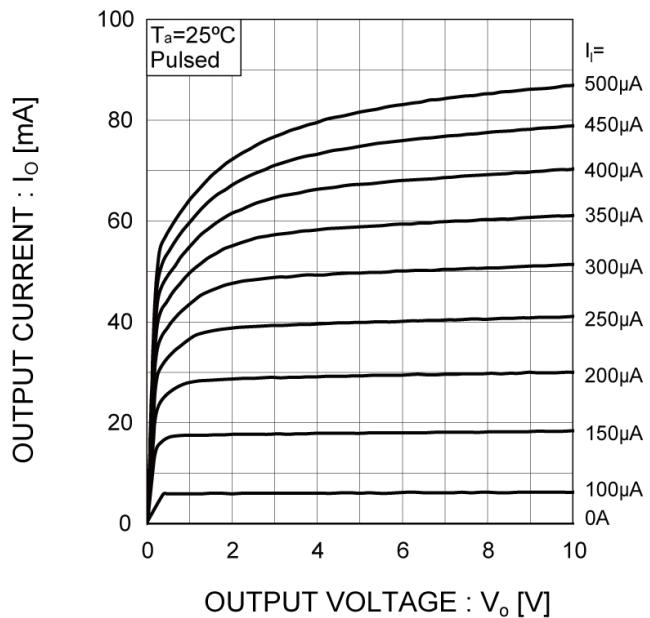
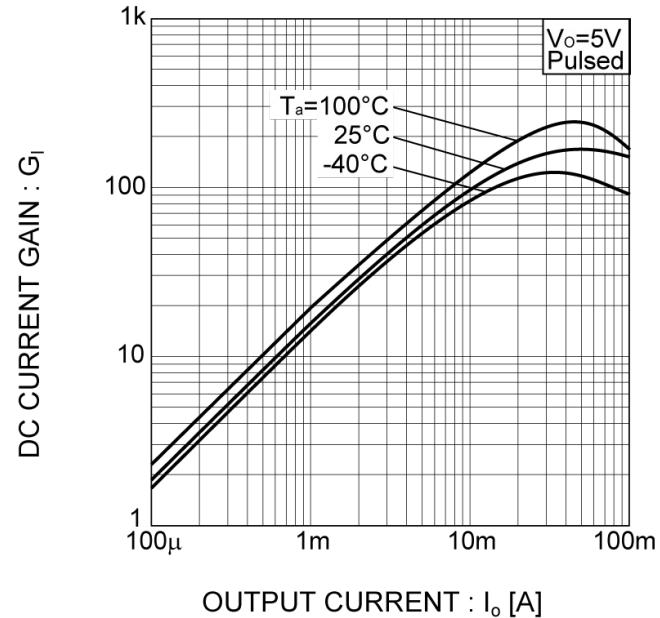
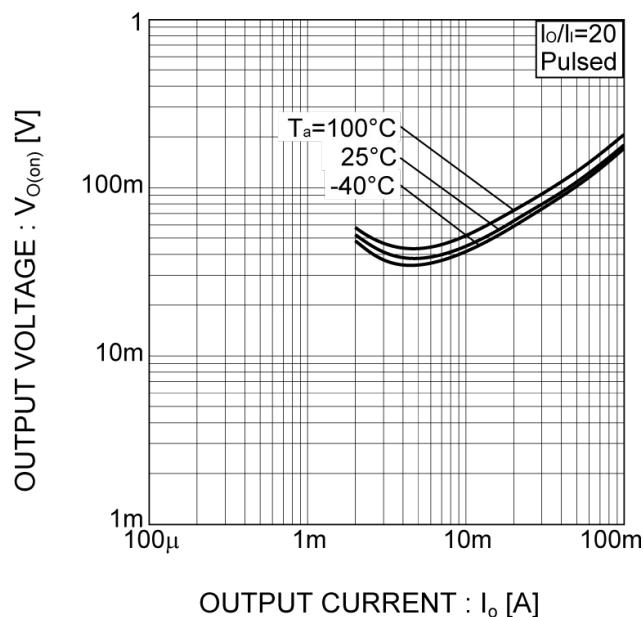


Fig.4 DC current gain vs. output current

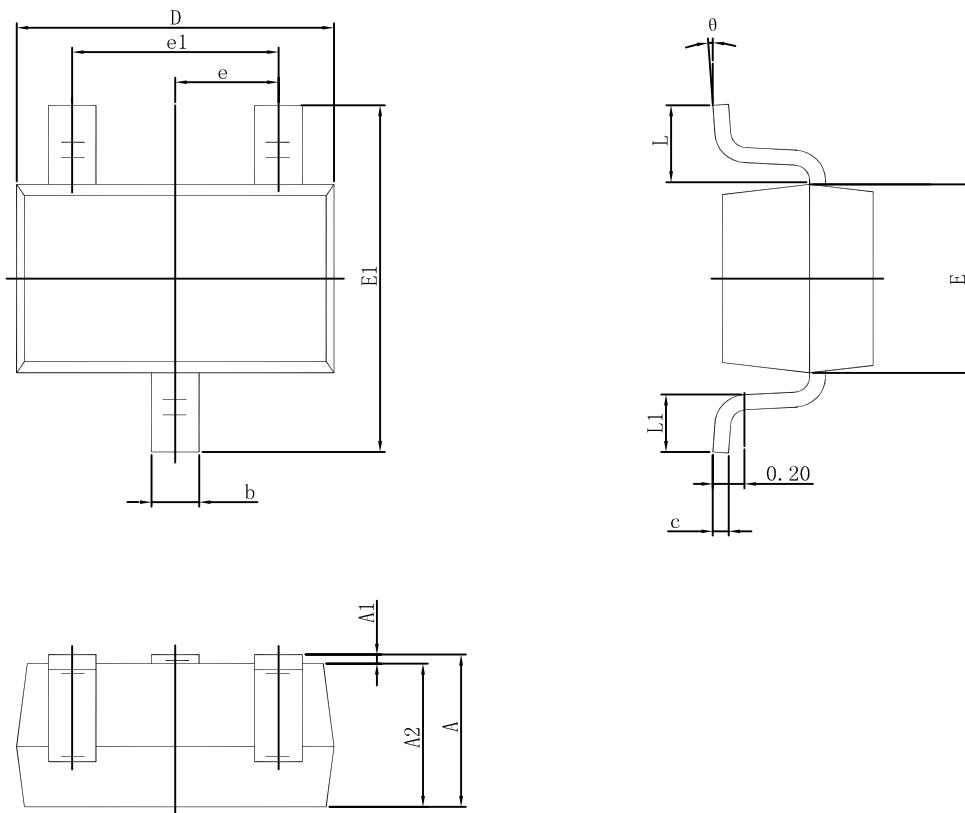


- **Electrical characteristic curves** ( $T_a = 25^\circ\text{C}$ )

Fig.5 Output voltage vs. output current



### SOT323 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.000	0.100	0.000	0.004
A2	0.900	1.000	0.035	0.039
b	0.200	0.400	0.008	0.016
c	0.080	0.150	0.003	0.006
D	2.000	2.200	0.079	0.087
E	1.150	1.350	0.045	0.053
E1	2.150	2.450	0.085	0.096
e	0.650 TYP.		0.026 TYP.	
e1	1.200	1.400	0.047	0.055
L	0.525 REF.		0.021 REF.	
L1	0.260	0.460	0.010	0.018
θ	0°	8°	0°	8°