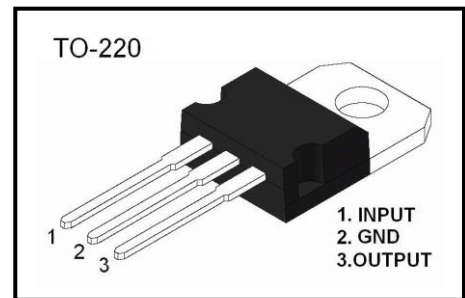


Description

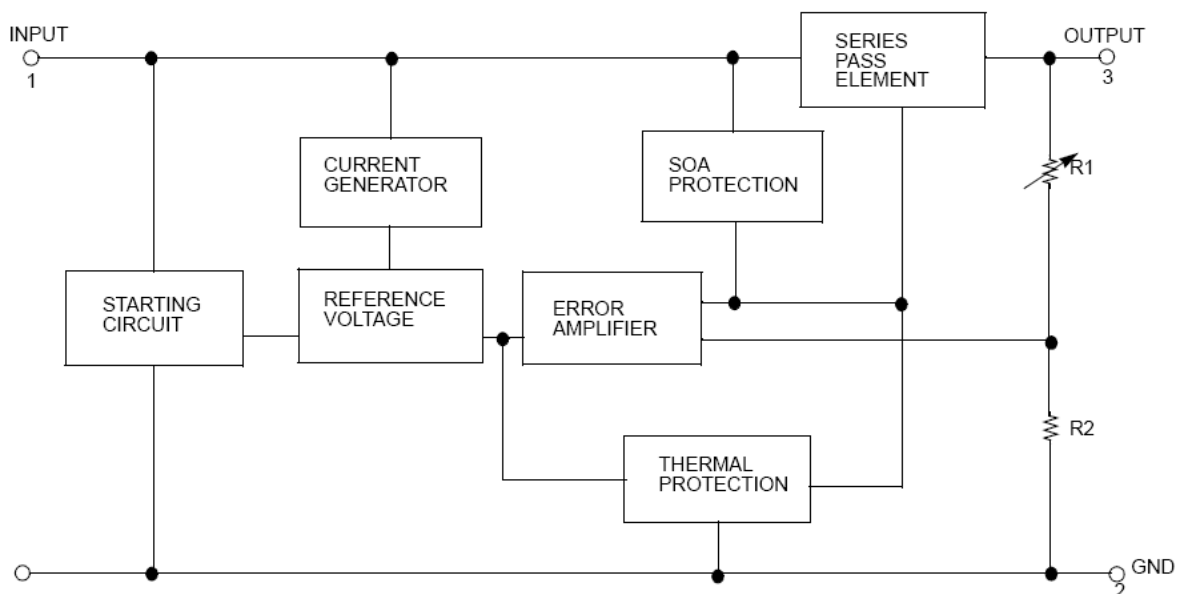
The L7812 three-terminal positive regulators are available in the TO-220 package with 12V fixed output voltages making it useful in a wide range of applications.

Features

Output Current up to 1.5A
 Output Voltages of 12V
 Thermal Overload Protection Short Circuit Protection
 Output Transistor Safe Operating area (SOA)Protection



Internal Block Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input Voltage	V_{IN}	35	V
Thermal Resistance Junction-Case	$R_{\theta JC}$	2.5	$^{\circ}\text{C/W}$
Thermal Resistance Junction-Air ($T_a = +25^{\circ}\text{C}$)	$R_{\theta JA}$	92	$^{\circ}\text{C/W}$
Operating Junction Temperature Range	T_{OPR}	0 ~ 150	$^{\circ}\text{C}$
Storage Temperature Range	T_{STG}	-55 ~ + 150	$^{\circ}\text{C}$

Electrical Characteristics(Refer to the test circuits , $I_O=500\text{mA}$, $V_I=19\text{V}$, $C_I = 0.33\mu\text{F}$, $C_O=0.1\mu\text{F}$ unless otherwise specified)

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Output Voltage		$T_j = 25^\circ\text{C}$	11.5	12	12.5	
	V_O	$I_O = 5\text{mA} \sim 750\text{mA}$ $V_I = 14.5 \sim 27\text{V}$	11.4	12	12.6	V
Line Regulation(Note)	ΔV_O	$T_j = 25^\circ\text{C}$			100	mV
		$V_I = 14.5\text{V} \sim 30\text{V}$			50	
Load Regulation(Note)	ΔV_O	$T_j = 25^\circ\text{C}$			240	mV
		$I_O = 0.25\text{A} \sim 1.0\text{A}$			120	
Quiescent Current	I_Q	$T_j = 25^\circ\text{C}$			8.0	mA
Quiescent Current Change	ΔI_Q	$I_O = 5\text{mA} \sim 1.5\text{A}$			0.5	mA
		$I_O = 14.5\text{V} \sim 30\text{V}$			0.8	
Output Voltage Drift	$\Delta V / \Delta T$	$I_O = 5\text{mA}$		-0.8		mV/ $^\circ\text{C}$
Output Noise Voltage	V_N	$f = 10\text{Hz} \sim 100\text{KHz}$, $T_A = 25^\circ\text{C}$		75		μV
Ripple Rejection	RR	$f = 120\text{Hz}$, $V_I = 15 \sim 25\text{V}$		80		dB
Dropout Voltage	V_D	$I_O = 1.0\text{A}$, $T_j = 25^\circ\text{C}$		2		V
Short Circuit Current	I_{SC}	$V_I = 35\text{V}$, $T_A = 25^\circ\text{C}$		200		mA
Peak Current	I_{PK}	$T_j = 25^\circ\text{C}$		2.2		A

Notes:

Load and line regulation are specified at constant junction temperature. Change in V_O due to heating effects must be taken into account separately. Pulse testing with low duty is used.

Test Circuits

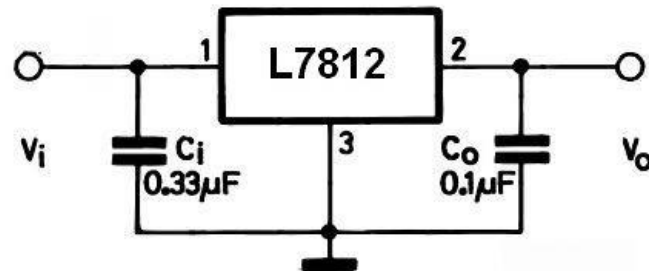


Figure 1. DC Parameters

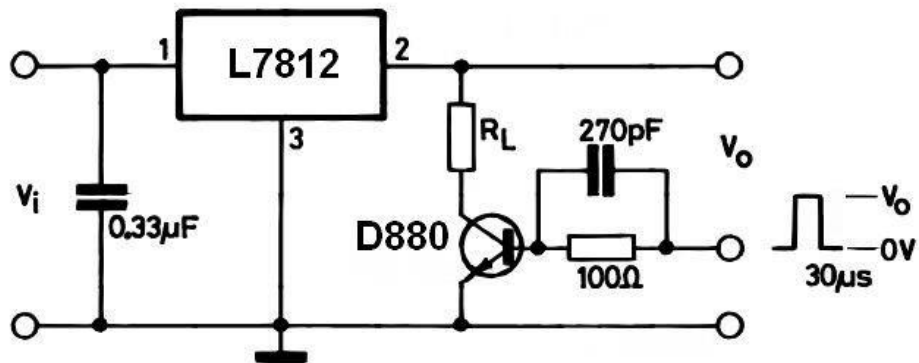


Figure 2. Load Regulation

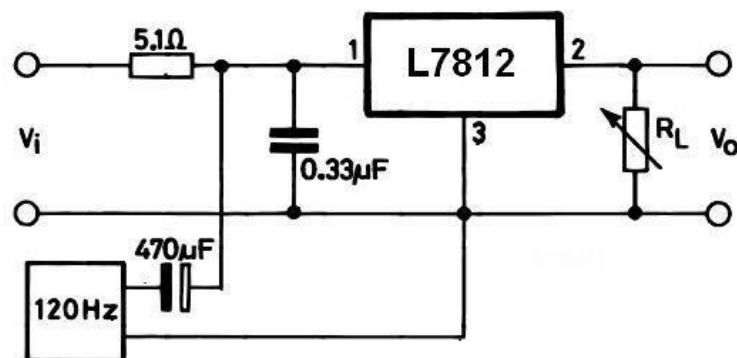


Figure 3. Ripple Rejection

Package Dimensions

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.30	4.70	0.171	0.184
A1	2.52	2.82	0.099	0.111
b	0.71	0.91	0.028	0.036
b1	1.17	1.37	0.046	0.054
c	0.40	0.55	0.012	0.020
c1	1.17	1.37	0.046	0.054
D	9.90	10.20	0.390	0.402
E	8.90	9.10	0.335	0.350
E1	12.50	13.00	0.472	0.492
e	2.44	2.64	0.096	0.104
e1	4.88	5.28	0.192	0.208
F	2.60	2.80	0.102	0.110
L	12.70	13.70	0.520	0.543
L1	2.90	3.30	0.150	0.165
Φ	3.75	3.95	0.142	0.156