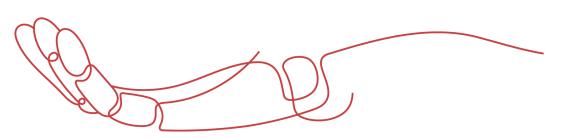




PRODUCT DATA SHEET



To learn more about JGSEMI, please visit our website at







Datasheet

urces Samples

Please note: Please check the JINGAO Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.jg-semi.cn. Please email any questions regarding the system integration to JINGAO_questions@jgsemi.com.



General Description

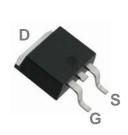
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

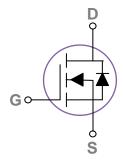
BVDSS	RDSON	ID
100V	75m Ω	15A

Features

- $100V,15A, RDS(ON) = 75m\Omega@VGS = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

TO252 Pin Configuration





Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings Tc=25℃ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V _{GS}	Gate-Source Voltage	±20	V
ı	Drain Current – Continuous (T _C =25°C)	15	А
ID	Drain Current – Continuous (T _C =100°C)	9.5	А
I _{DM}	Drain Current – Pulsed ¹	60	А
D	Power Dissipation (T _C =25°C)	50	W
P_{D}	Power Dissipation – Derate above 25°C	0.4	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
TJ	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient		62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case		2.5	°C/W



Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	100			V
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.05		V/°C
I _{DSS}	Danier Course I and a constant	V _{DS} =100V , V _{GS} =0V , T _J =25°C			1	uA
	Drain-Source Leakage Current	V _{DS} =80V , V _{GS} =0V , T _J =125°C			10	uA
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$			±100	nA

On Characteristics

R _{DS(ON)} Static Drain-Source On-Resistar	Statio Drain Source On Begintance	V _{GS} =10V , I _D =5A		75	95	mΩ
	Static Dialii-Source Off-Resistance	V _{GS} =4.5V , I _D =3A		85	110	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V -V I -250A	1.0	1.8	2.5	V
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$-V_{GS}=V_{DS}$, $I_D=250uA$		-5		mV/°C
gfs	Forward Transconductance	V_{DS} =10V , I_{D} =3A		8.7		S

Dynamic and switching Characteristics

Q_{g}	Total Gate Charge ^{2, 3}		 9.3	13	
Q_{gs}	Gate-Source Charge ^{2, 3}	V_{DS} =48V , V_{GS} =10V , I_{D} =5A	 2.1	4.2	nC
Q_{gd}	Gate-Drain Charge ^{2, 3}		 1.8	4	
T _{d(on)}	Turn-On Delay Time ^{2, 3}		 2.9	6	
Tr	Rise Time ^{2, 3}	V_{DD} =30V , V_{GS} =10V , R_{G} =3.3 Ω	 9.5	18	20
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}	I _D =1A	 18.4	35	ns
T_f	Fall Time ^{2, 3}		 5.3	10	
C _{iss}	Input Capacitance		 1480	2150	
C _{oss}	Output Capacitance	V_{DS} =50V , V_{GS} =0V , F=1MHz	 480	700	pF
C _{rss}	Reverse Transfer Capacitance		 35	55	
Rg	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	 1.3	2.6	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	-V _G =V _D =0V , Force Current			15	Α
I _{SM}	Pulsed Source Current	V _G -V _D -UV , Force Current			60	Α
V_{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =1A , T _J =25°C			1	V
t _{rr}	Reverse Recovery Time ²	V _G s=30V,I _S =1A , dI/dt=100A/µs				ns
Q _{rr}	Reverse Recovery Charge ²	T _J =25°C				nC

Note:

- 1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
- 2. The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%.
- 3. Essentially independent of operating temperature.

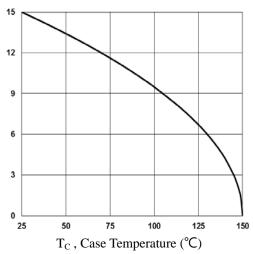


Fig.1 Continuous Drain Current vs. TC

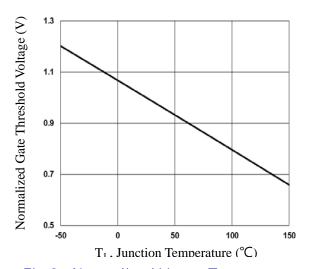


Fig.3 Normalized V_{th} vs. T_J

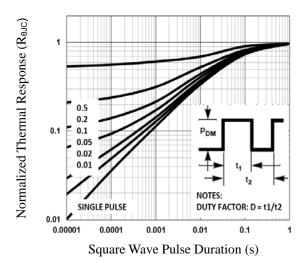


Fig.5 Normalized Transient Impedance

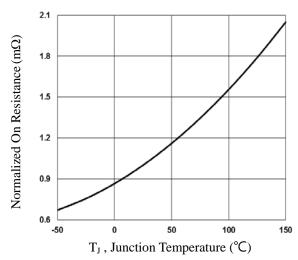


Fig.2 Normalized RDSON vs. TJ

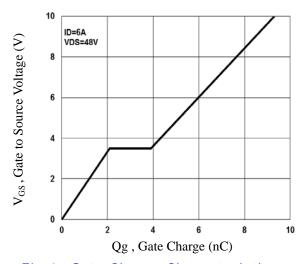


Fig.4 Gate Charge Characteristics

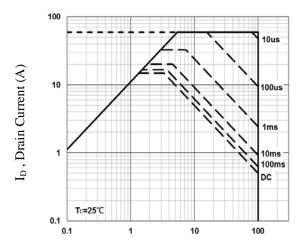
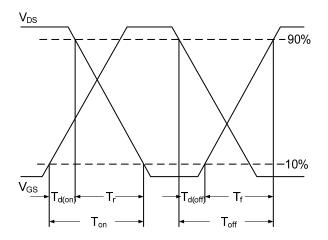
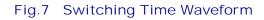


Fig.6 Maximum Safe Operation Area





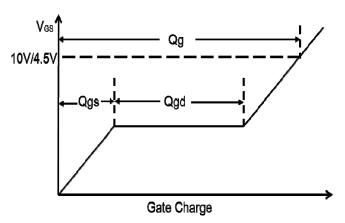
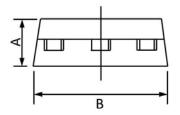
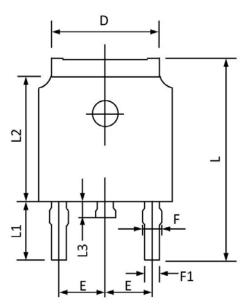


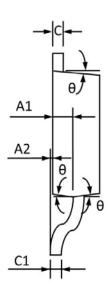
Fig.8 Gate Charge Waveform



TO252 PACKAGE INFORMATION







Cymbol	Dimensions 1	In Millimeters	Dimension	s In Inches	
Symbol	Min	Max	Min	Max	
A	2.20	2.40	0.087	0.094	
A1	0.91	1.11	0.036	0.044	
A2	0.00	0.15	0.000	0.006	
В	6.50	6.70	0.256	0.264	
C	0.46	0.580	0.018	0.230	
C 1	0.46	0.580	0.018	0.030	
D	5.10	5.46	0.201	0.215	
E	2.186	2.386	0.086	0.094	
F	0.74	0.94	0.029	0.037	
F1	0.660	0.860	0.026	0.034	
L	9.80	10.40	0.386	0.409	
L1	2.9	REF	0.114REF		
L2	6.00	6.20	0.236	0.244	
L3	0.60	1.00	0.024	0.039	
θ	3°	9°	3°	9°	



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