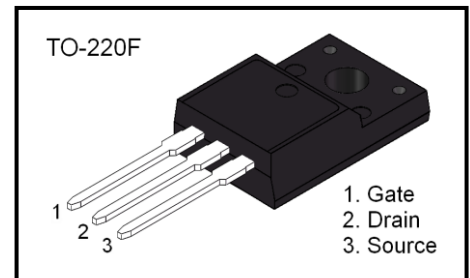
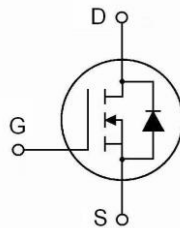


General Description

The ZH12N60 is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and have a high rugged avalanche characteristic. This power MOSFET is usually used in high speed switching applications including power supplies, PWM motor controls, high efficient DC to DC converters and bridge circuits

Features

- V_{DS} 650V
- I_D 12A
- $R_{DS(ON)}$ ($V_{GS} = 10V$) $<0.75\Omega$
- Fast Switching Capability
- Avalanche Energy Specified
- Improved dv/dt Capability, High Ruggedness



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$)

Parameter		Symbol	Ratings	Units
Gate-Drain Voltage		V_{DSS}	600	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	I_D	12	A
	Pulsed (Note 2)	I_{DM}	48	
Power Dissipation		P_D	51	W
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	400	mJ
	Repetitive (Note 2)	E_{AR}	15.6	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.5	V/ns
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	$-55 \sim 155$	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature

3. $L = 14.2\text{mH}$, $I_{AS} = 12\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 12\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

Thermal Characteristic

Parameter	Symbol	Value	Units
Maximum Thermal Resistance, Junction-case	$R_{\theta JC}$	2.43	$^\circ\text{C/W}$
Maximum Thermal Resistance, Junction-Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C/W}$

Electrical Characteristics ($T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter		Symbol	Test Conditions	Min.	Typ	Max.	Units
Off Characteristics							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =-250μA	600			V
Breakdown Voltage Temperature Coefficient		ΔBV _{DSS} /ΔT _J	I _D = 250μA, Referenced to 25 °C		0.7		V/°C
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} = 600V, V _{GS} = 0V			1	μA
Gate Leakage Current	Forward	I _{GSS}	V _{DS} =0V, V _{GS} =30V			100	nA
	Reverse		V _{DS} =0V, V _{GS} =-30V			-100	nA
On Characteristics							
Gate Threshold Voltage		V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	2.0		4.0	V
Drain-source On-Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D = 6A		0.60	0.75	Ω
Dynamic Characteristics							
Input Capacitance		C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz		1600	1900	pF
Output Capacitance		C _{oss}			175	210	
Reverse Transfer Capacitance		C _{rss}			10	22	
Switching Characteristics)							
Turn-On Delay Time		t _{d(on)}	V _{DS} = 300V, I _D = 12A R _G = 25Ω (Note1, 2)		100	110	ns
Turn-on Rise Time		t _r			125	138	
Turn-Off elay Time		t _{d(off)}			180	230	
Turn-Off Fall		t _f			104	140	
Total Gate Charge		Q _g	V _{DS} =480V, I _D = 12A V _{GS} = 10V (Note1, 2)		39	54	nC
Gate-Source Charge		Q _{gs}			10		
Gate-Drain Charge		Q _{gd}			9		
Drain-Source Diode Characteristics							
Diode Forward Voltage		V _{SD}	V _{GS} =0V, I _S =12A			1.4	V
Diode Forward Current		I _S	-			12	A
Pulsed Diode Forward Current		I _{SM}				48	A
Reverse Recovery Time		t _{rr}	I _S = 12A, V _{GS} = 0V		420		ns
Reverse Recovery Charge		Q _{rr}	dI _F /dt =100A/μs (Note 1)		4.2		nC

Notes: 1. Pulse Test : Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

Test Circuits and Waveforms

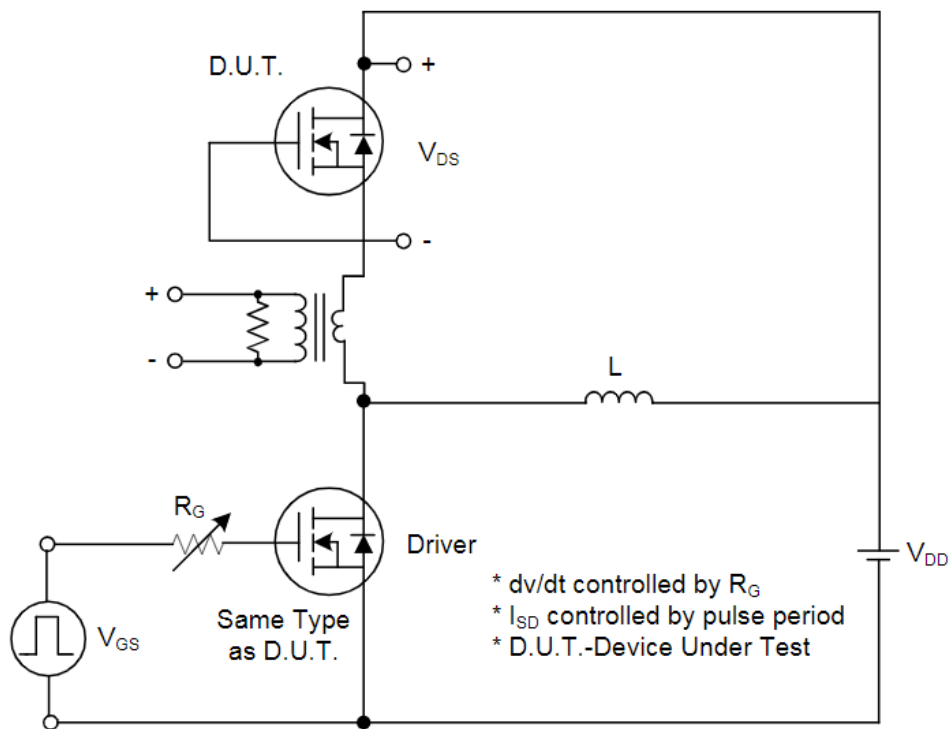


Figure 1. Peak Diode Recovery dv/dt Test Circuit

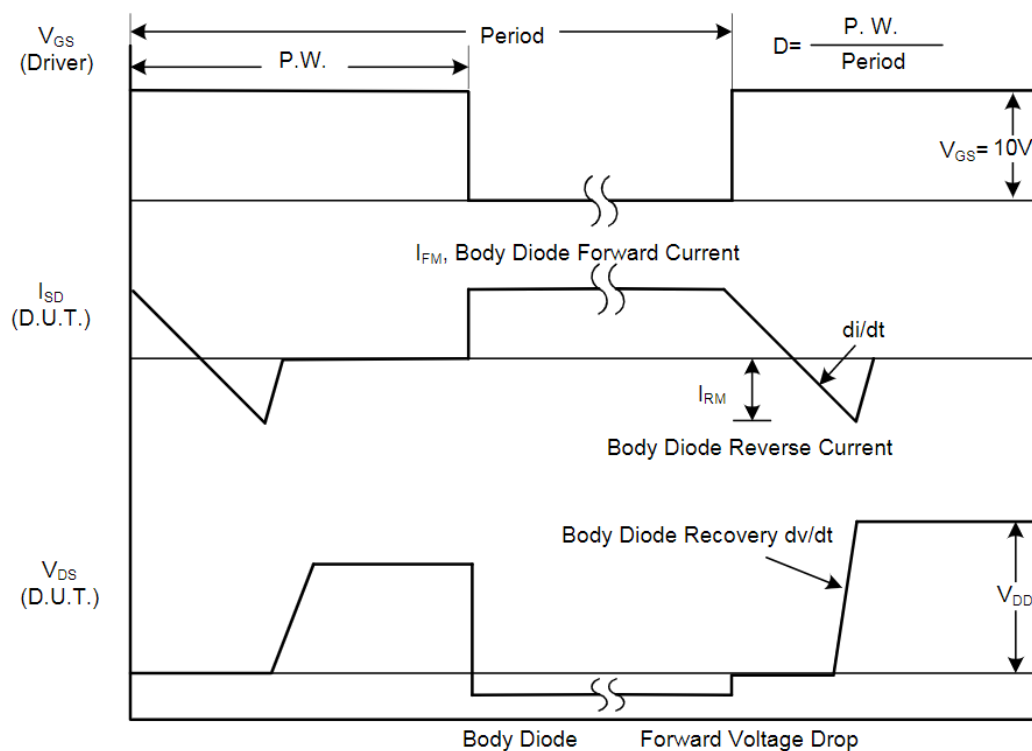


Figure 2. Peak Diode Recovery dv/dt Waveforms

Typical Characteristics

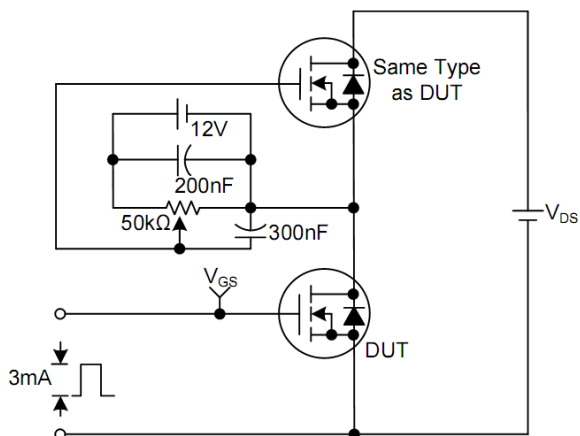


Figure 3. Gate Charge Test Circuit

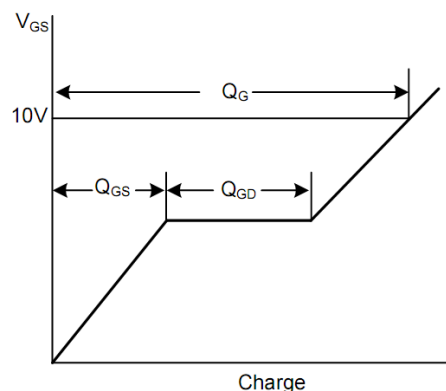


Figure 4. Gate Charge Waveforms

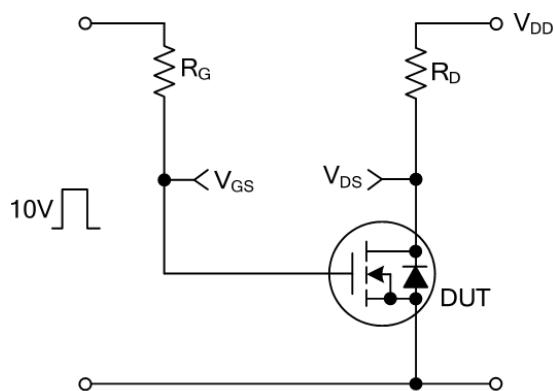


Figure 5. Resistive Switching Circuit

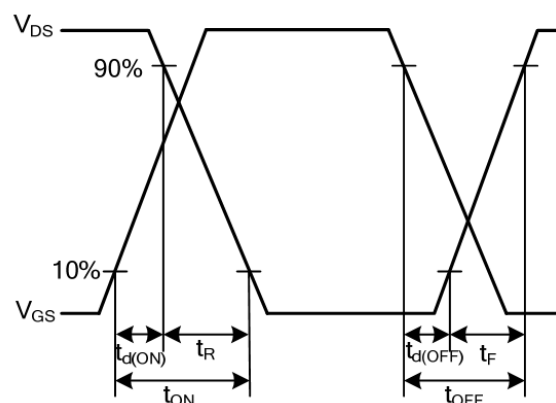


Figure 7. Resistive Switching Waveforms

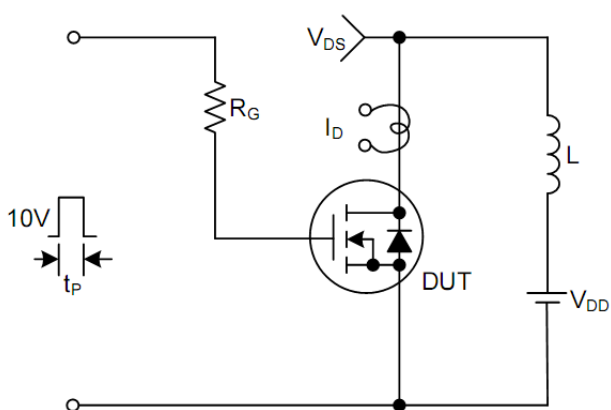


Figure 7. Unclamped Inductive Switching Test Circuit

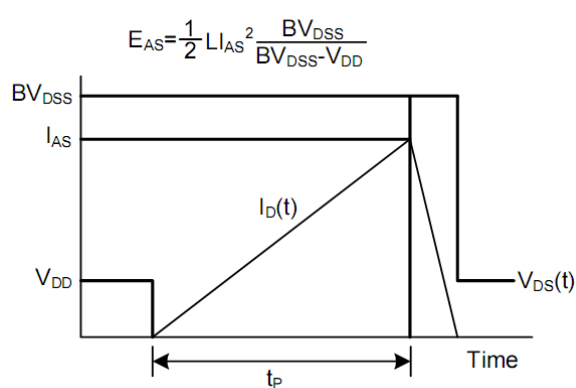
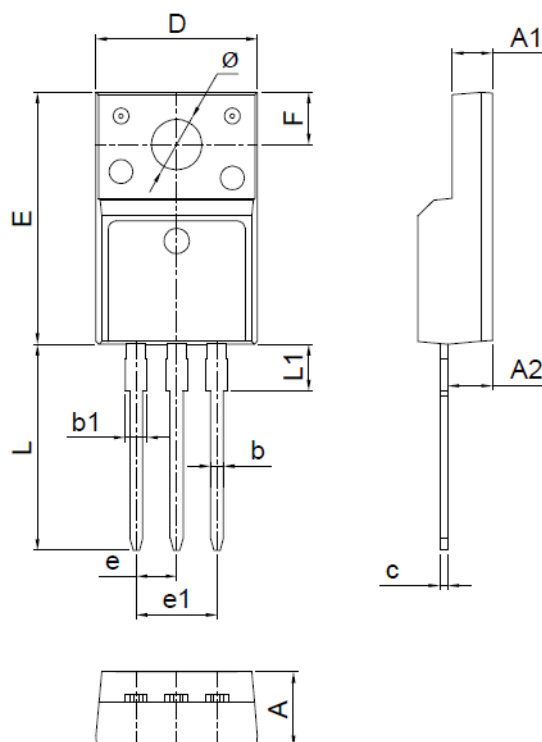


Figure 8. Unclamped Inductive Switching Waveforms

Package Dimensions



Symbol	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	4.50	4.90	0.177	0.193
A1	2.34	2.74	0.092	0.108
A2	2.66	2.86	0.105	0.113
b	0.75	0.85	0.030	0.033
b1	1.24	1.44	0.049	0.057
c	0.40	0.60	0.016	0.024
D	10.00	10.32	0.394	0.406
E	15.75	16.05	0.620	0.632
e	2.44	2.64	0.096	0.104
e1	4.88	5.28	0.192	0.208
F	3.10	3.5	0.122	0.138
L	12.90	13.50	0.508	0.531
L1	2.90	3.30	0.114	0.130
Φ	3.10	3.30	0.122	0.130